

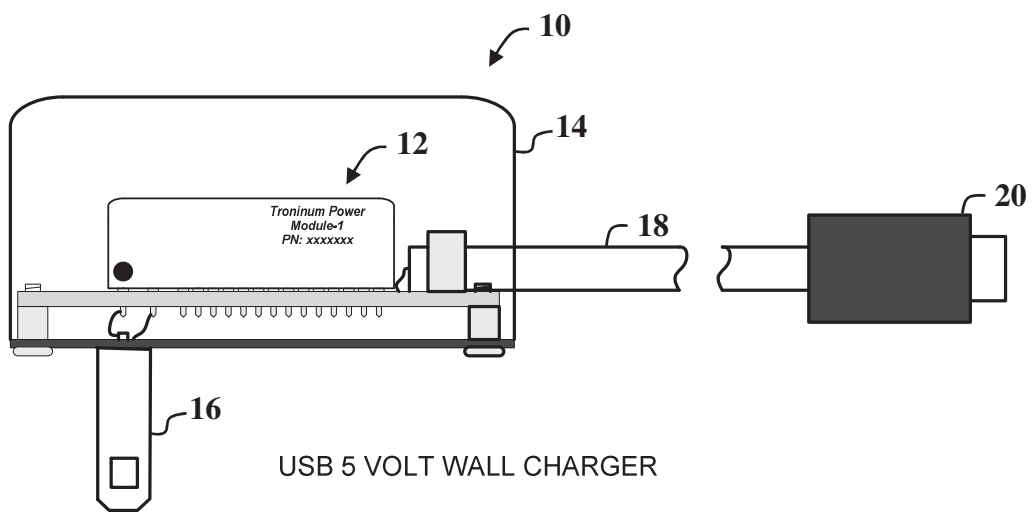


Figure 1

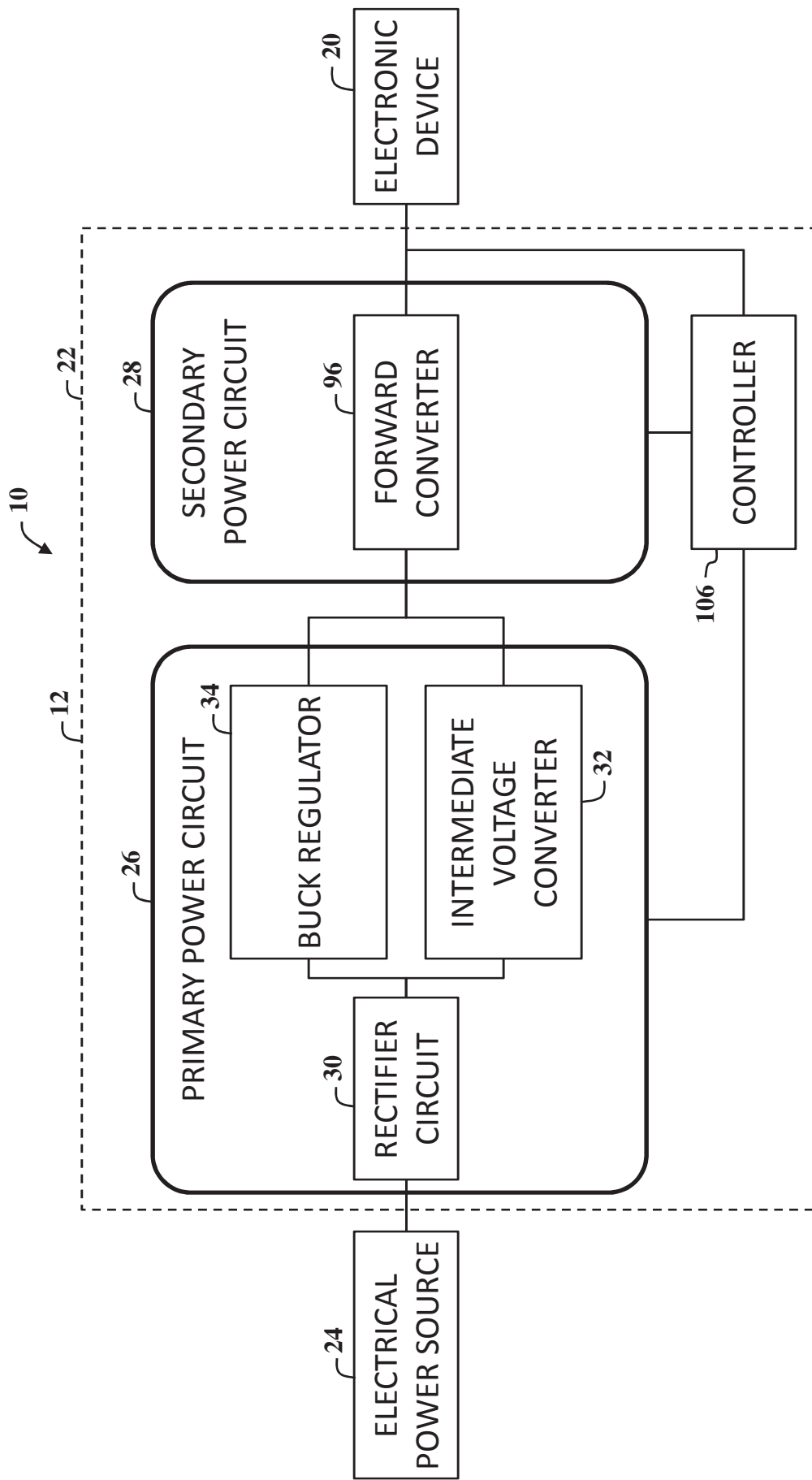


Figure 2

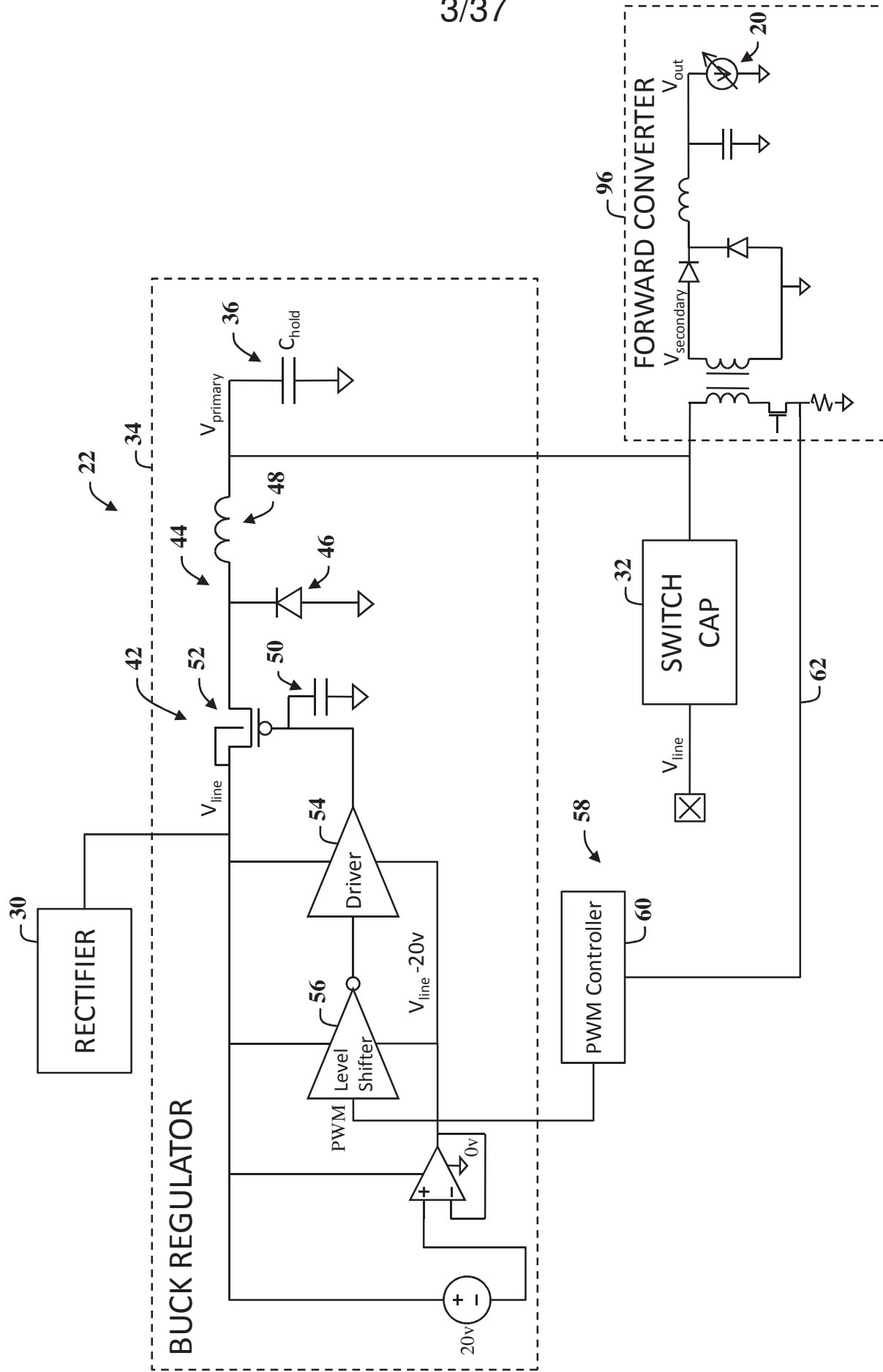


Figure 3

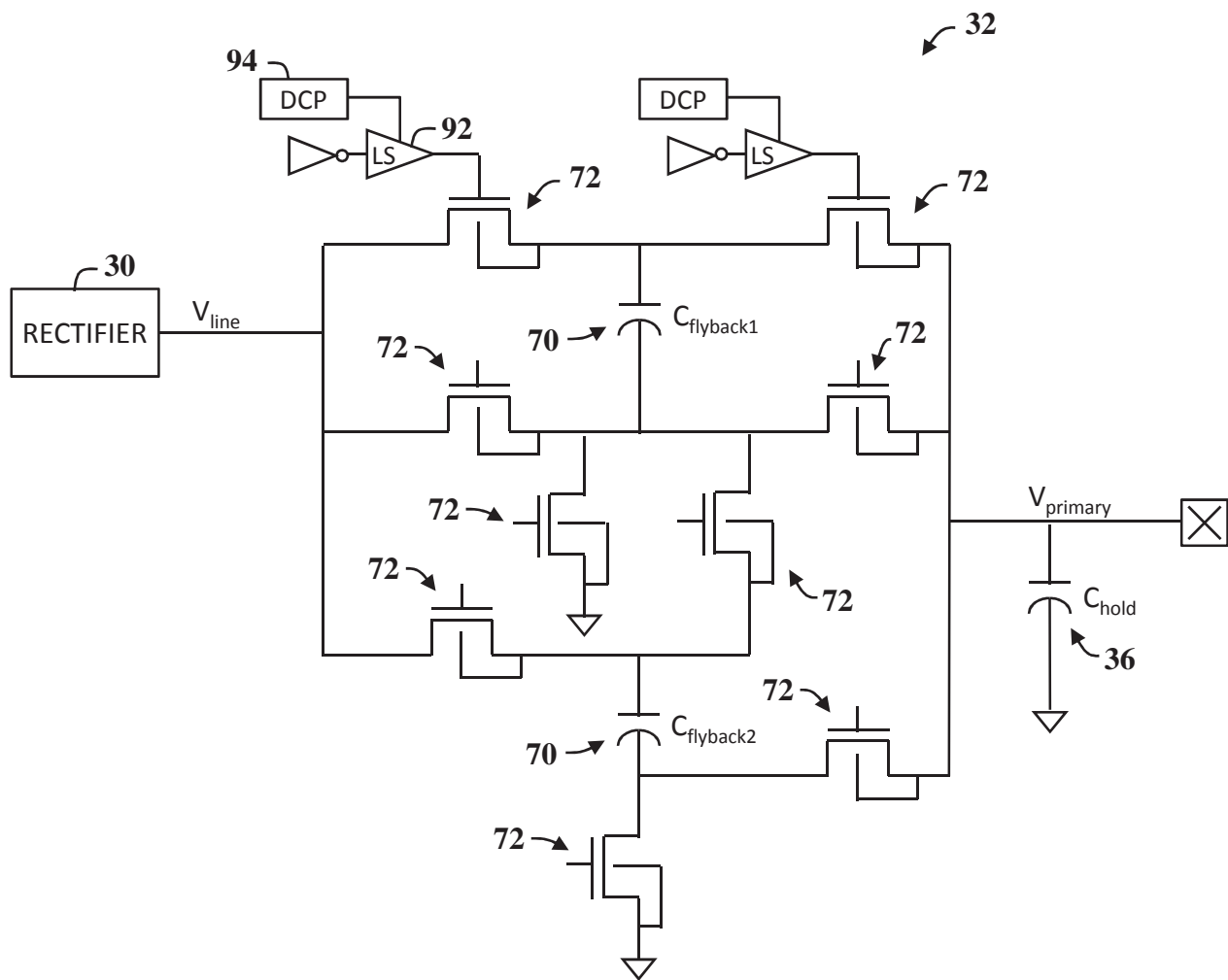


Figure 4

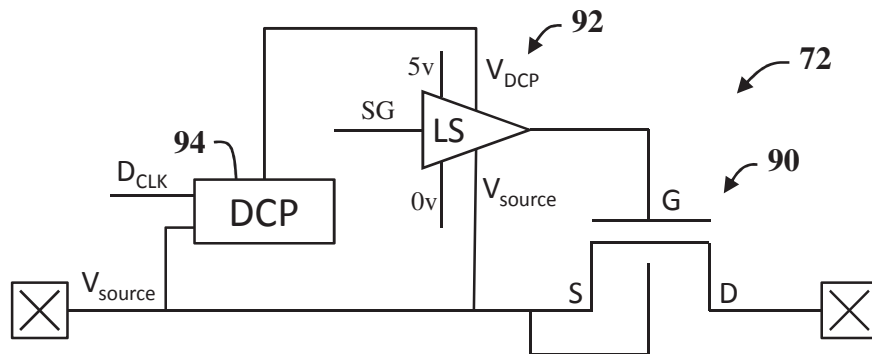


Figure 5

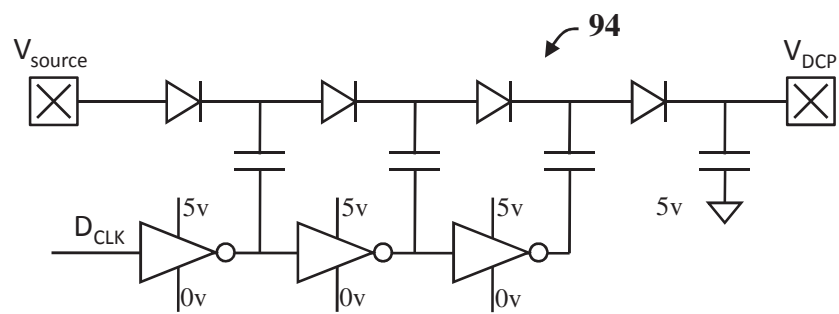


Figure 6

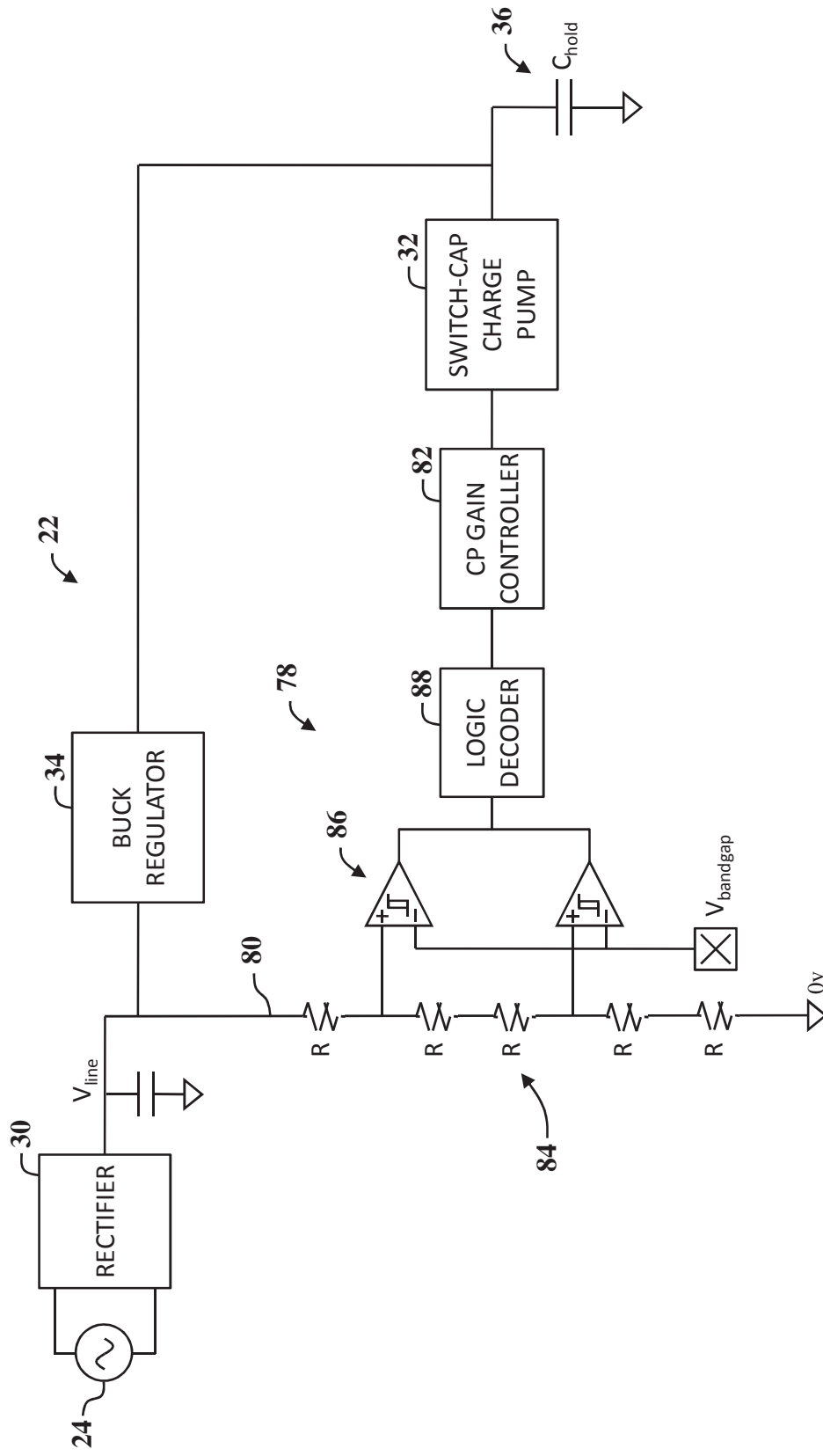


Figure 7

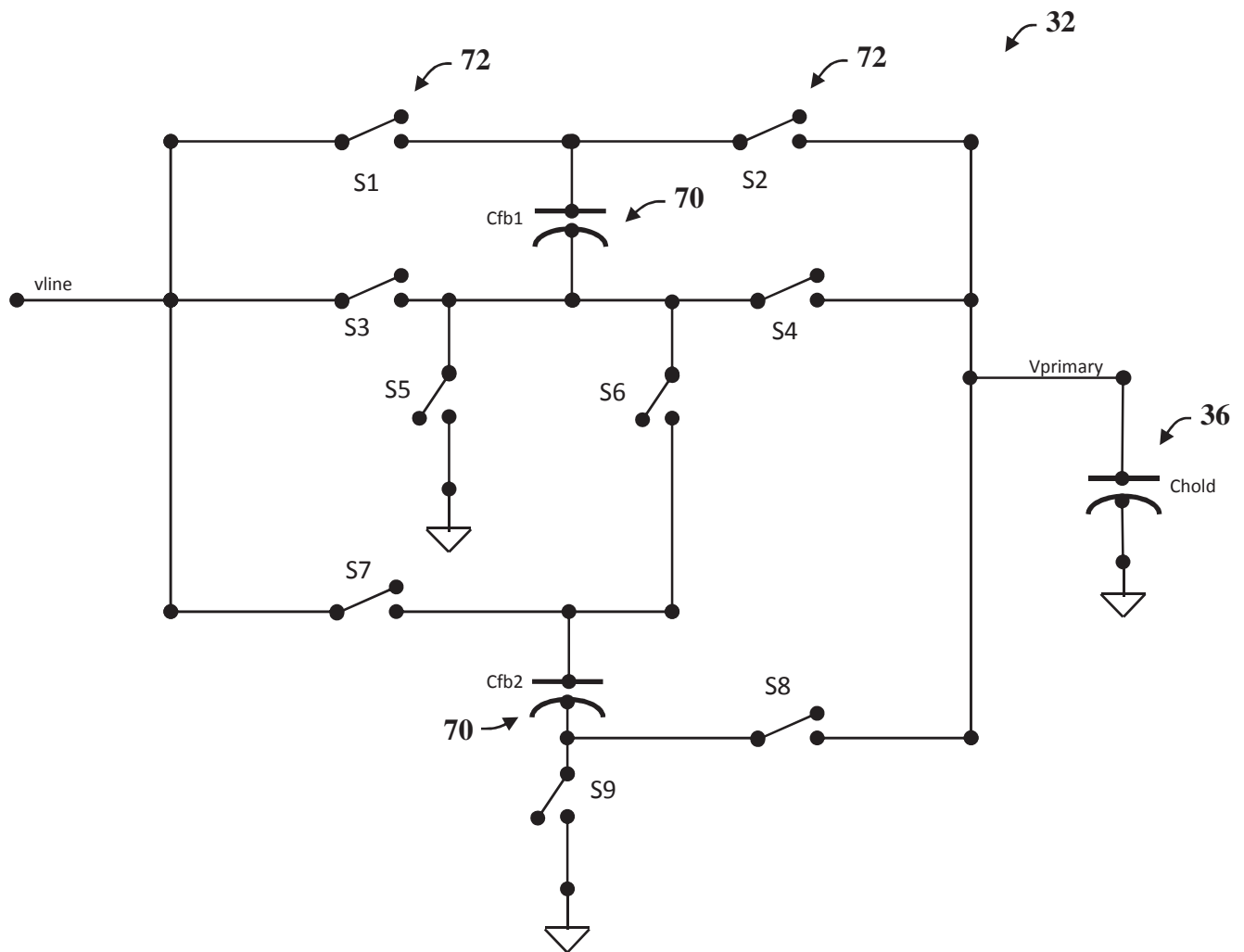
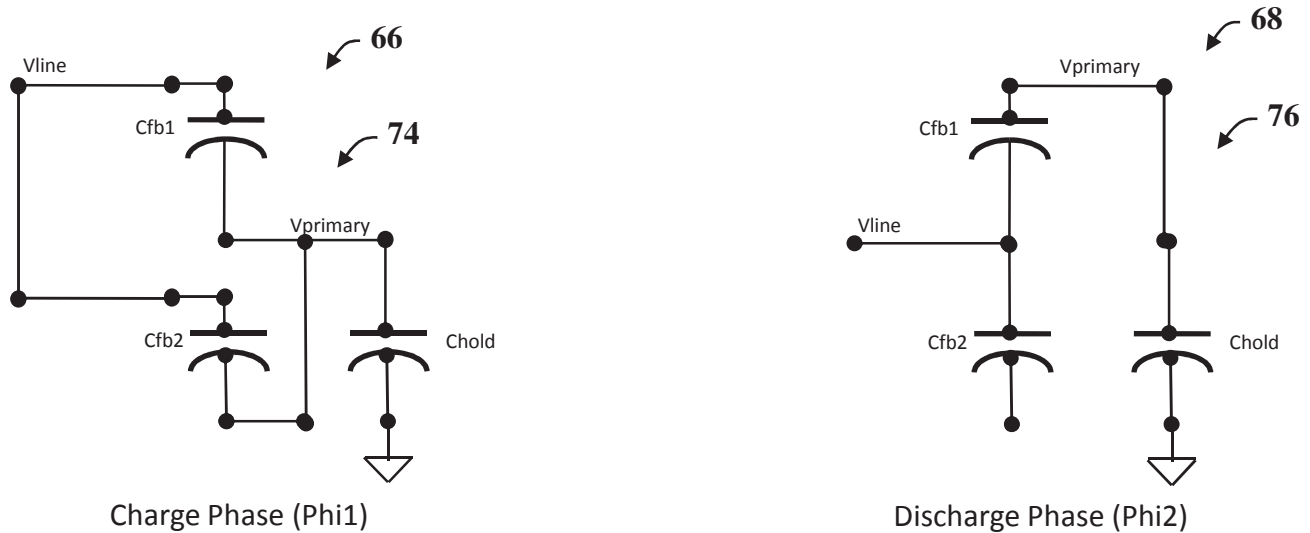


Figure 8

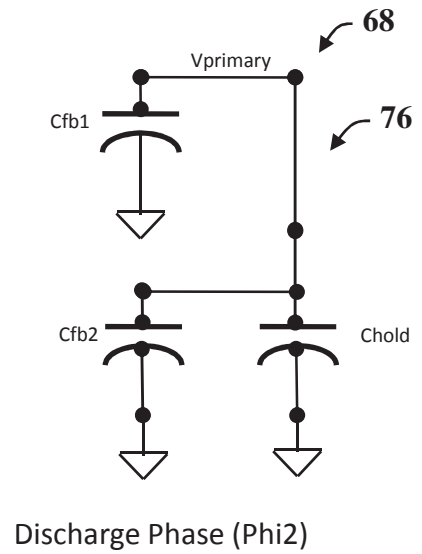
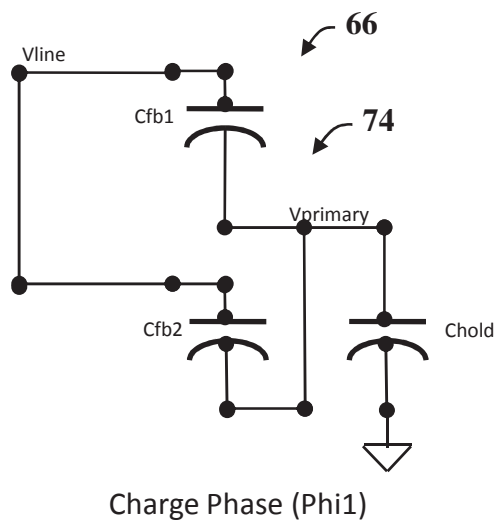
| Switch Name | Phase 1 | Phase 2<br>G=1/2 | Phase 2<br>G=2/3 | Phase 2 G=1 |
|-------------|---------|------------------|------------------|-------------|
| S1          | On      | Off              | Off              | Off         |
| S2          | Off     | On               | On               | On          |
| S3          | Off     | Off              | Off              | On          |
| S4          | On      | Off              | Off              | Off         |
| S5          | Off     | On               | Off              | Off         |
| S6          | Off     | Off              | On               | Off         |
| S7          | On      | Off              | Off              | On          |
| S8          | On      | Off              | Off              | Off         |
| S9          | Off     | On               | On               | Off         |

Figure 9



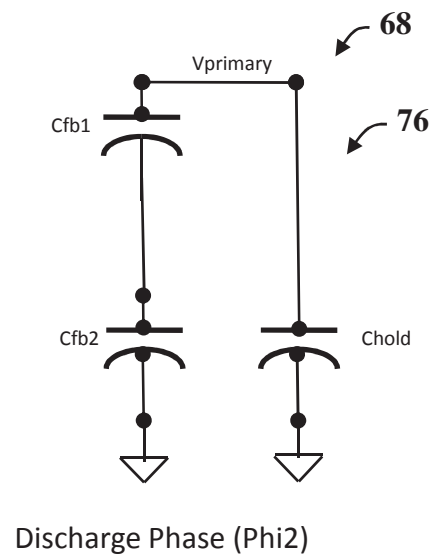
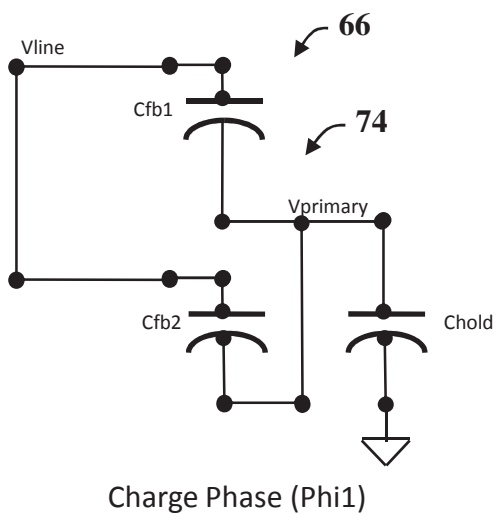
Switched Capacitor Gain: 1x

Figure 10



Switched Capacitor Gain:  $1/2x$

Figure 11



Switched Capacitor Gain:  $2/3 x$

Figure 12

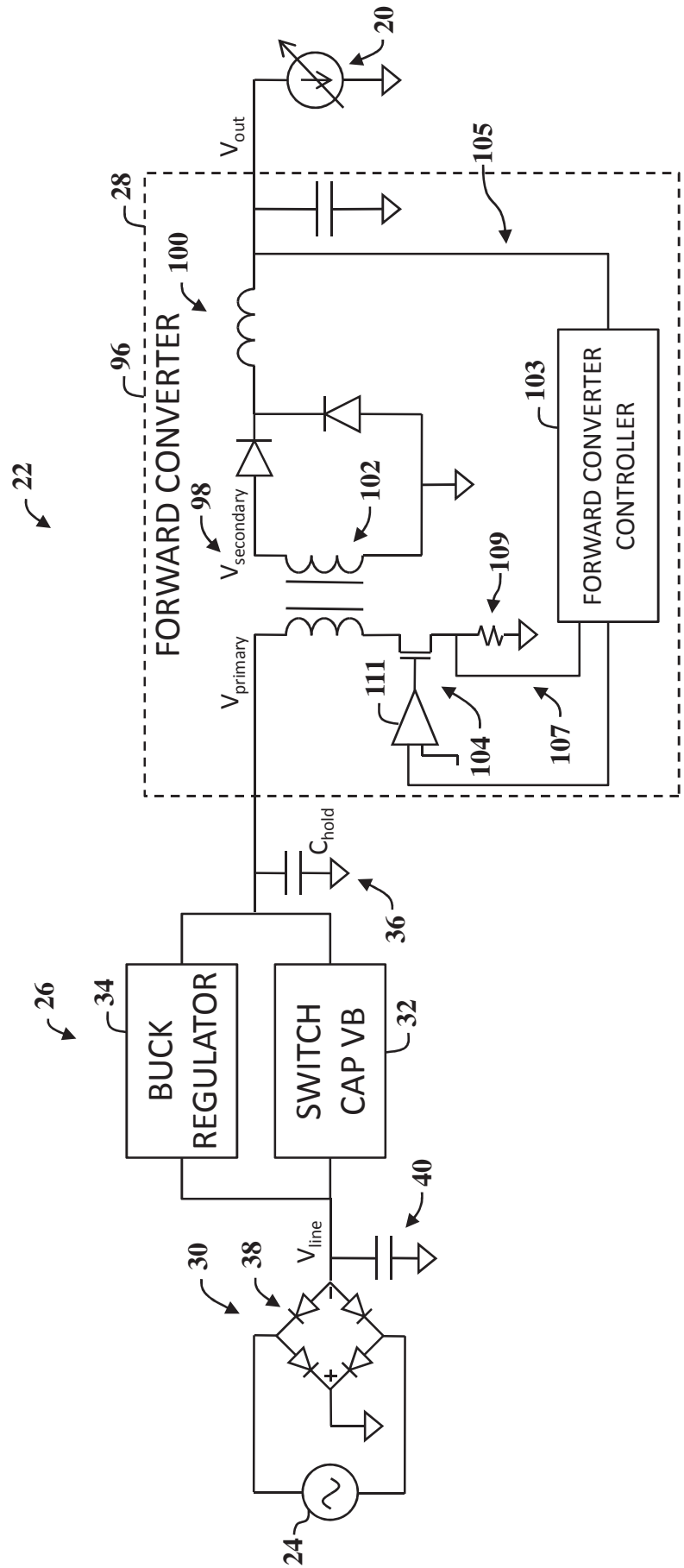


Figure 13

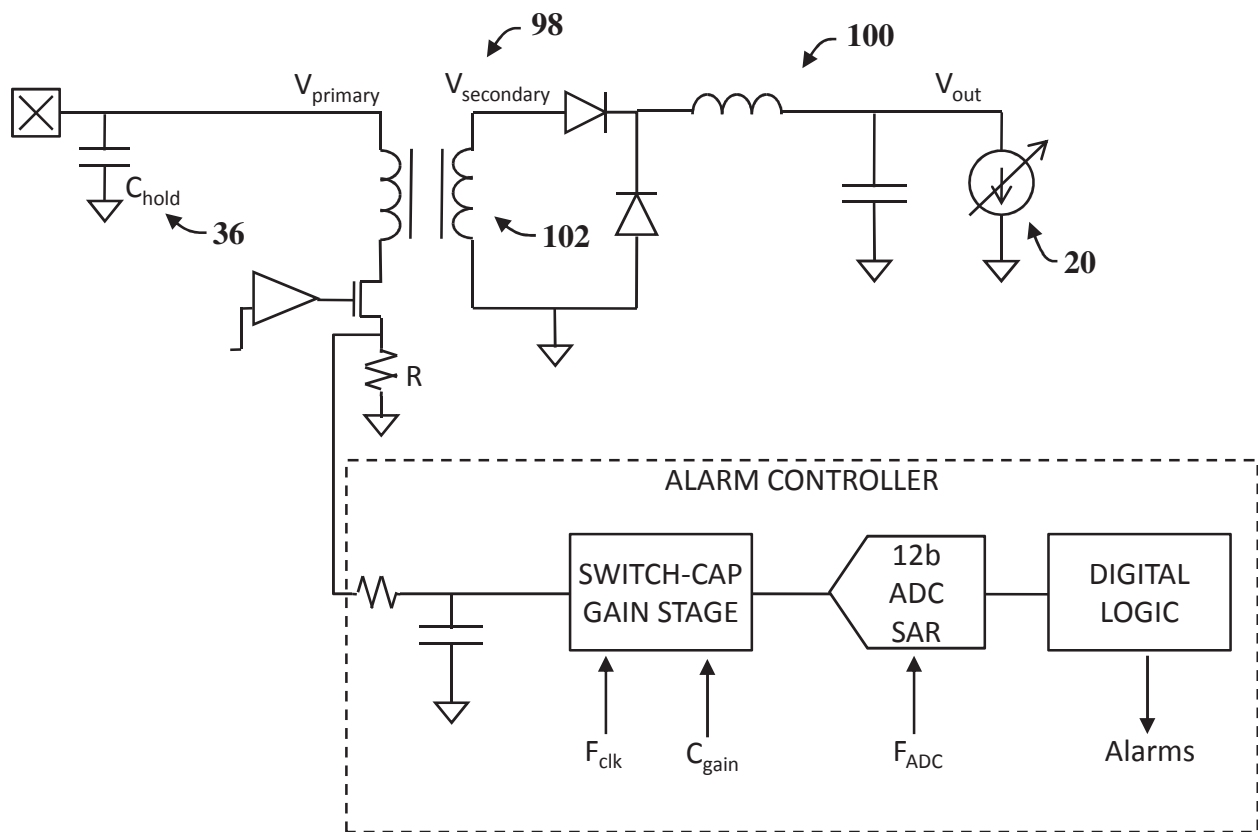


Figure 14

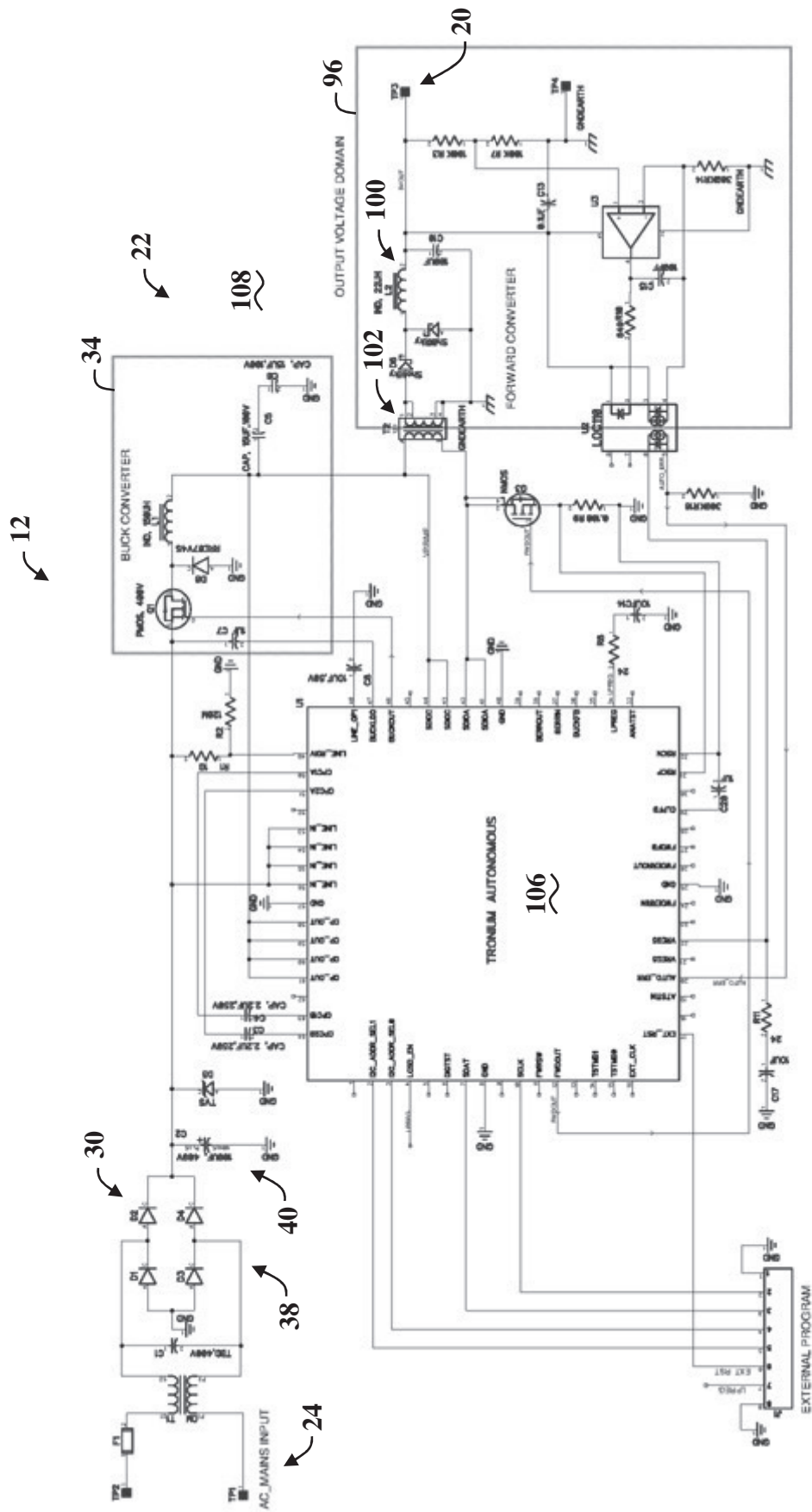


Figure 15

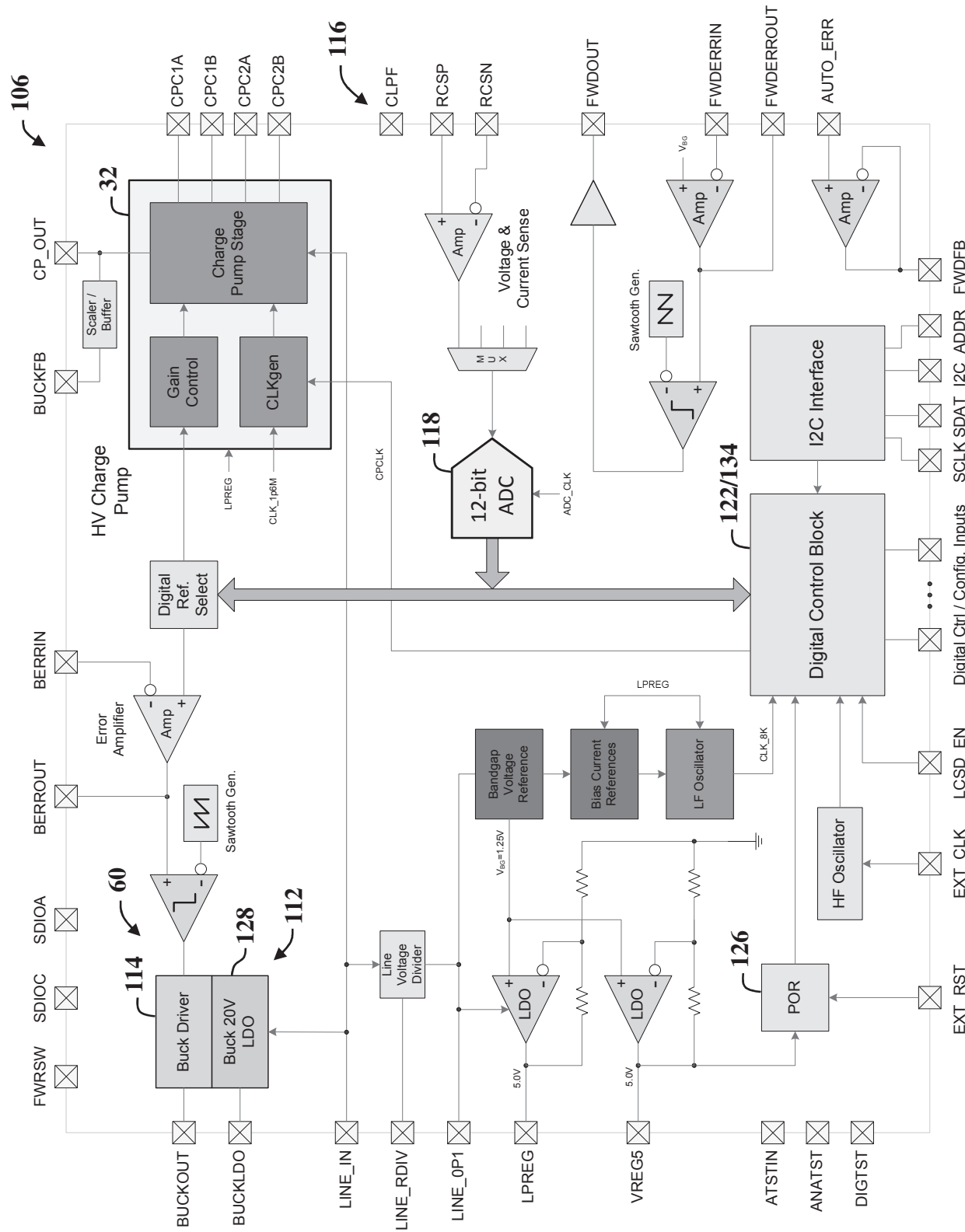


Figure 16

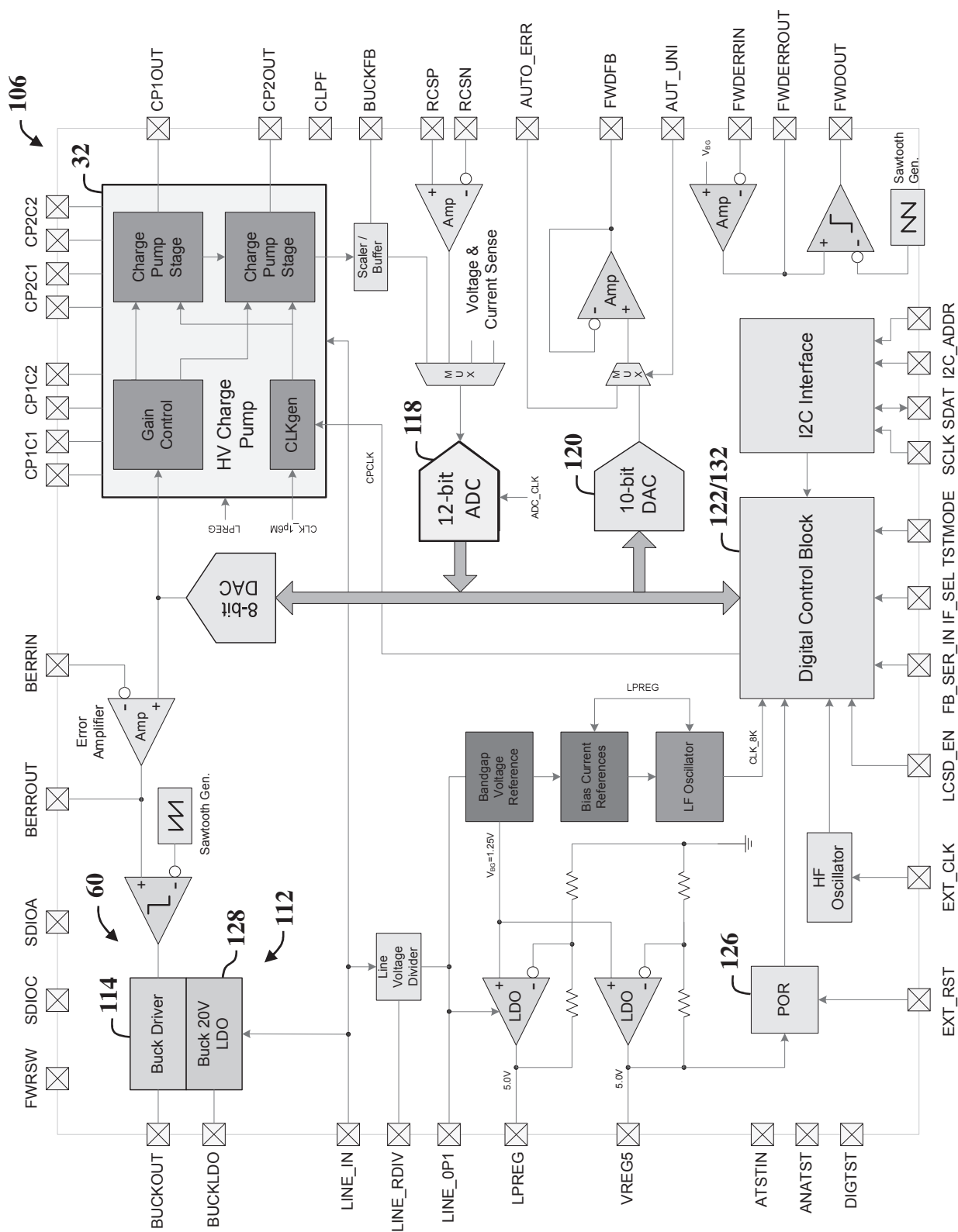


Figure 17A

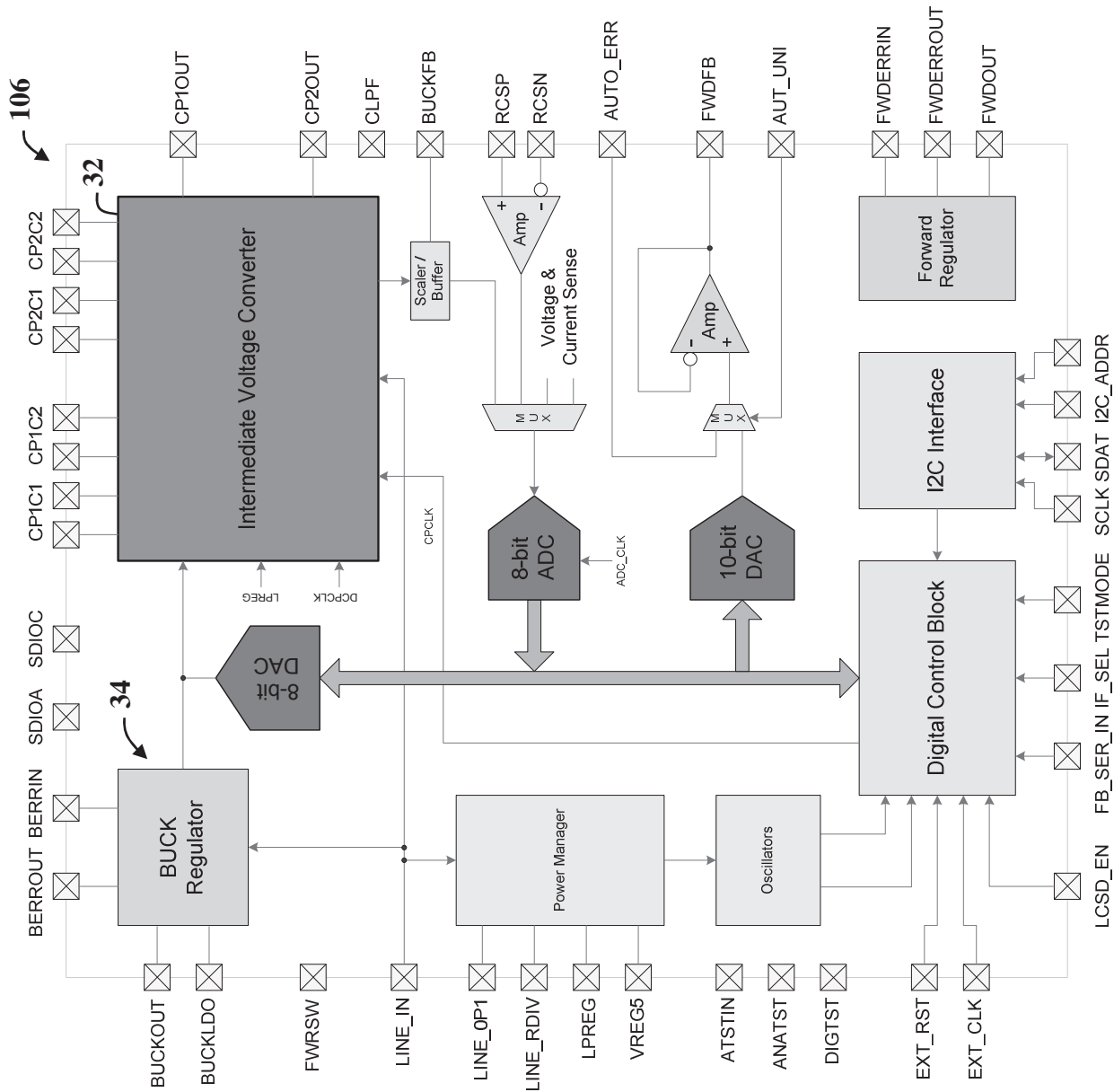


Figure 17B

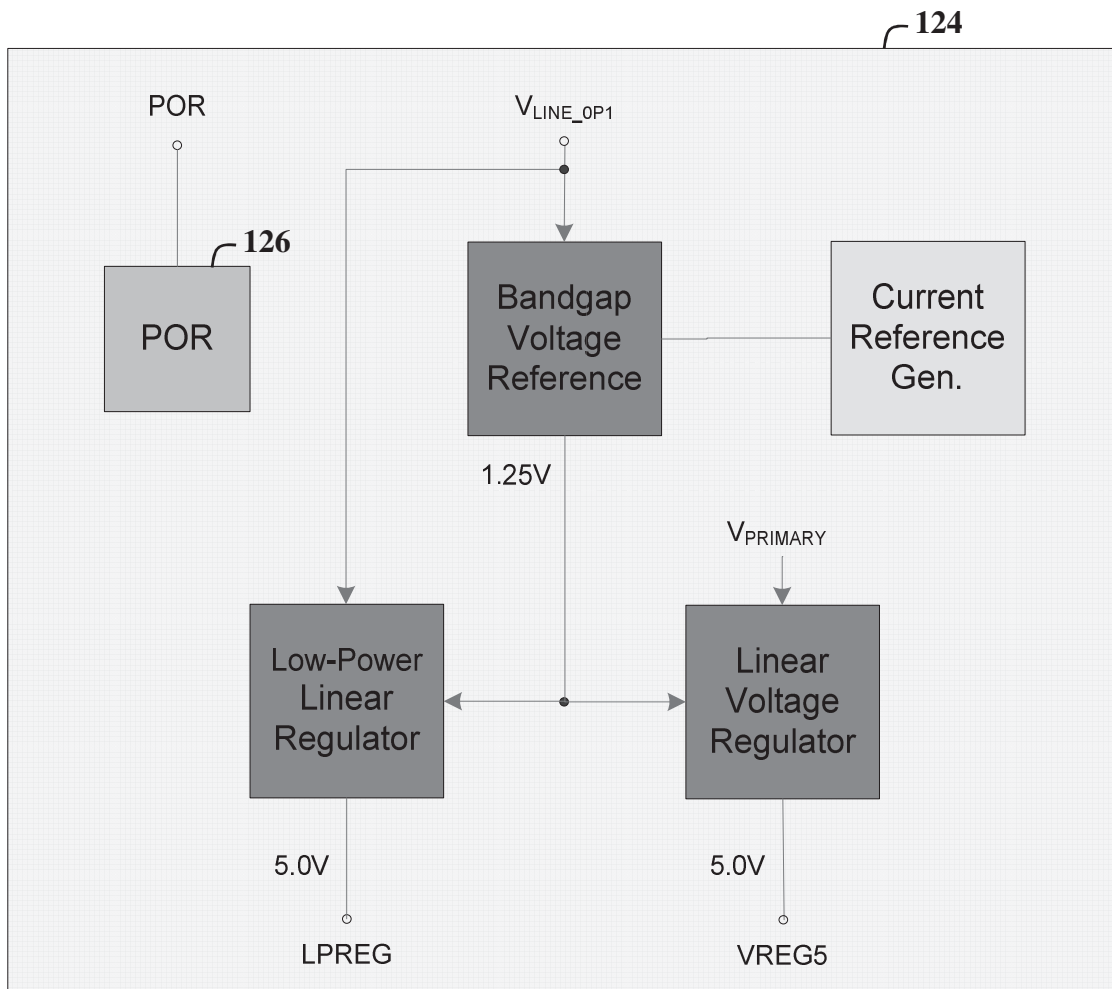


Figure 18

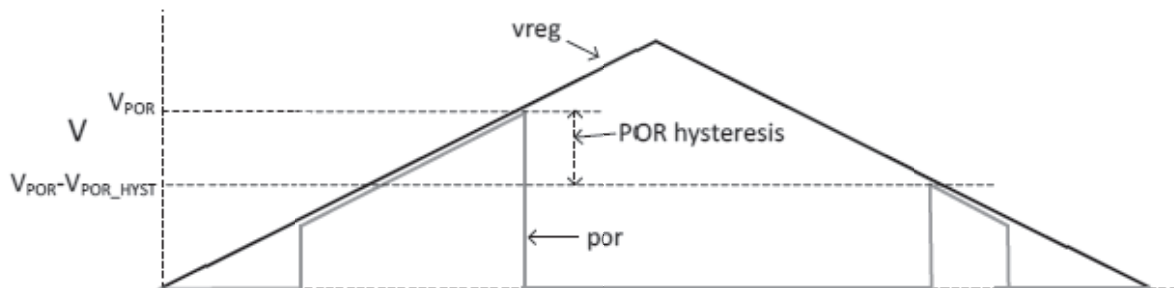
**POR Threshold Voltages**

Figure 19

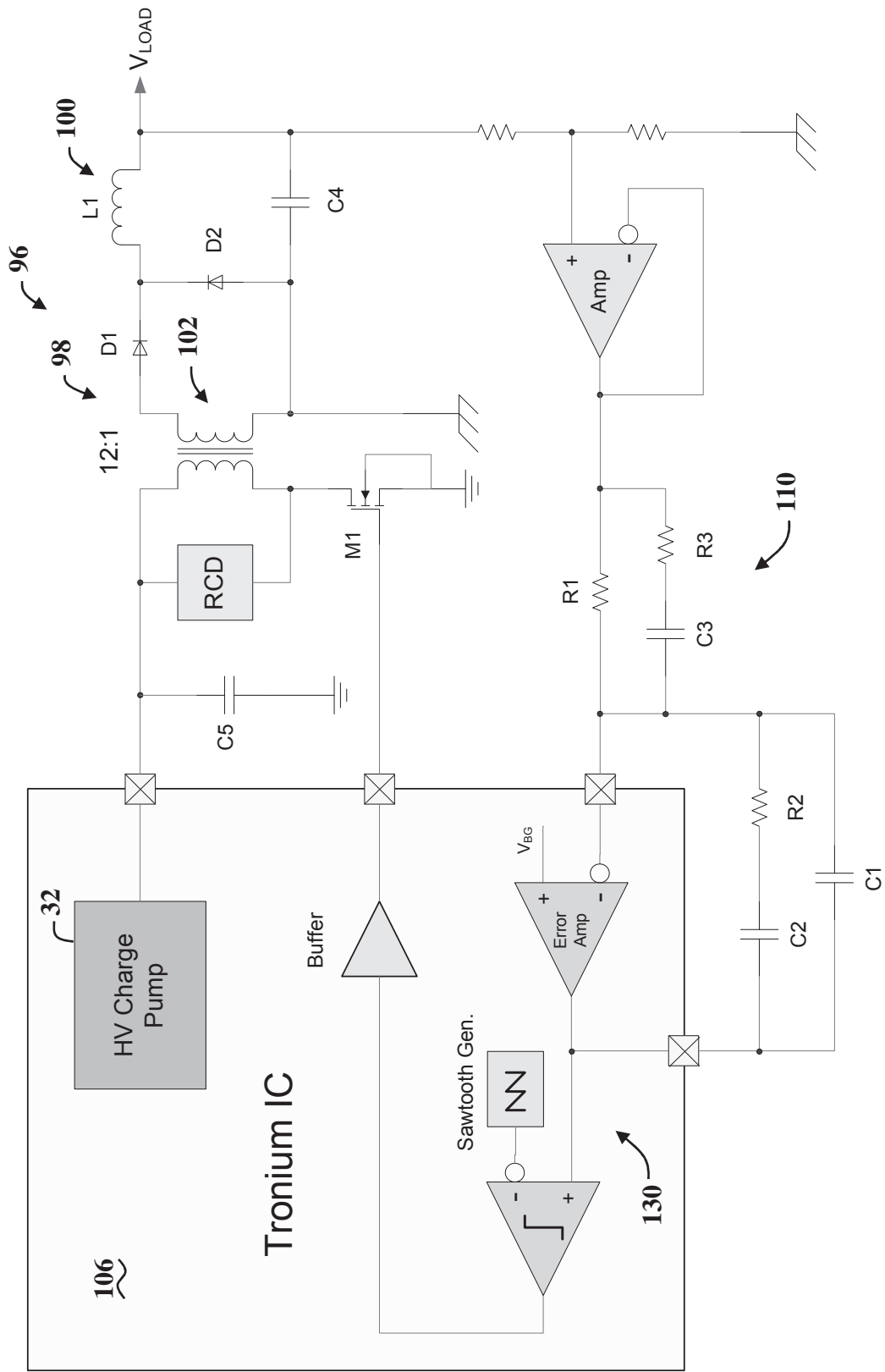


Figure 20

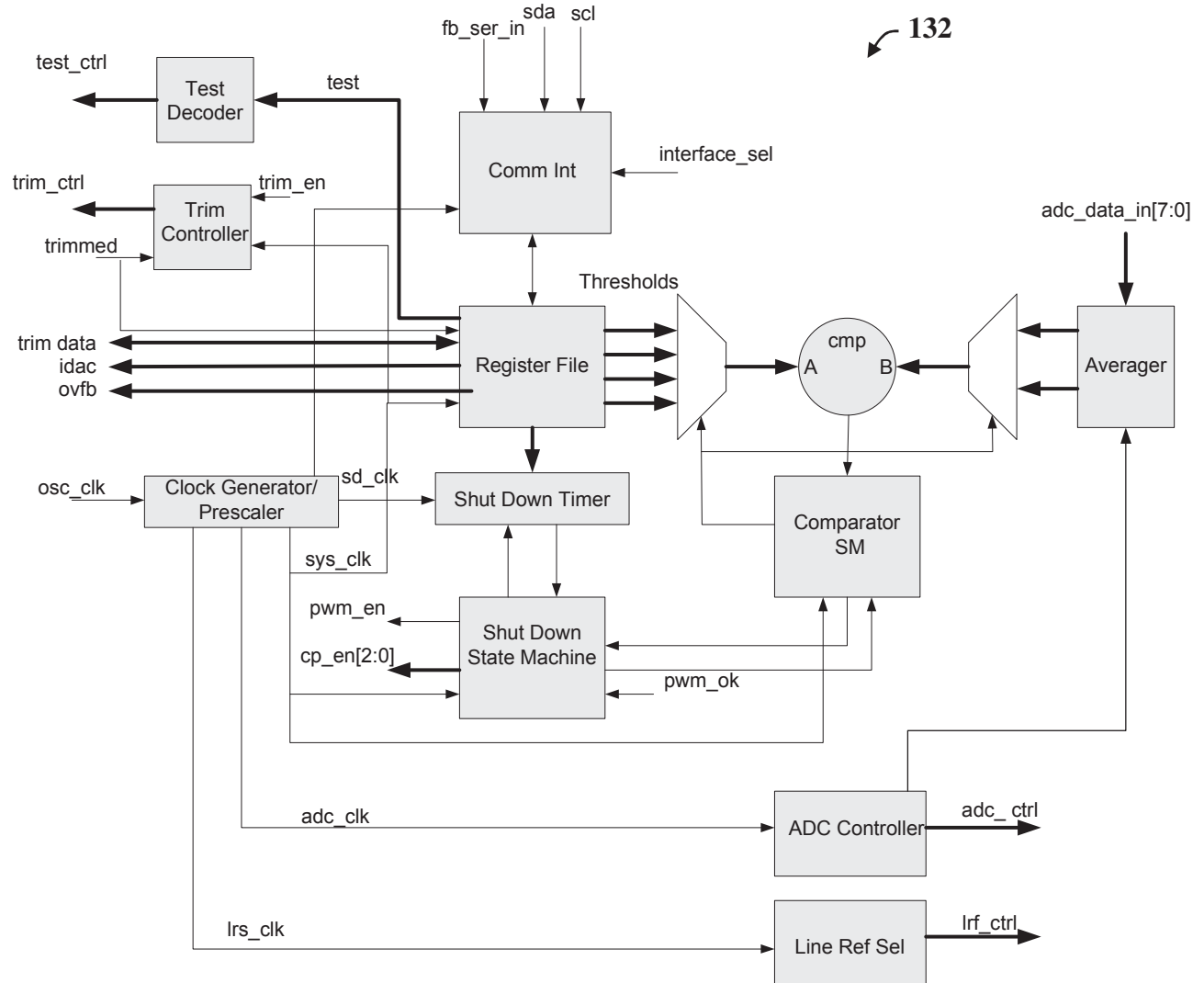


Figure 21

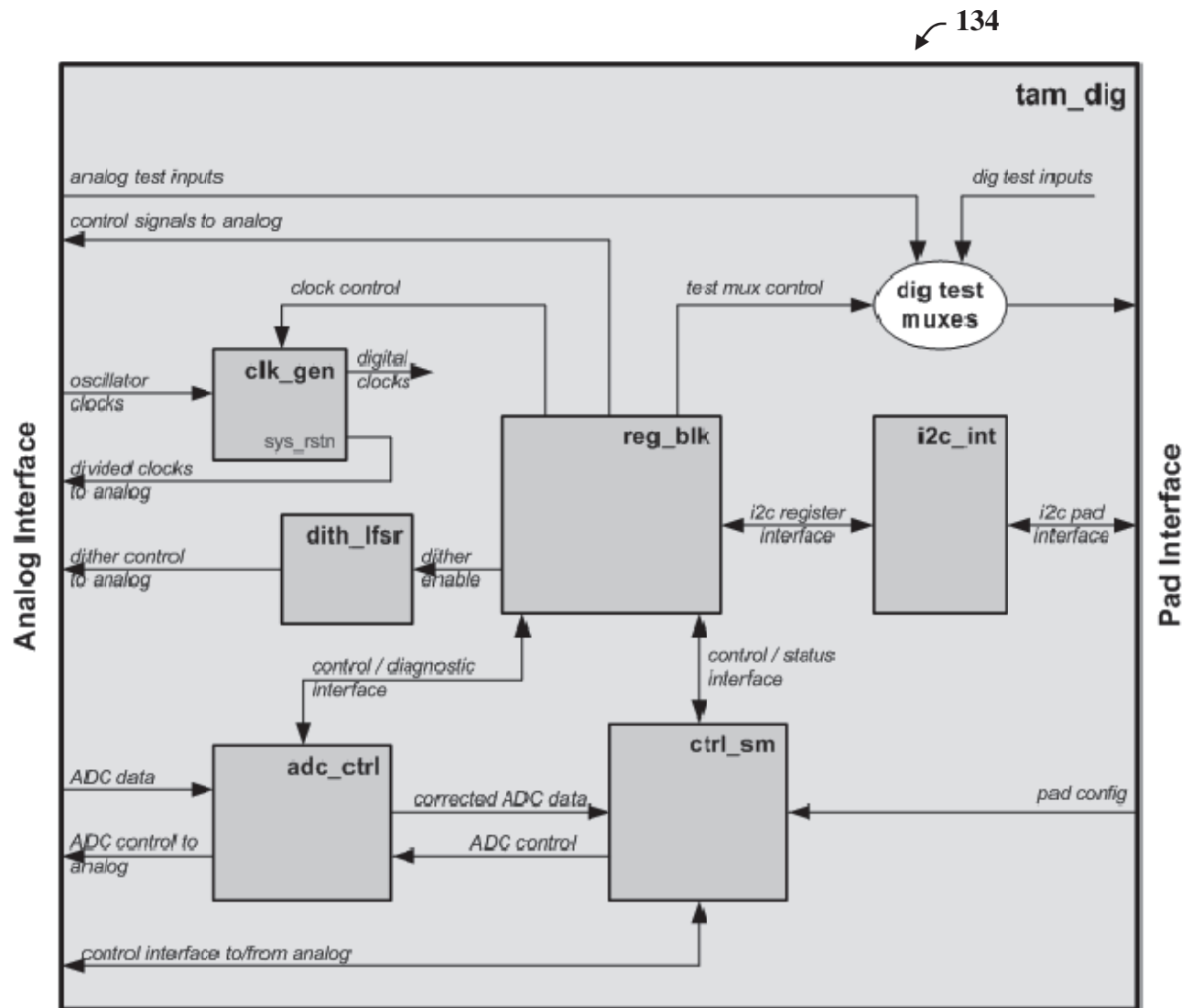


Figure 22

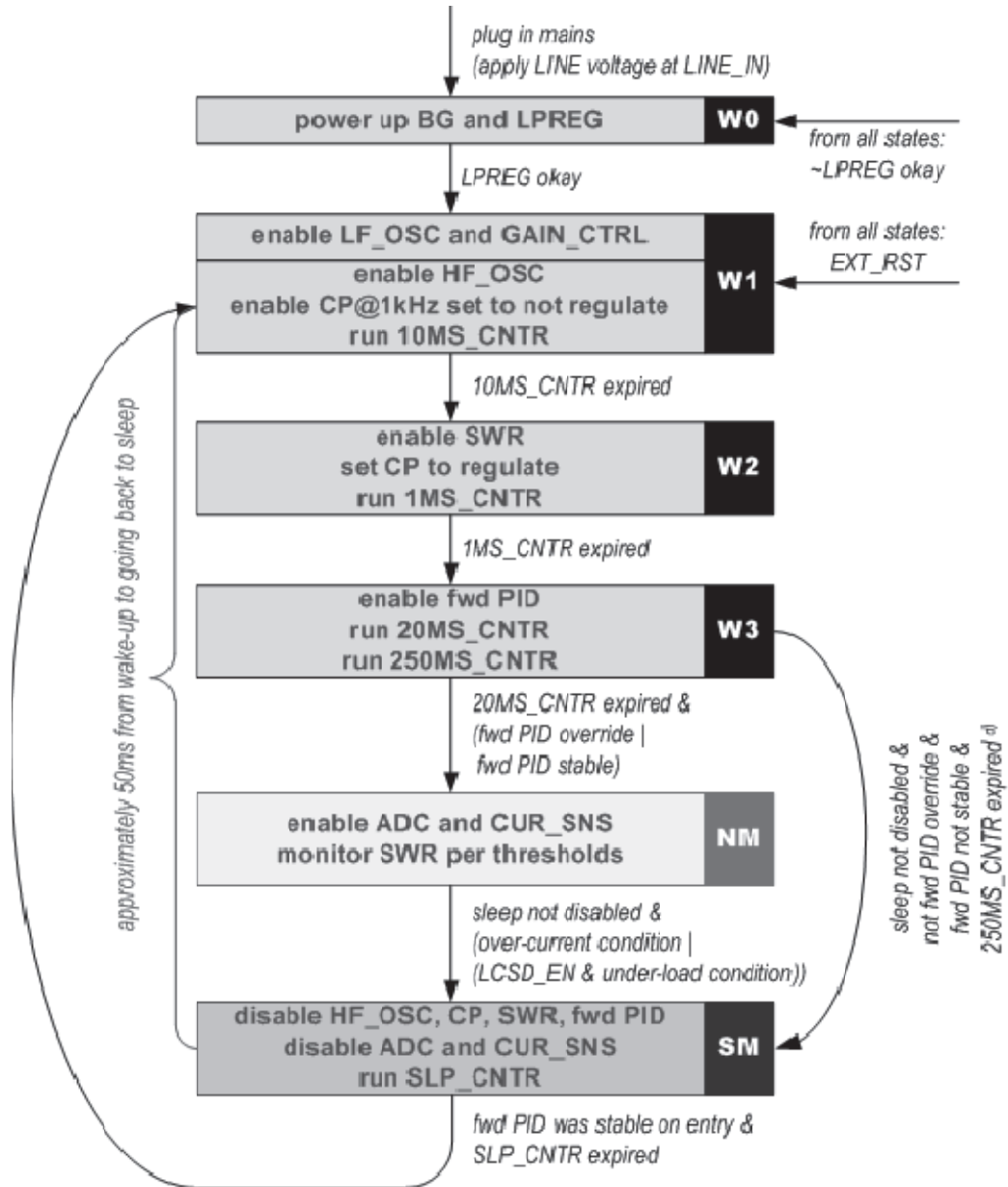


Figure 23

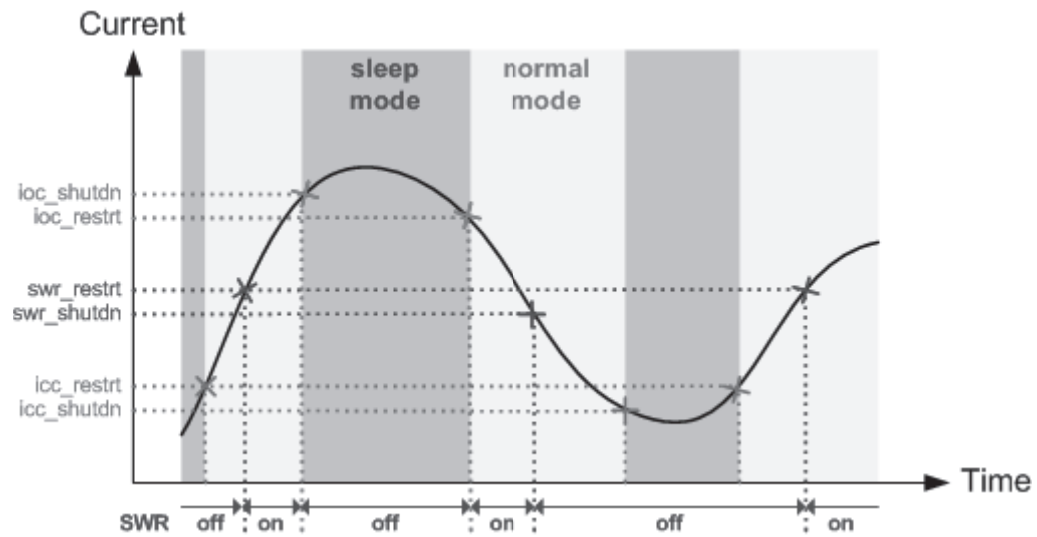


Figure 24

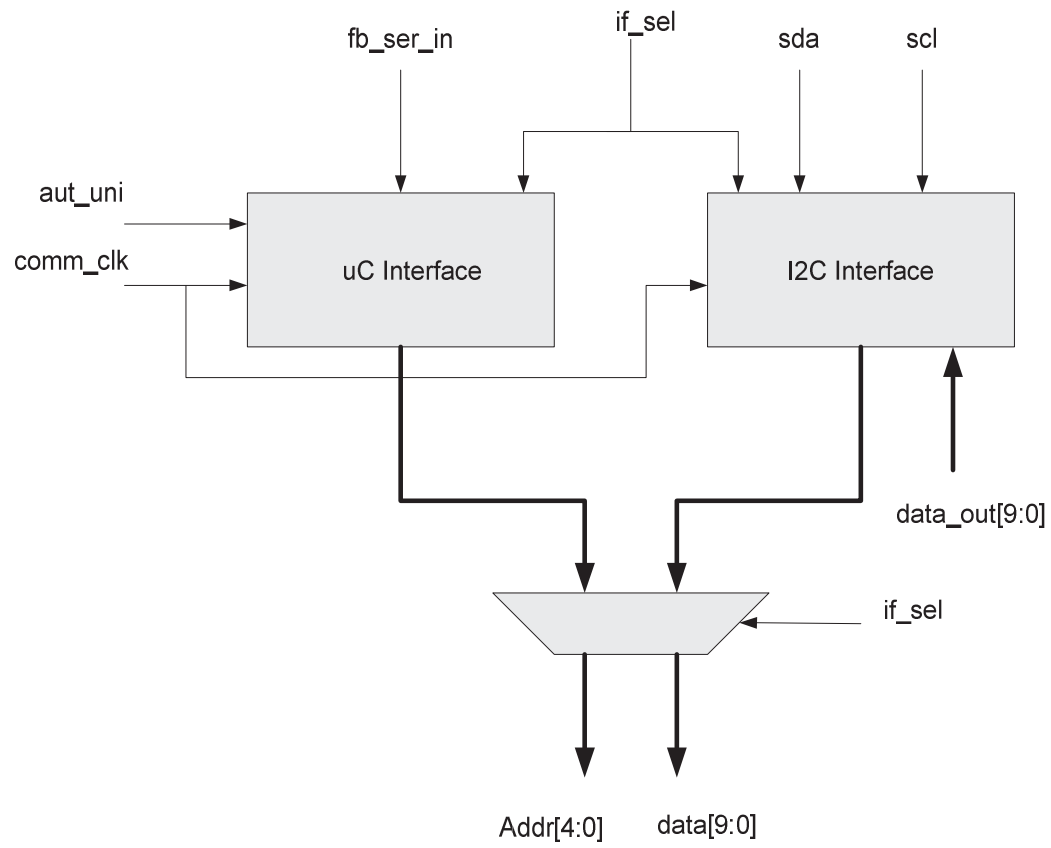


Figure 25

Packet Format

|  |       |     |           |           |      |
|--|-------|-----|-----------|-----------|------|
|  | START | R/W | Addr[4:0] | Data[9:0] | IDLE |
|--|-------|-----|-----------|-----------|------|

Start = Not Idle State  
Idle = Inactive state from Optical Isolator  
R/W = 1 for Read

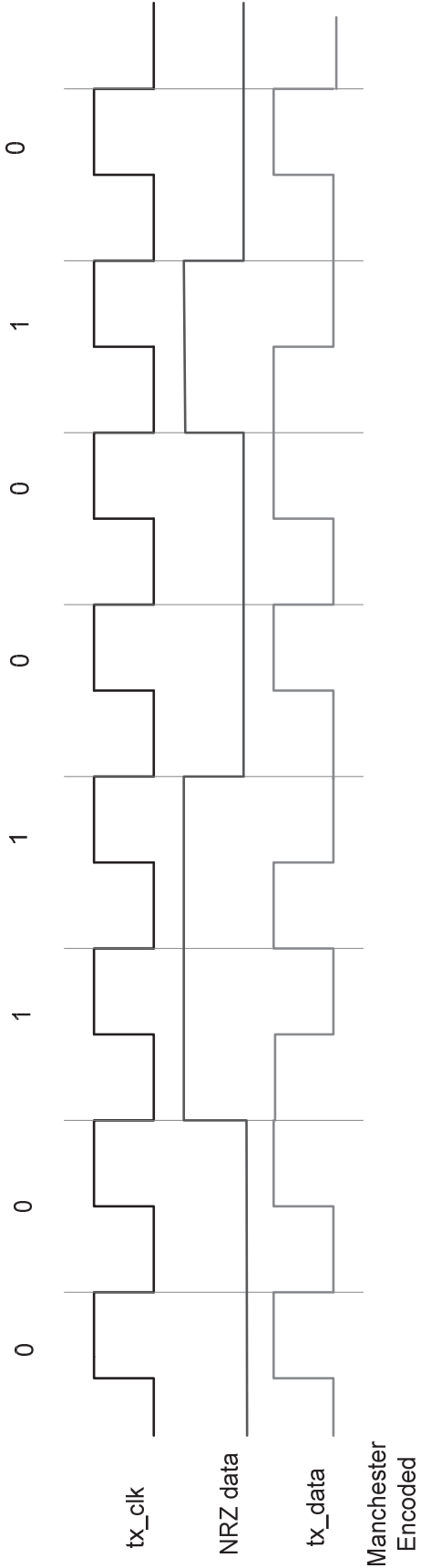


Figure 26

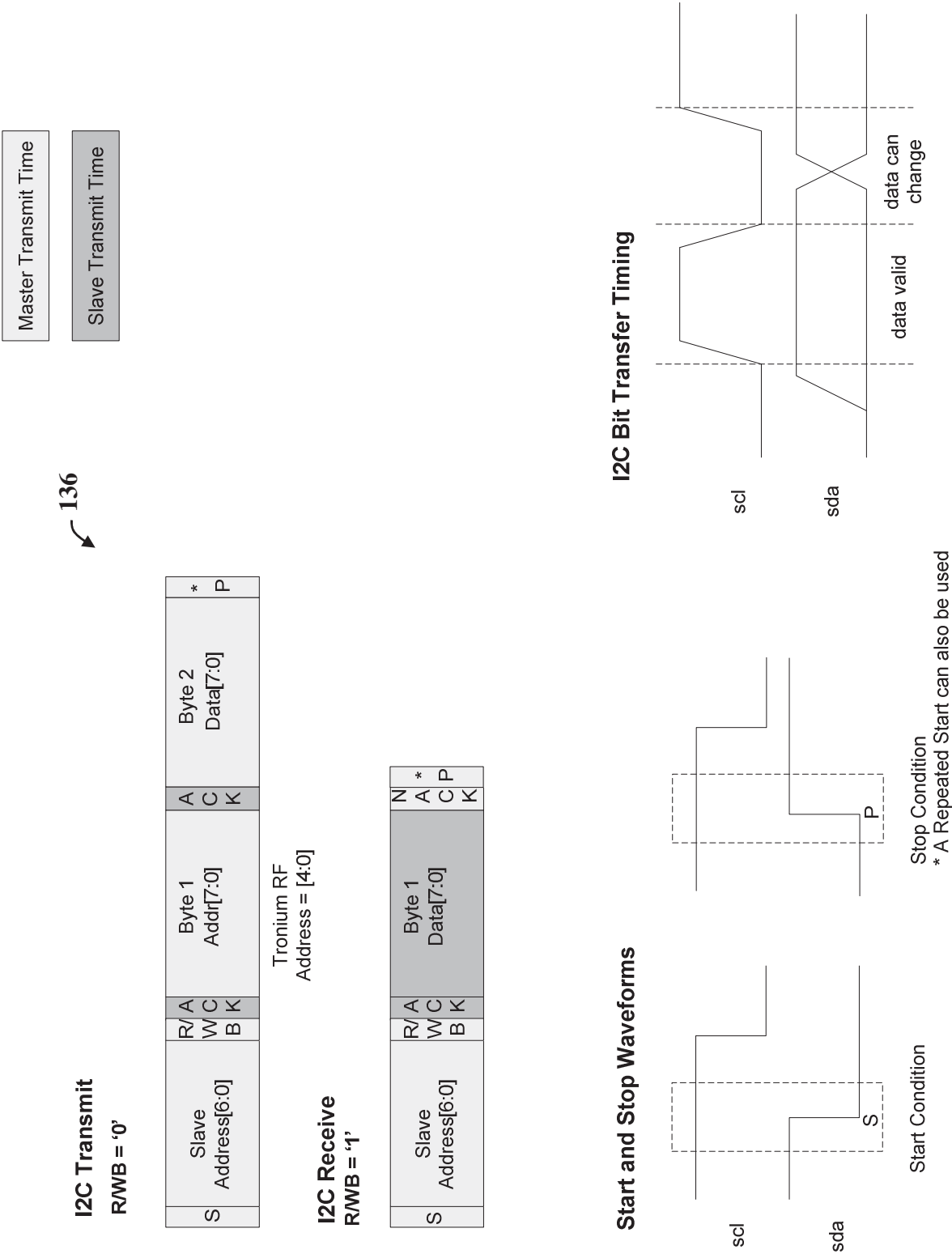


Figure 27

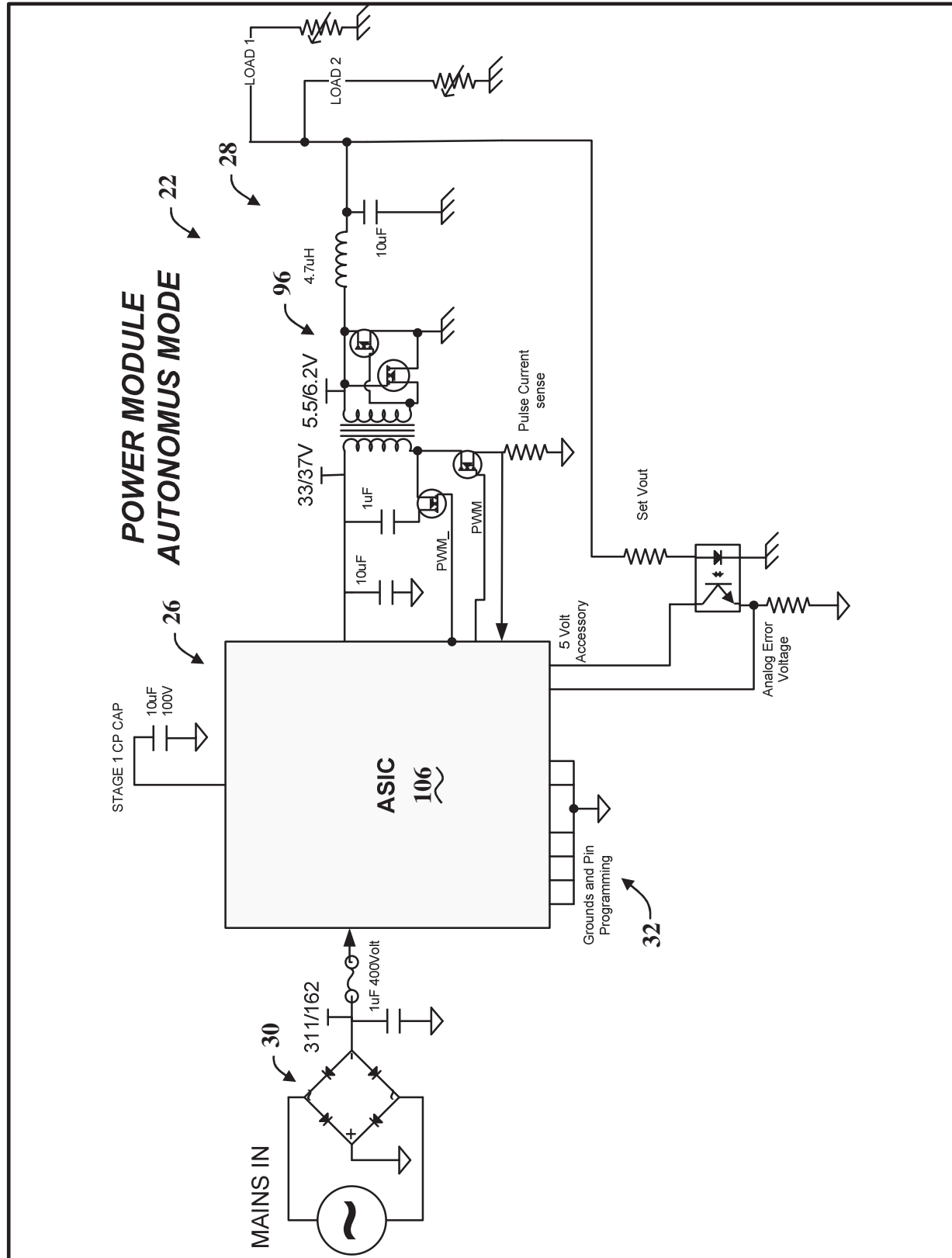


Figure 28

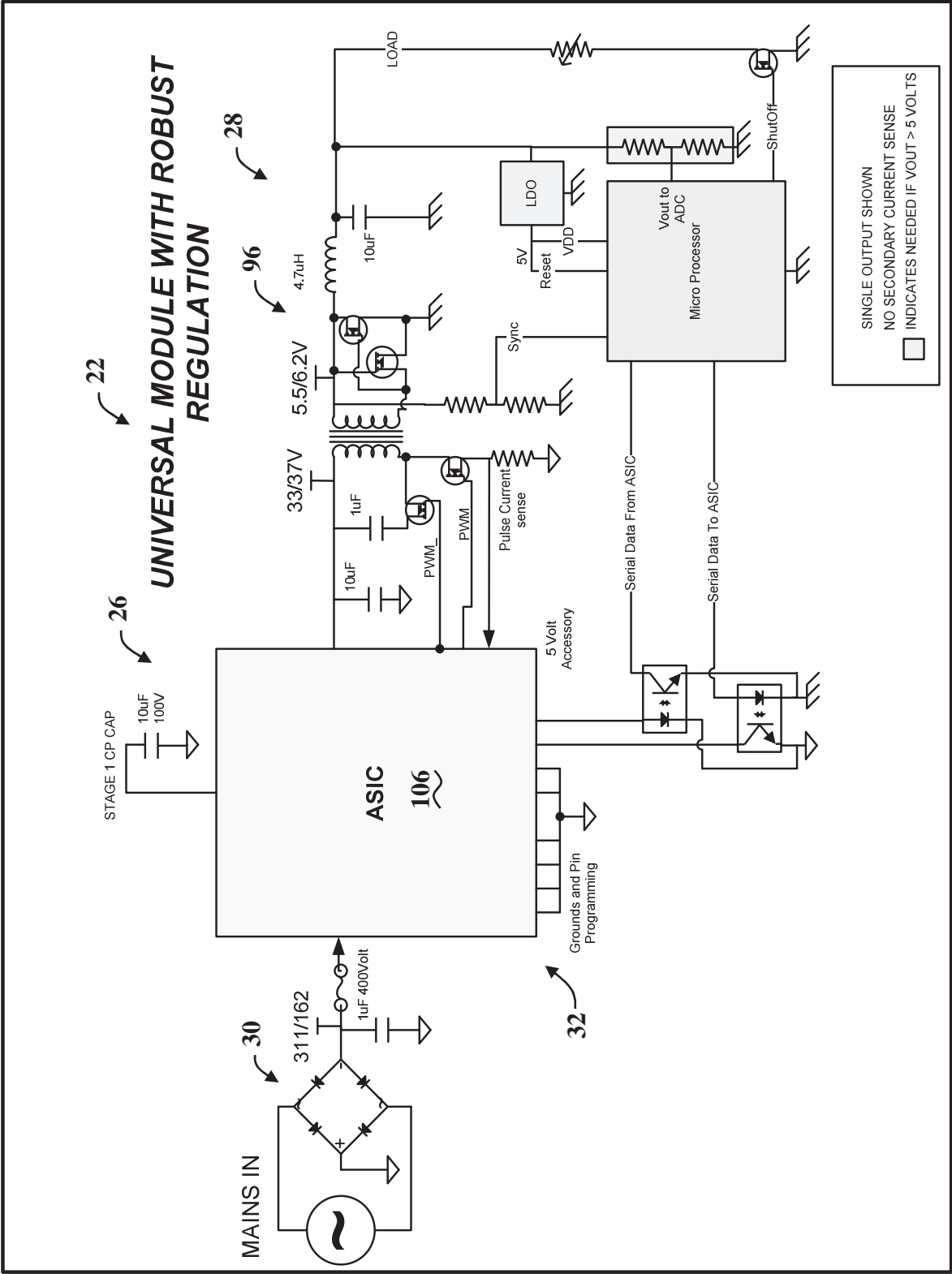


Figure 29

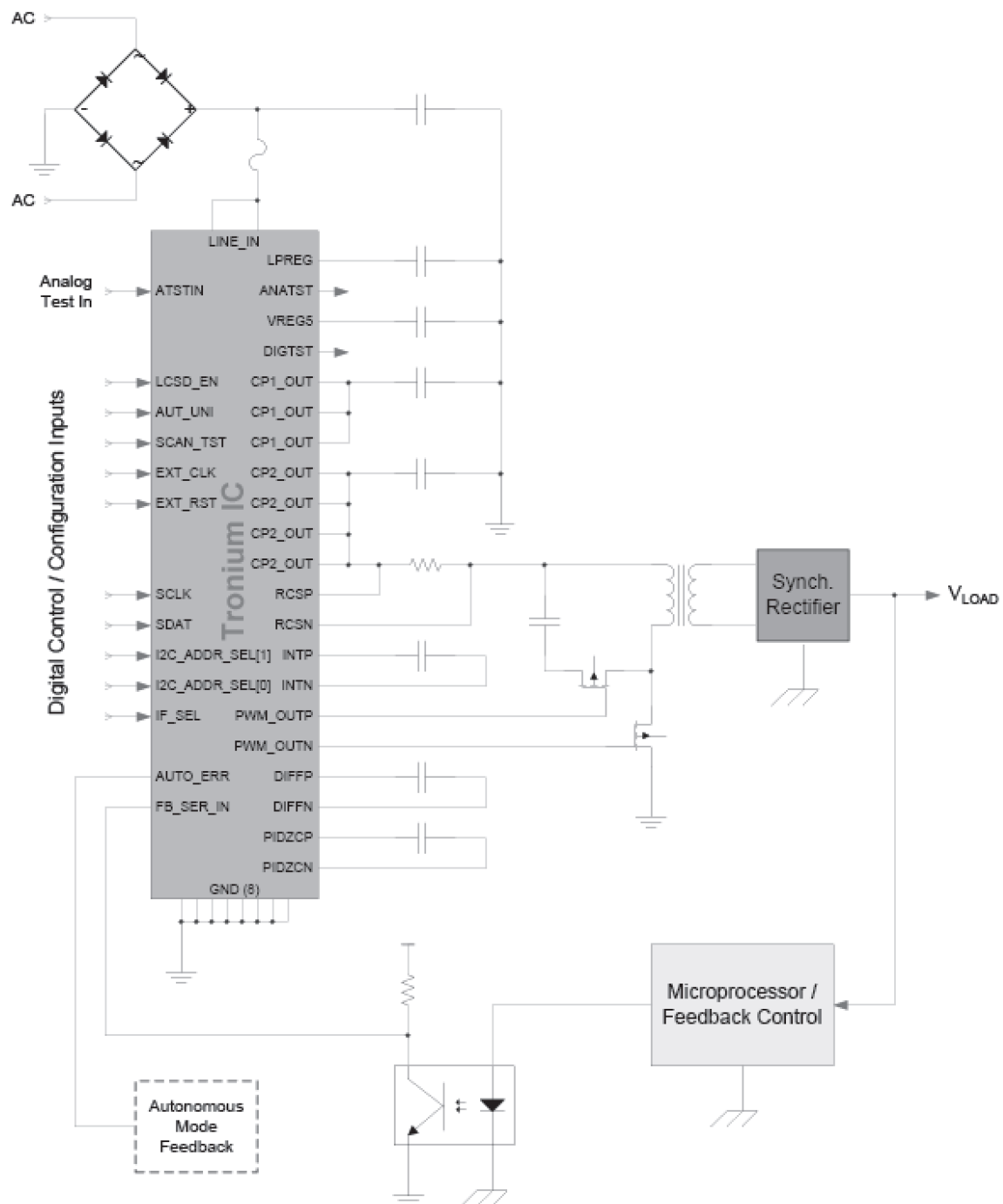


Figure 30

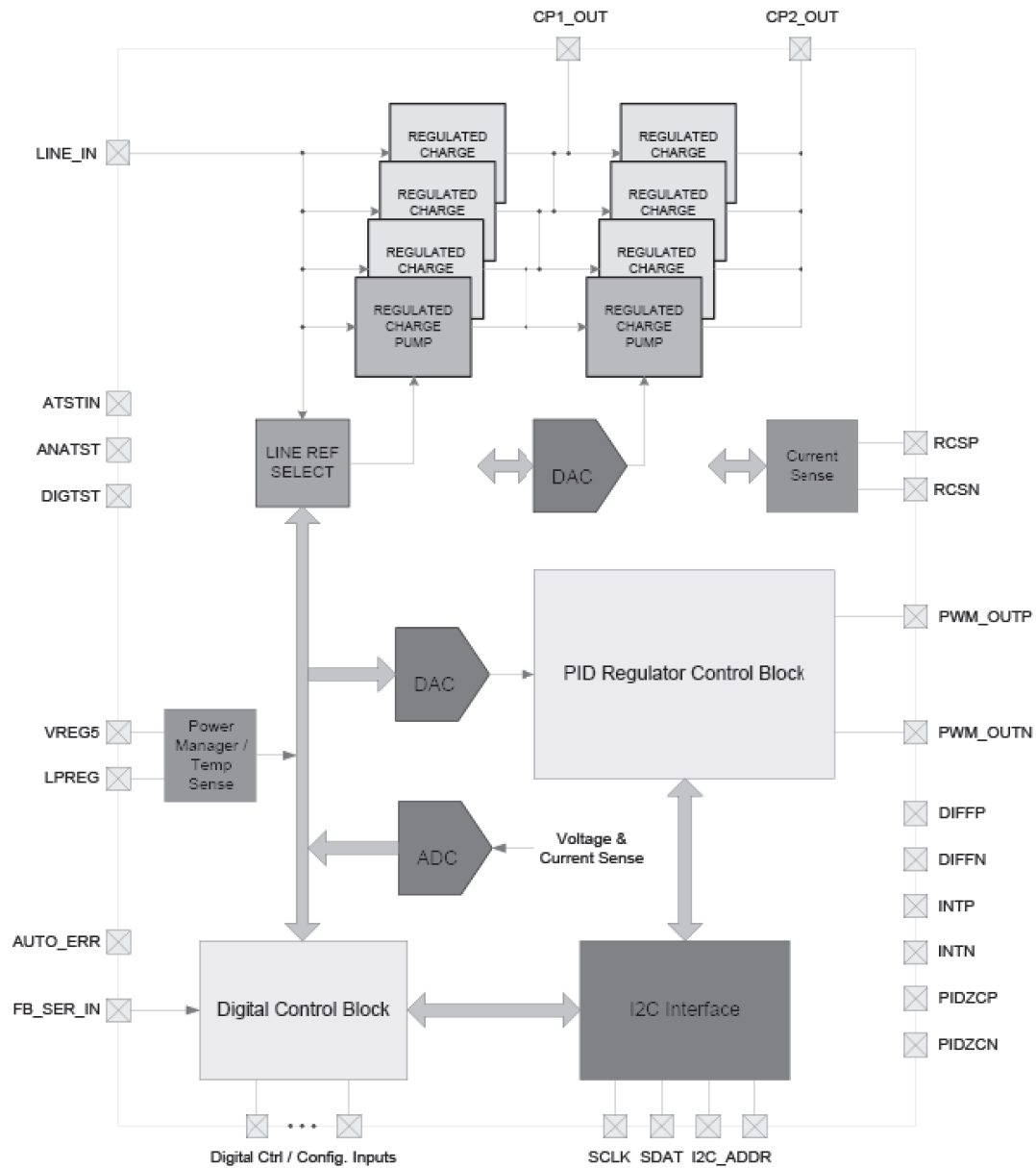


Figure 31

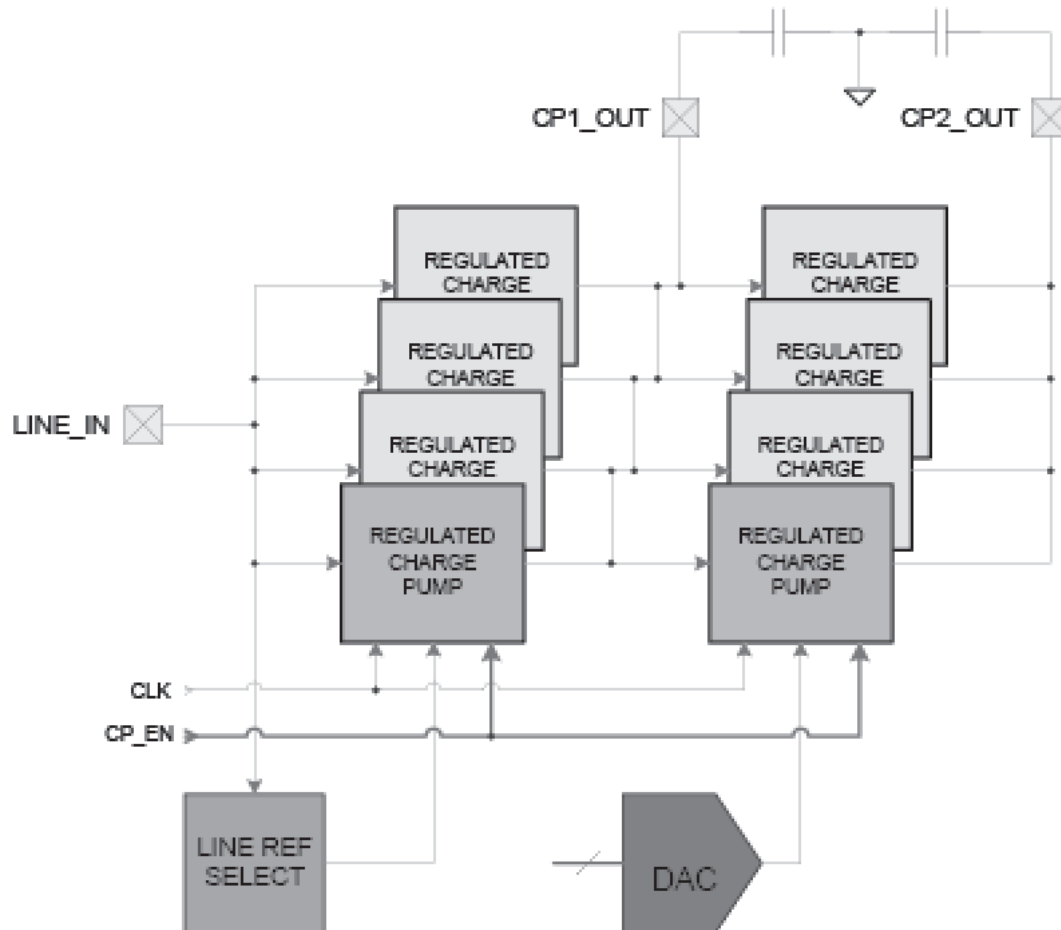


Figure 32

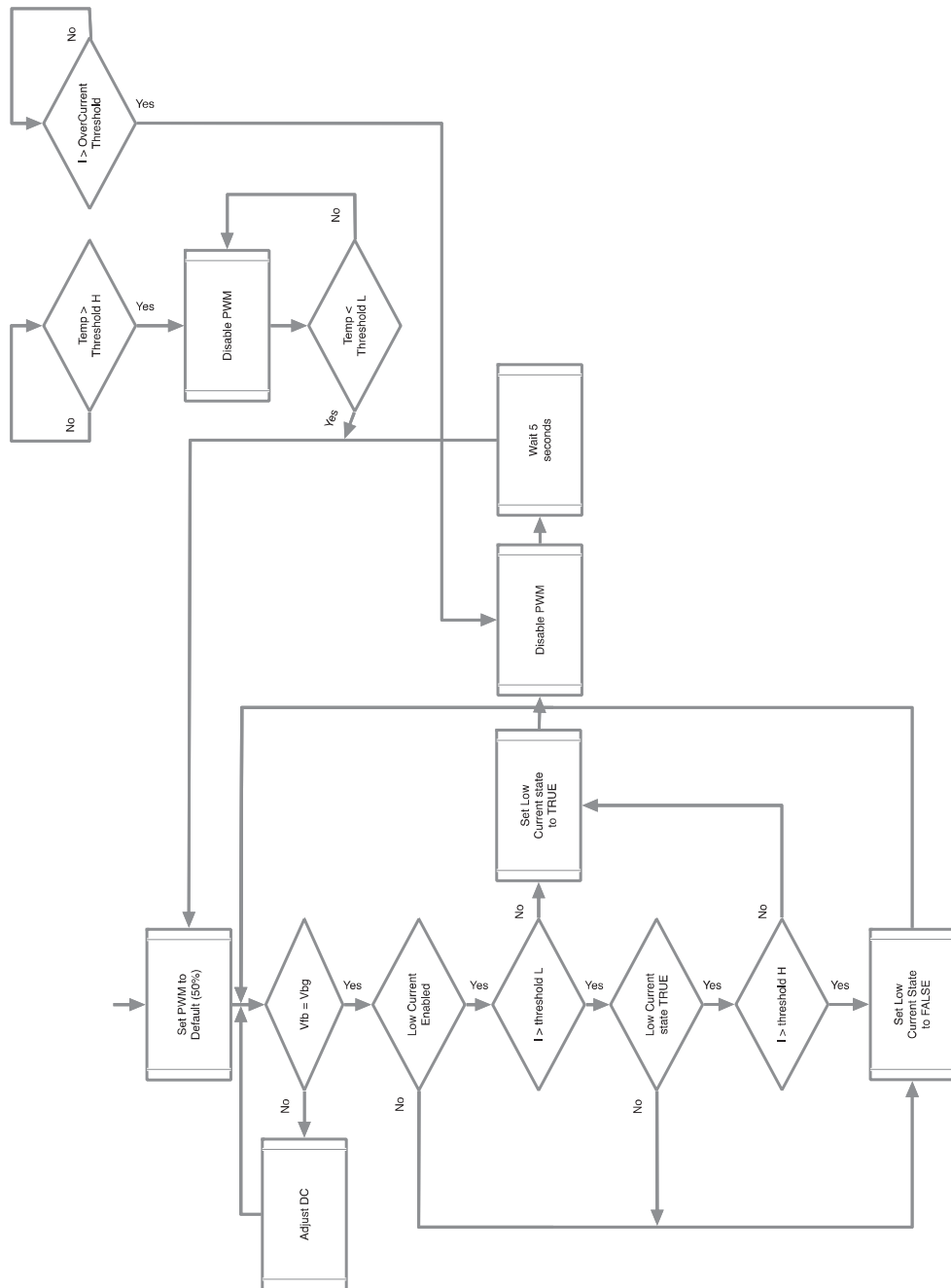
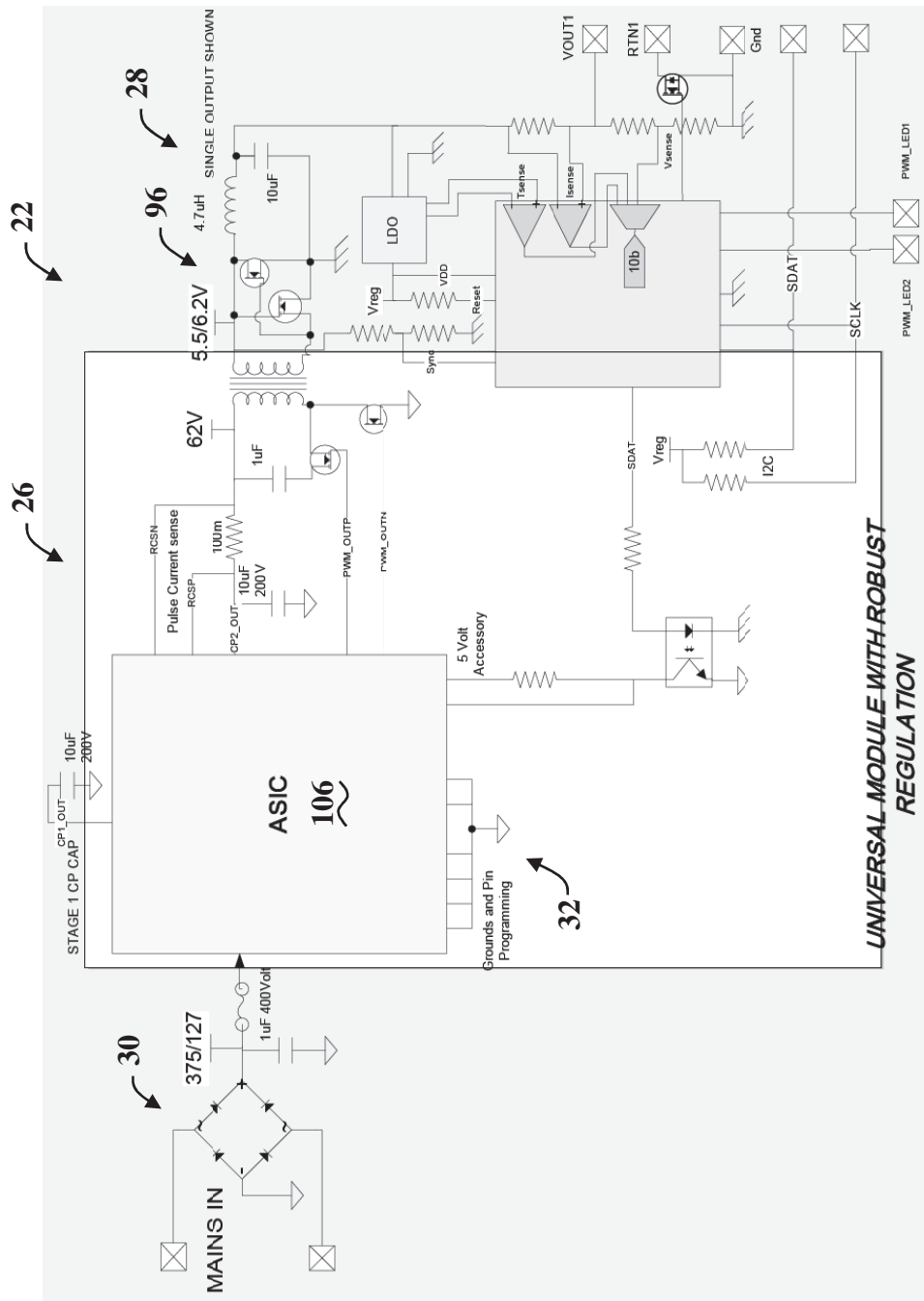


Figure 33



### Figure 34

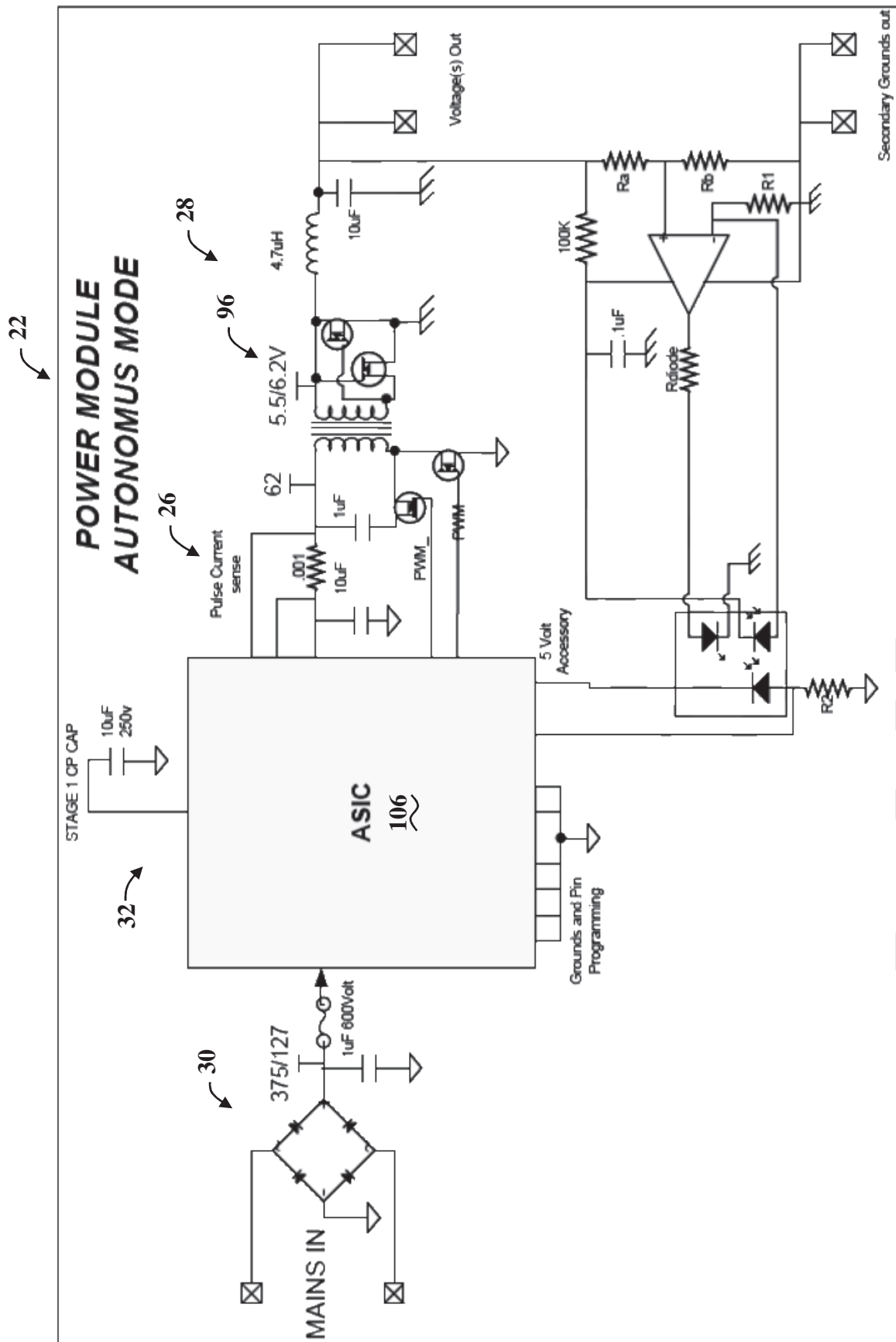


Figure 35

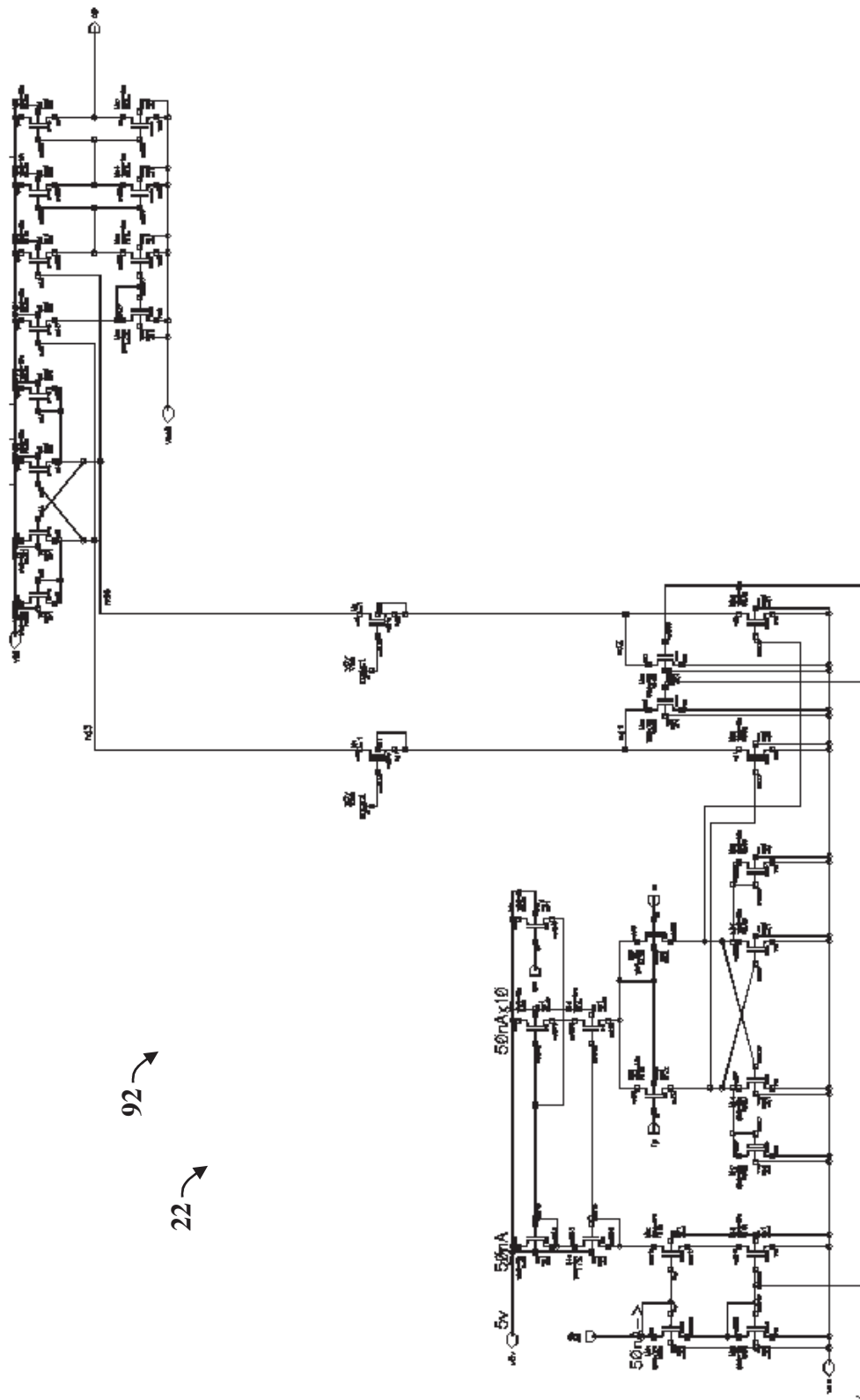


Figure 36

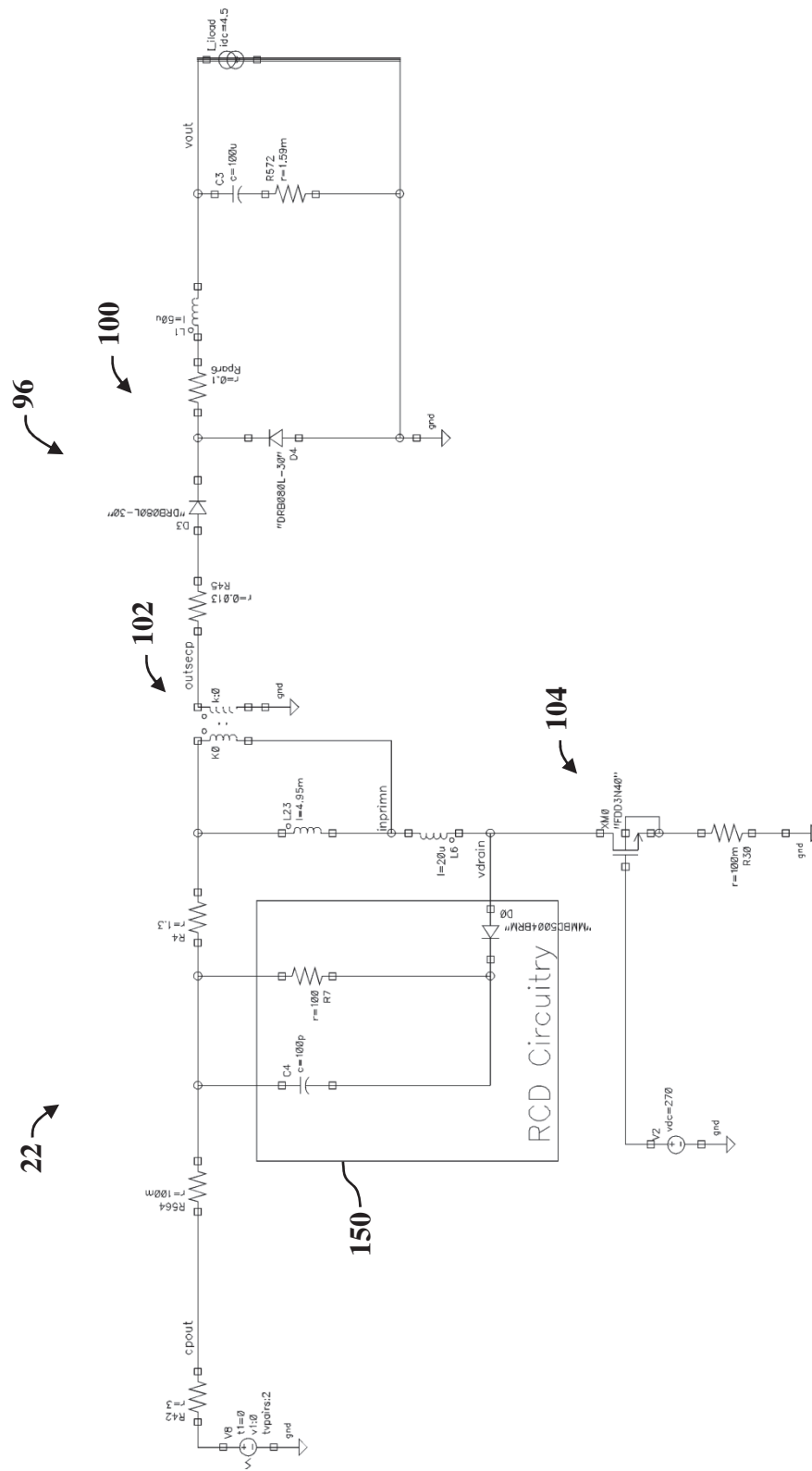


Figure 37

Figure 38

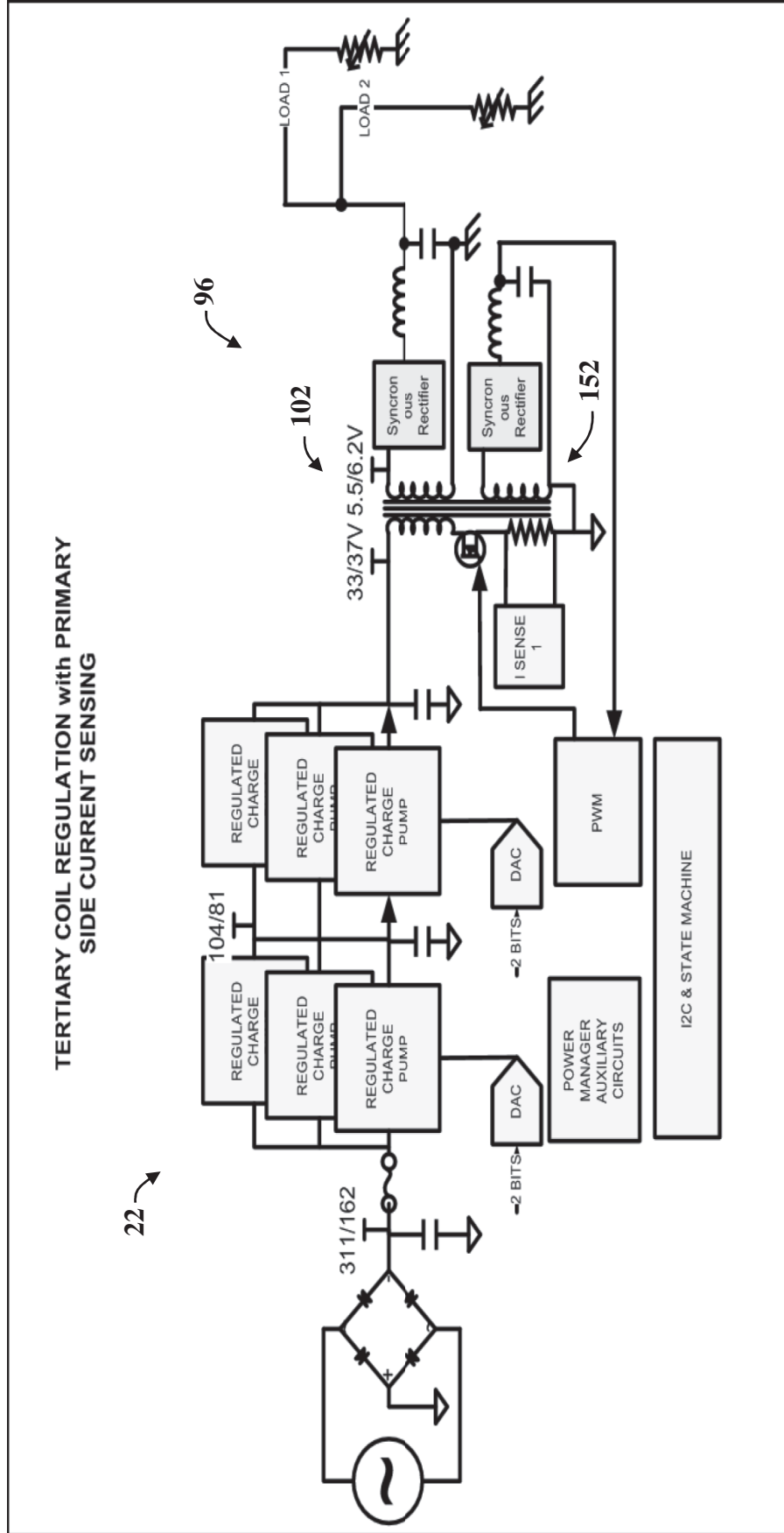


Figure 39

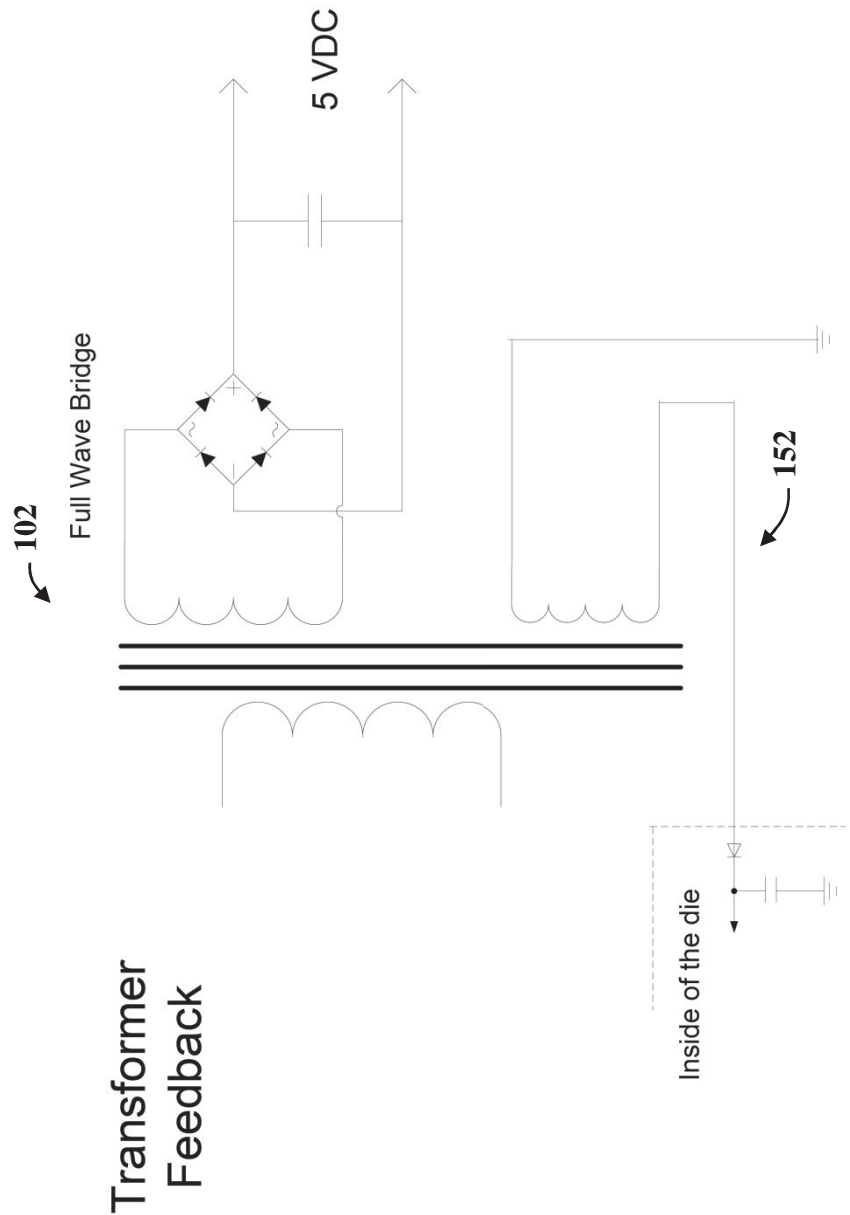


Figure 40

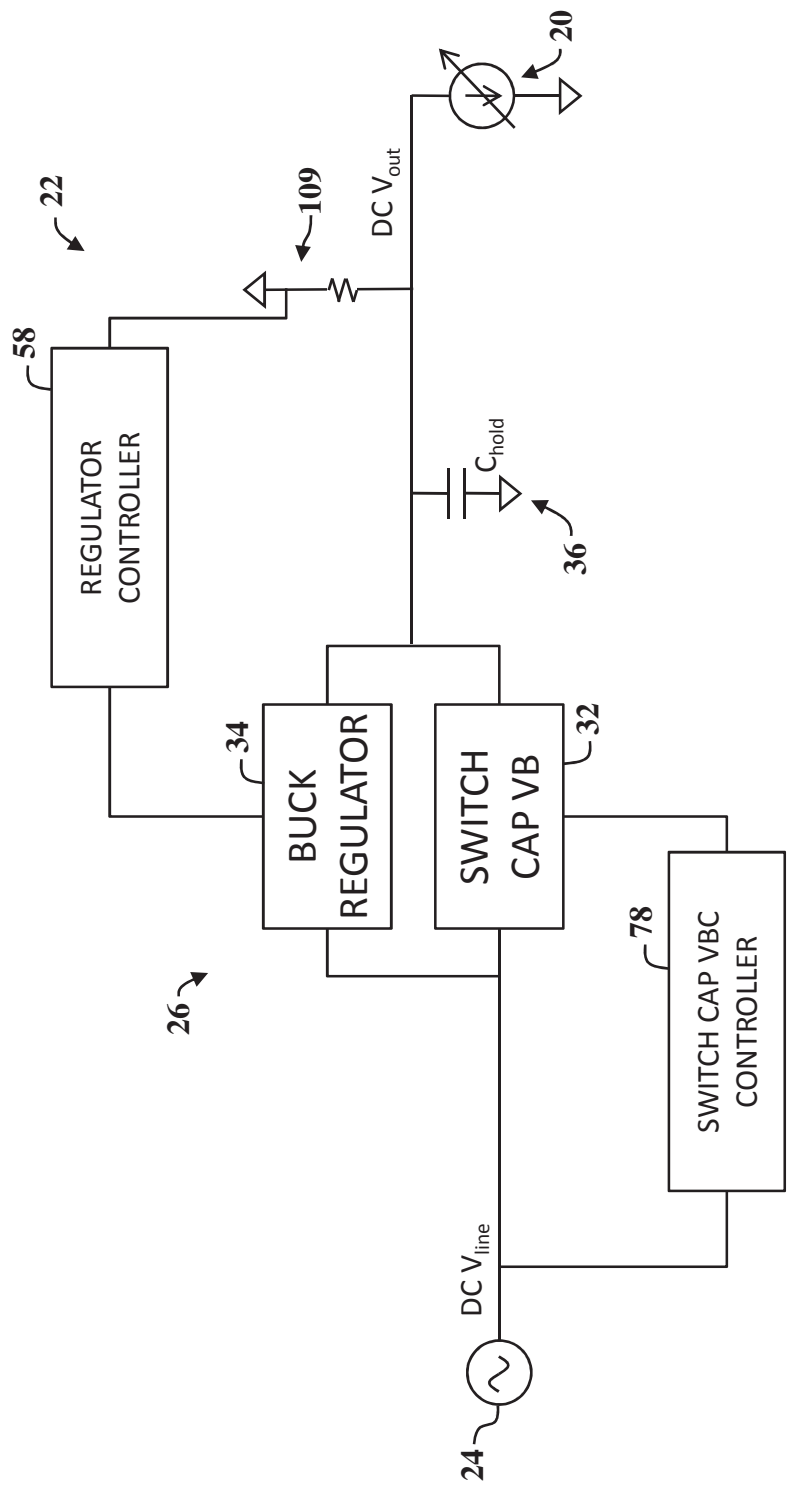


Figure 41