

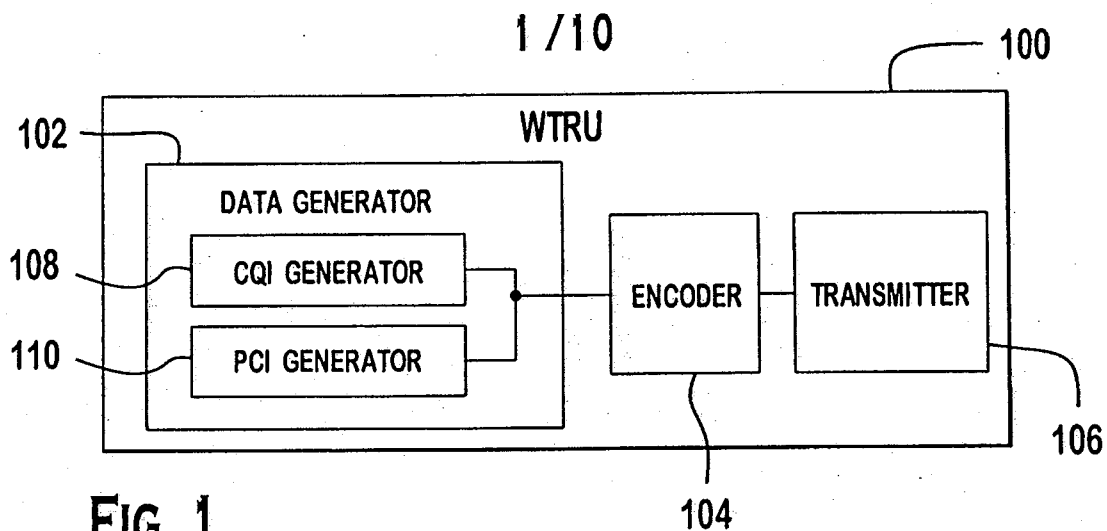


FIG. 1

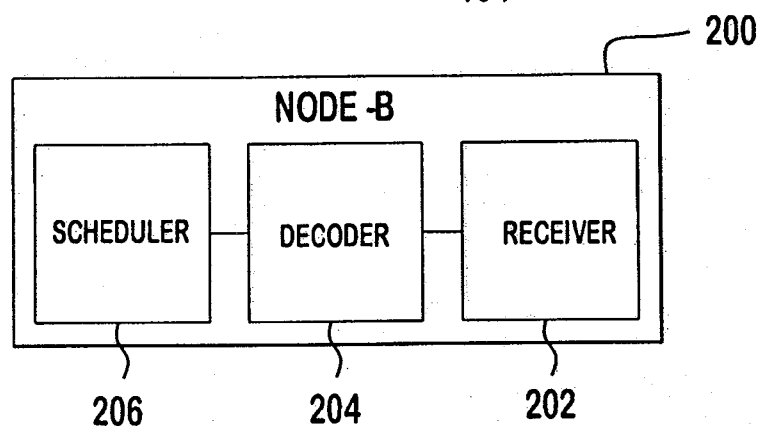


FIG. 2

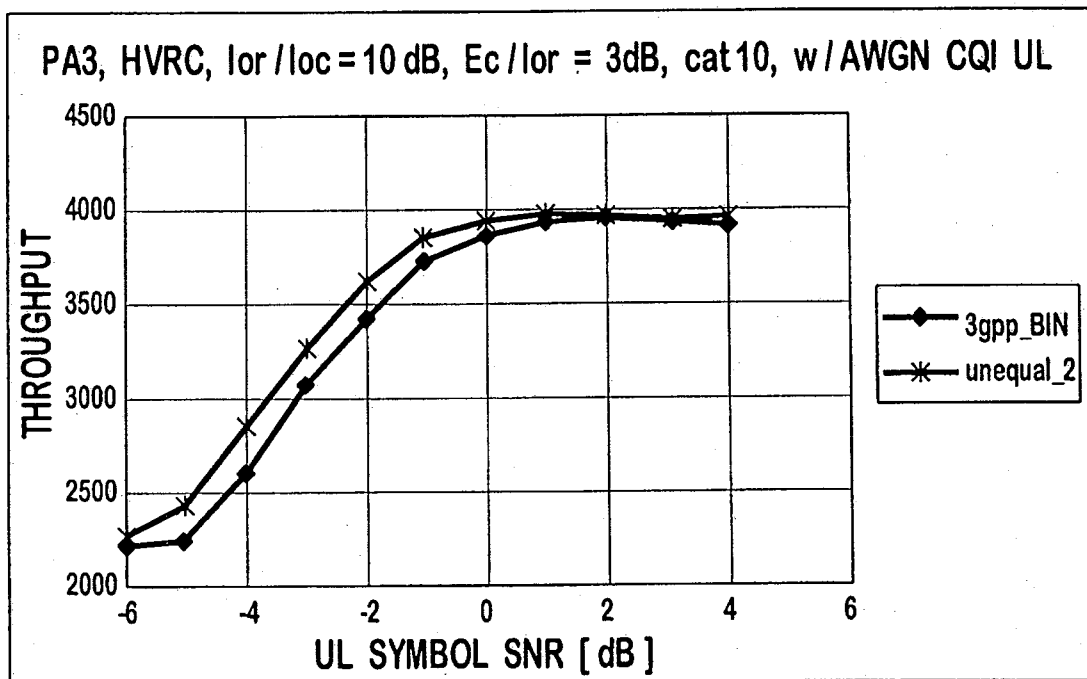


FIG. 3

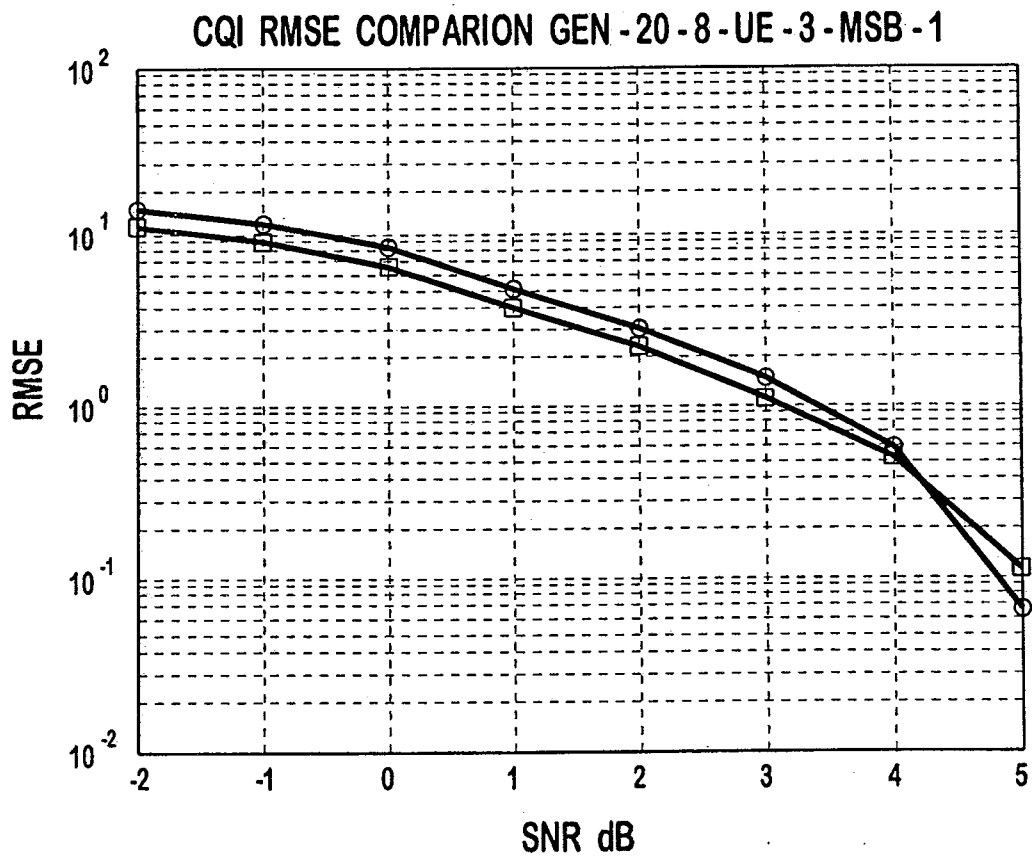


FIG. 4

R7 HS-DPCCH TypeB PCICQI BERs: AWGN, LINEAR BLOCK CODES, UEP

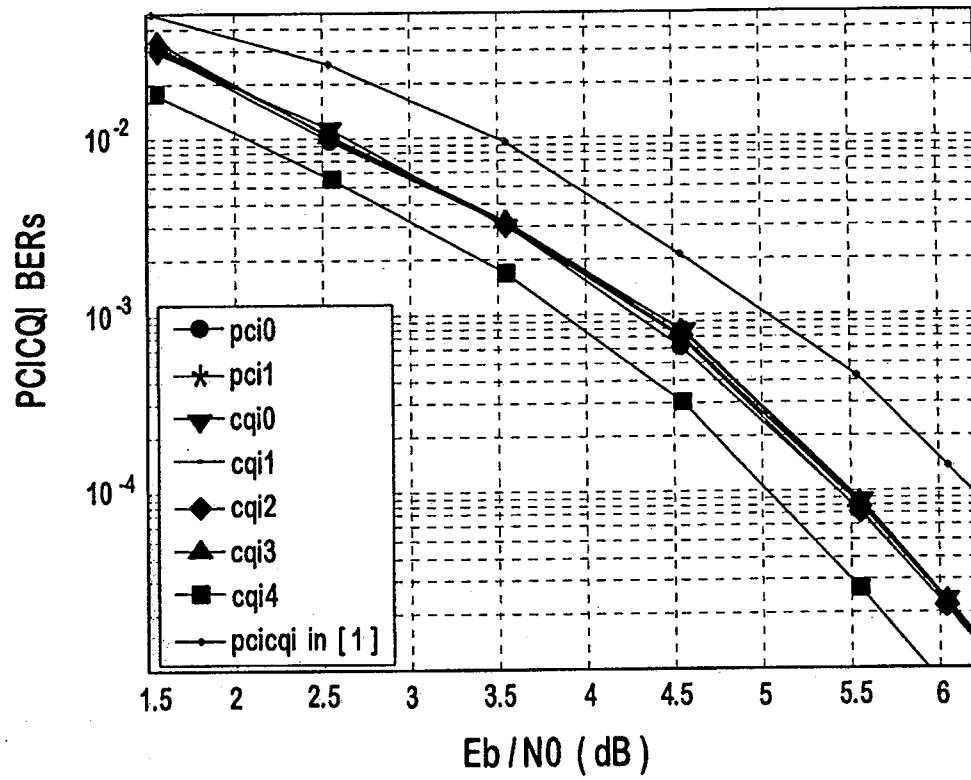


FIG. 5

R7 HS-DPCCH Type B PCICQI BLER: AWGN, LINEAR BLOCK CODECS

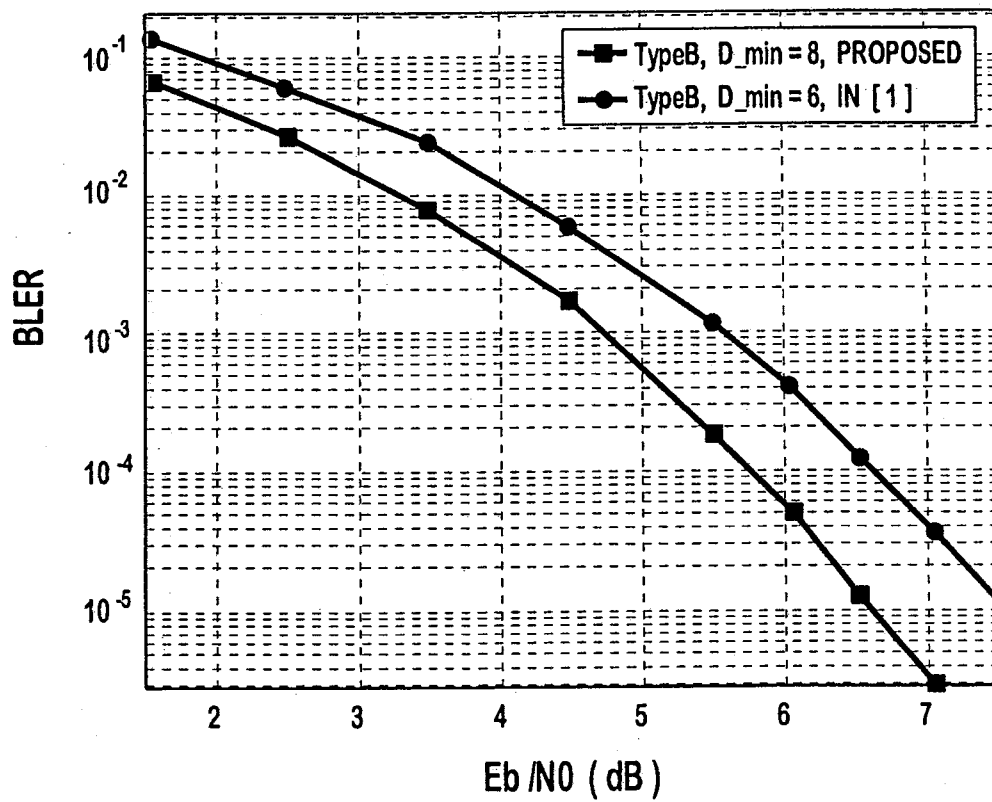


FIG. 6

R7 HS-DPCCH TypeA/B PCICQI BLER: AWGN, LINEAR BLOCK CODES

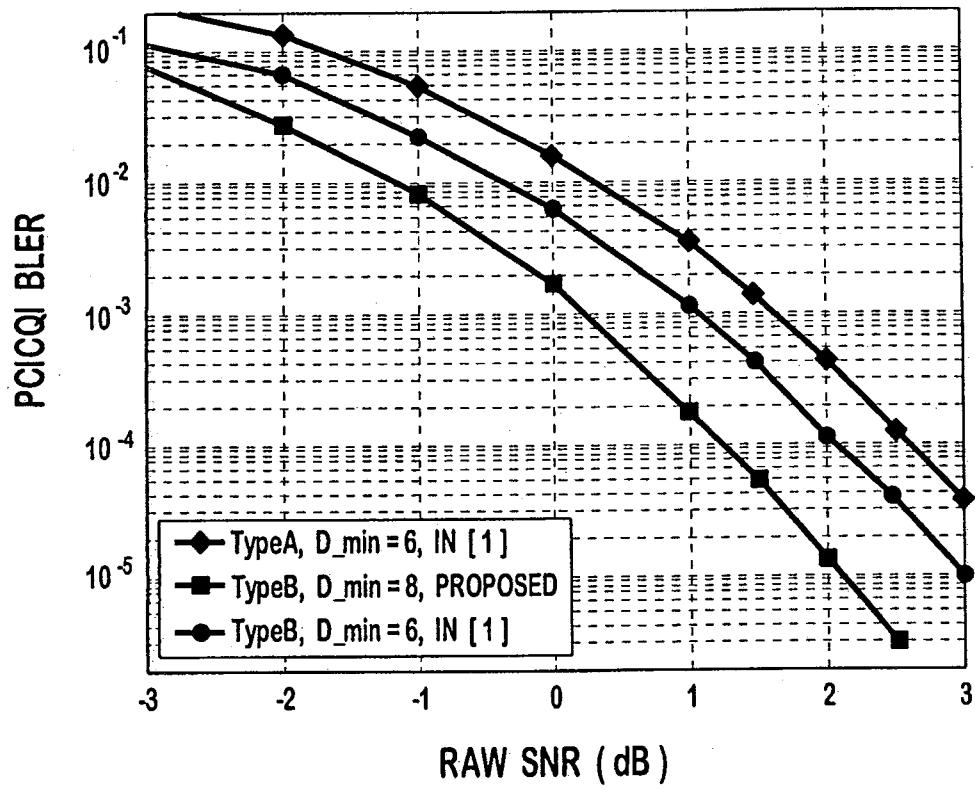


FIG. 7

R7 HS-DPCCH TypeB 5-bit CQI MSEs: AWGN, LINEAR BLOCK CODES

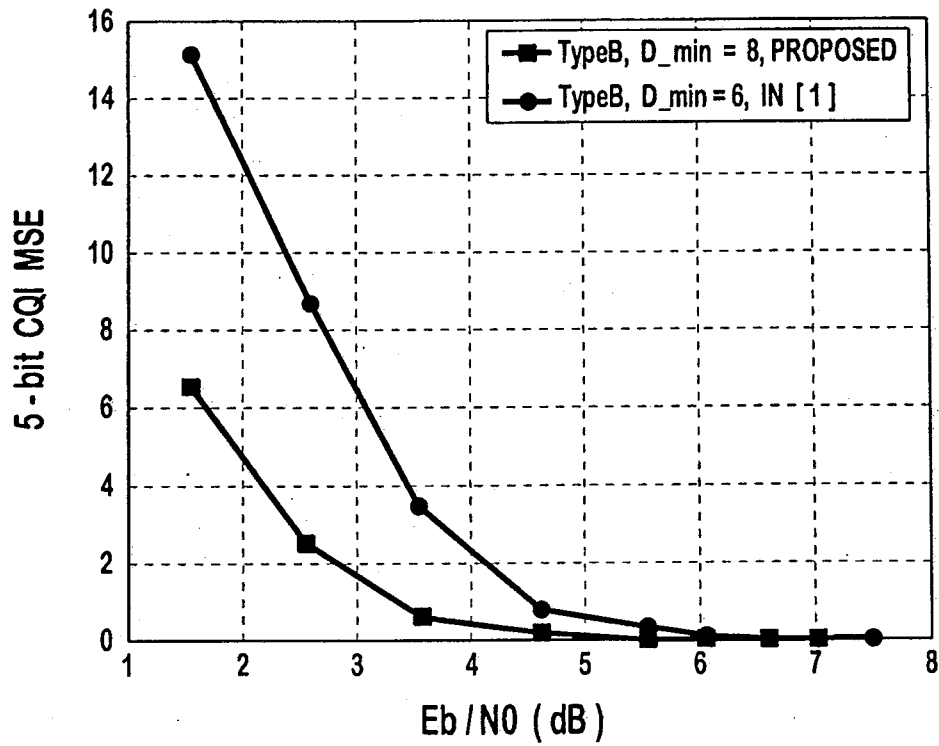


FIG. 8

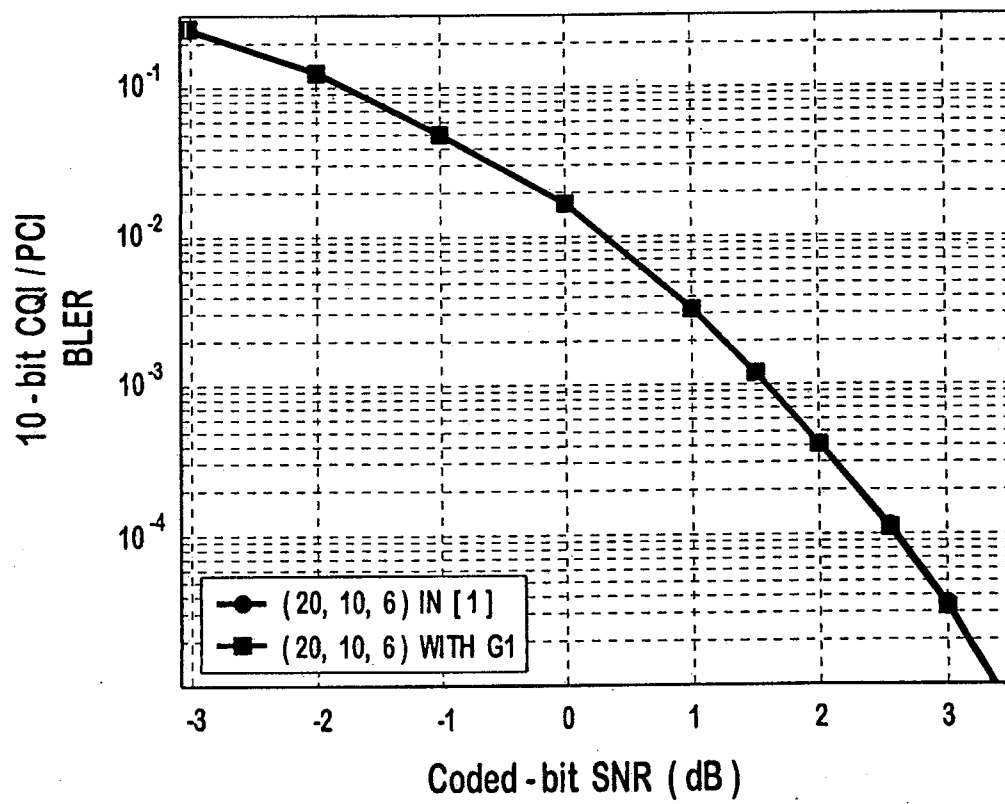


FIG. 9

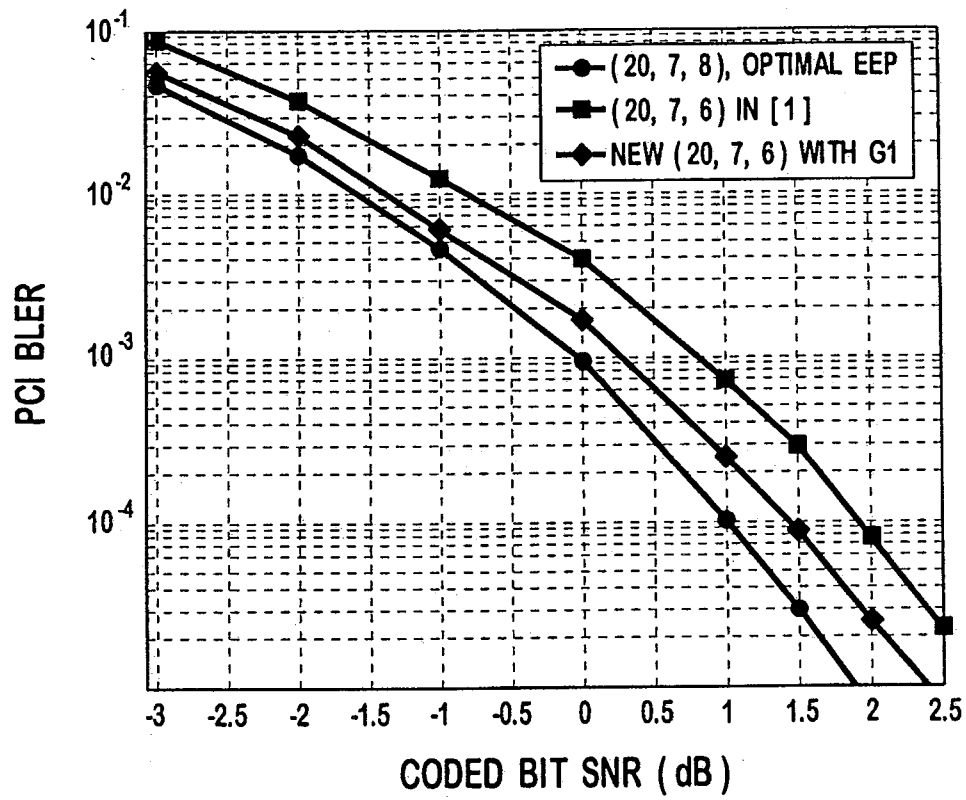


FIG. 10

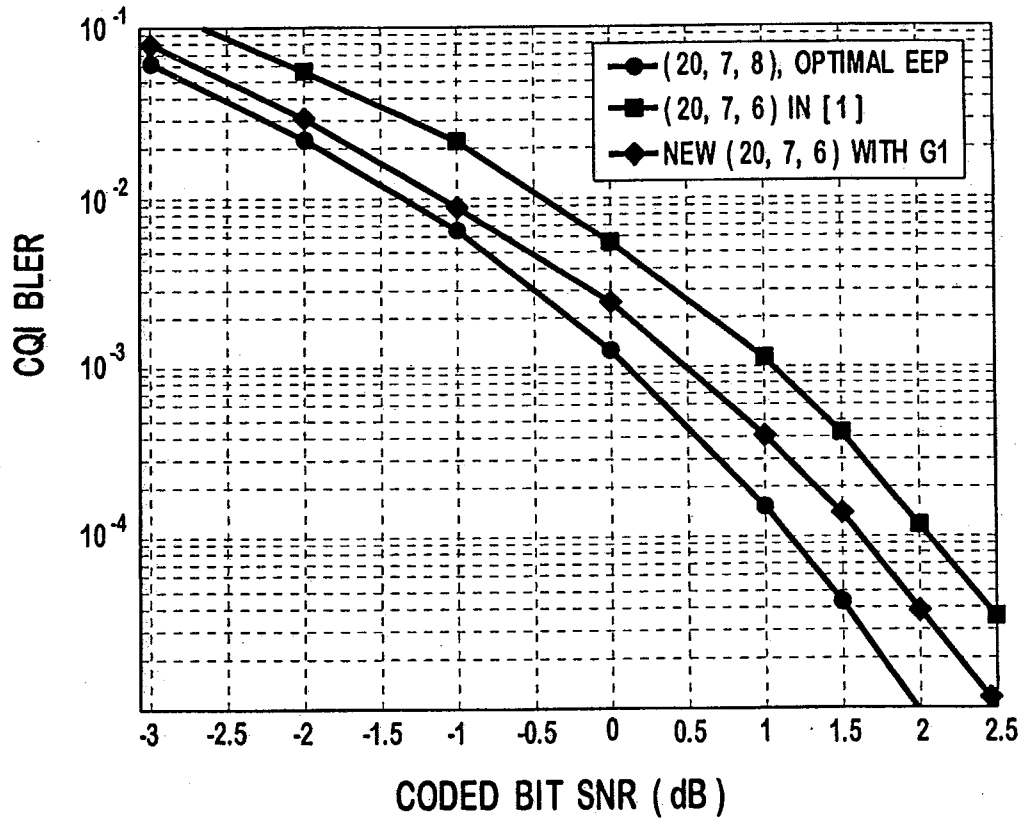


FIG. 11

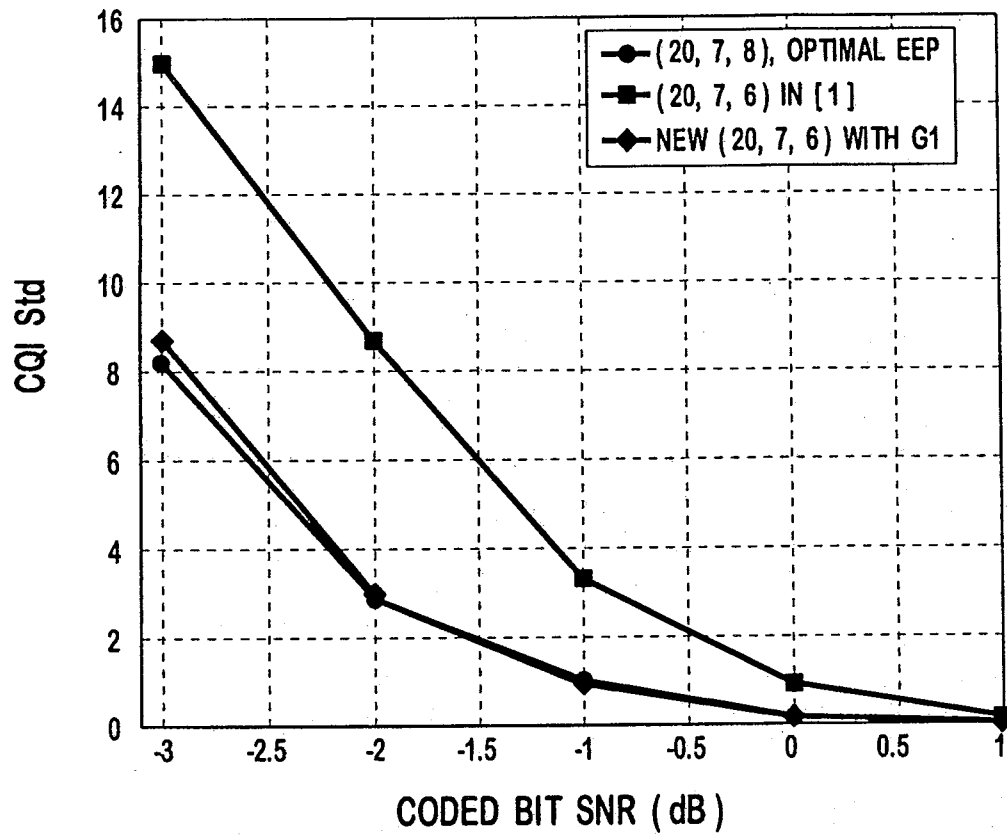


FIG. 12