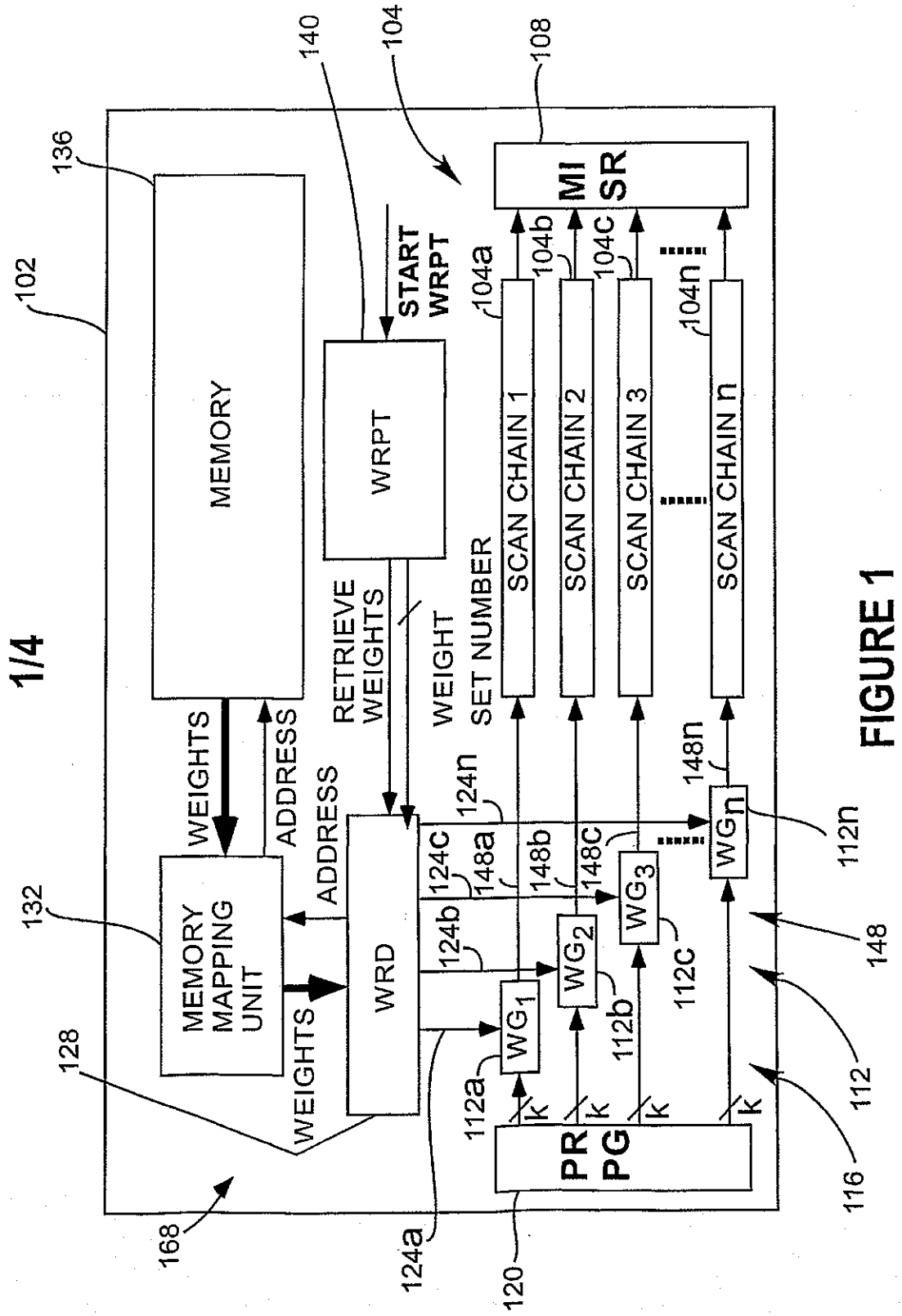

1/4
102
136
MEMORY
140
104
WRPT
START WRPT
108
MI
SR
104a
104b
104c
104n
SCAN CHAIN 1
SCAN CHAIN 2
SCAN CHAIN 3
SCAN CHAIN n
RETRIEVE WEIGHTS
WEIGHT SET NUMBER
WEIGHTS
ADDRESS
132
MEMORY MAPPING UNIT
ADDRESS
WEIGHTS
WRD
124a
124b
124c
124n
148a
148b
148c
148n
WG1
WG2
WG3
WGn
112a
112b
112c
112n
148
112
116
128
168
120
PR
PG
k

FIGURE 1

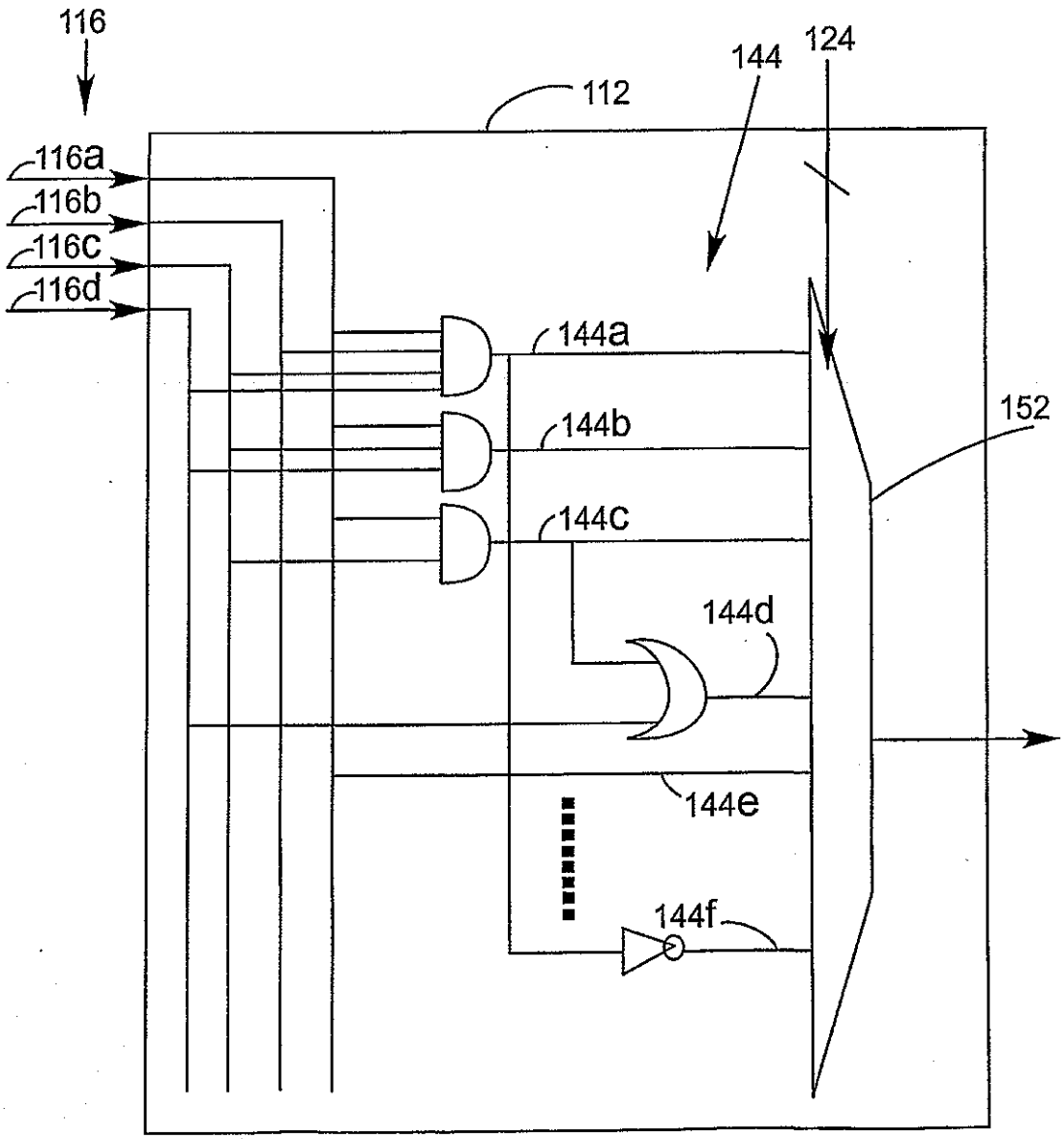

116
144
124
112
116a
116b
116c
116d
144a
144b
144c
144d
144e
144f
152

FIGURE 2

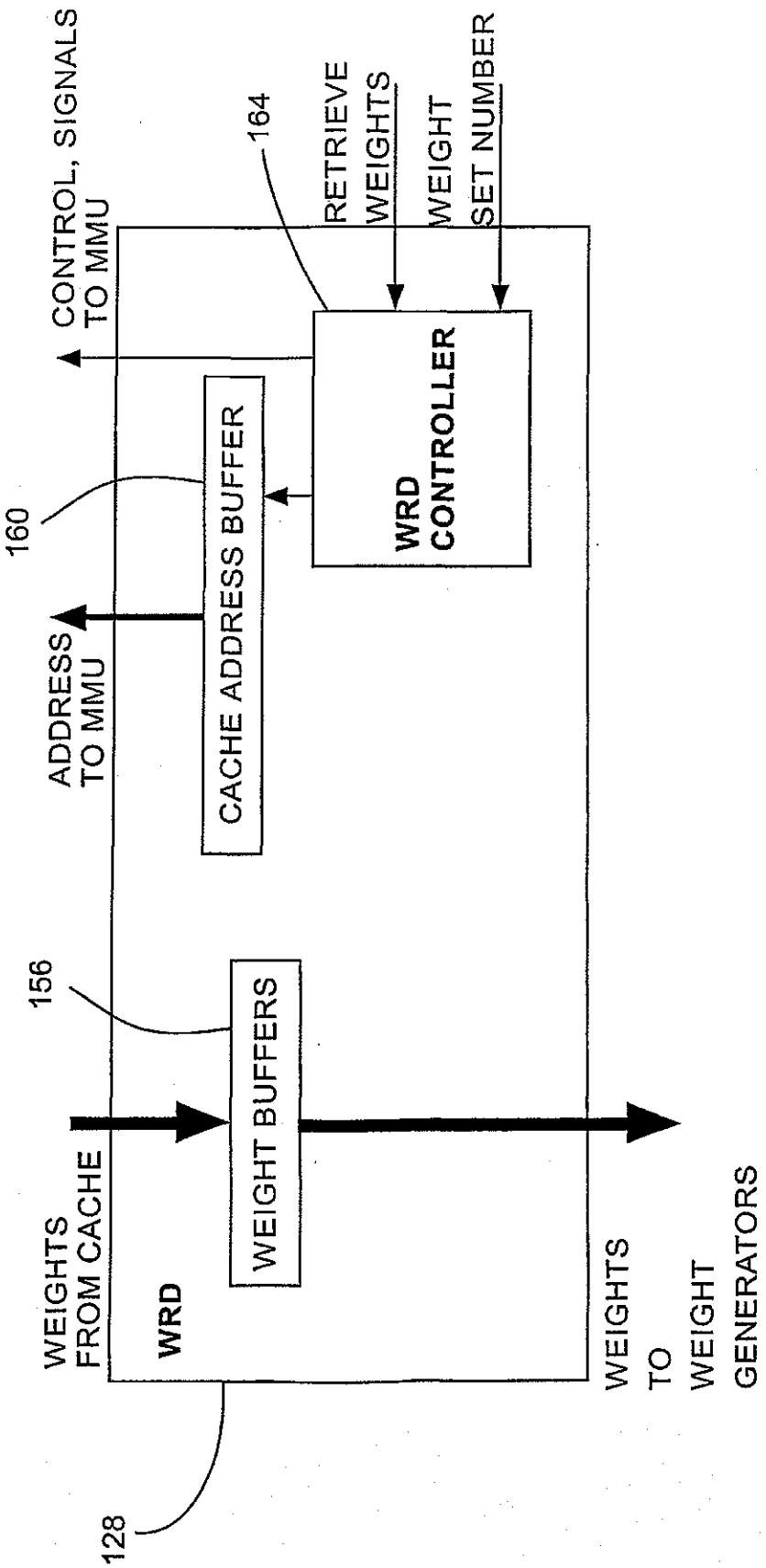

CONTROL, SIGNALS TO MMU
164
RETRIEVE WEIGHTS
WEIGHT SET NUMBER
WRD CONTROLLER
160
CACHE ADDRESS BUFFER
ADDRESS TO MMU
156
WEIGHT BUFFERS
WEIGHTS FROM CACHE
WRD
WEIGHTS TO WEIGHT GENERATORS
128

FIGURE 3

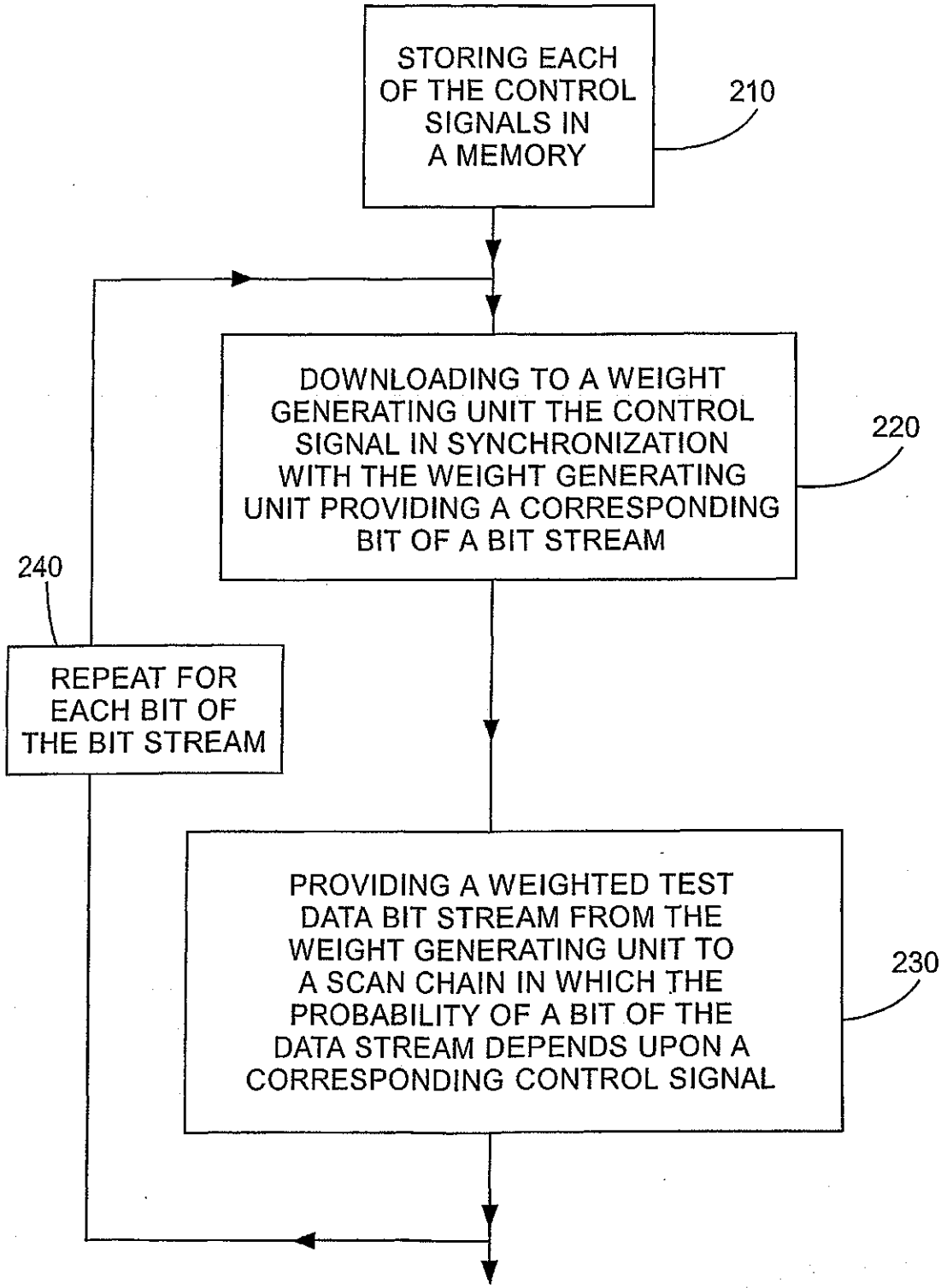

STORING EACH OF THE CONTROL SIGNALS IN A MEMORY
210
DOWNLOADING TO A WEIGHT GENERATING UNIT THE CONTROL SIGNAL IN SYNCHRONIZATION WITH THE WEIGHT GENERATING UNIT PROVIDING A CORRESPONDING BIT OF A BIT STREAM
220
240
REPEAT FOR EACH BIT OF THE BIT STREAM
PROVIDING A WEIGHTED TEST DATA BIT STREAM FROM THE WEIGHT GENERATING UNIT TO A SCAN CHAIN IN WHICH THE PROBABILITY OF A BIT OF THE DATA STREAM DEPENDS UPON A CORRESPONDING CONTROL SIGNAL
230

FIGURE 4