What is claimed is:

1. A pixel collection circuit, comprising:

a light intensity detector which is configured to output in real time a first electric signal representing an intensity of a received optical signal;

a first state storage module, a first input end of which is coupled to the light intensity detector, a first output end of which is coupled to a row request line, a second output end of which is coupled to a column request line, and which is configured to enable the pixel collection circuit to be an initiatively-activated state and store the initiatively-activated state when a change of the first electric signal meets a predetermined condition, transmit a row request signal and/or a column request signal to the row request line and/or the column request line in the initiatively-activated state, and transmit a pulse signal to at least one adjacent pixel collection circuit in the initiatively-activated state;

a second state storage module, a first input end of which is coupled to at least one adjacent pixel collection circuit, an output end of which is coupled to the first state storage module, and which is configured to, upon the receipt of a pulse signal from the at least one adjacent pixel collection circuit, enable the pixel collection circuit to be in a passively-activated state and store the passively-activated state, and transmit a notification to the first state storage module in the passively-activated state, so as to enable the first state storage module to transmit the row request signal and/or the column request signal to the row request line and/or the column request line;

a light intensity signal collection and storage module, a first input end of which is coupled to an output end of the light intensity detector, a second input end of which is coupled to the first state storage module, a third input end of which is coupled to the second state storage module, and which is configured to store and output the first electric signal as a light intensity signal in accordance with the state of the pixel collection circuit; and

a time information storage module, a first input end of which is coupled to a time signal line, a second input end of which is coupled to the first state storage module, a third input end of which is coupled to the second state storage module, and which is configured to store and output a corresponding time signal in accordance with the state of the pixel collection circuit.

2. The pixel collection circuit according to claim 1, further comprising:

a light intensity change amplifier, an input end of which is coupled to the output end of the light intensity detector and which is configured to preprocess the first electric signal so as to generate a second electric signal; and

a dual-threshold filter, an output end of which is coupled to an output end of the light intensity change amplifier, and which is configured to determine whether a change of the second electric signal meets the predetermined condition,

wherein the first state storage module is coupled to the dual-threshold filter and further configured to enable the pixel collection circuit to be in the initiatively-activated state when the change of the second electric signal meets the predetermined condition.

3. The pixel collection circuit according to claim 2, wherein the light intensity signal collection and storage module is further configured to:

sample in real time the first electric signal when the pixel collection circuit is in a nonactivated state; and

after the pixel collection circuit is in an activated state, store the sampled first electric signal until a row response signal has been received from the first state storage module or the second state storage module, and output the stored first electric signal as the light intensity signal.

4. The pixel collection circuit according to claim 3, wherein the time information storage module is further configured to:

sample a first time signal from the time signal line when the pixel collection circuit is in the nonactivated state; and

after the pixel collection circuit is in the activated state, store the sampled first time signal until the row response signal has been received from the first state storage module or the second state storage module, and output the stored first time signal as the time signal.

5. The pixel collection circuit according to claim 4, wherein a second input end of the first state storage module is coupled to a row response line and a third input end of the first state

storage module is coupled to a column response line, and the first state storage module is further configured to enable the pixel collection circuit to be deactivated upon the receipt of a~~the~~ row response signal from the row response line and a column~~row~~ response signal from~~form~~ the column response line simultaneously; and

a third output end of the first state storage module is coupled to the light intensity change amplifier, and the first state storage module is further configured to transmit a resetting signal to the light intensity change amplifier when the pixel collection circuit is in the activated state or in a deactivated state.

6. The pixel collection circuit according to claim 5, wherein the first state storage module includes a first latch configured to be set when a change of a~~the~~ second electric signal meets the predetermined condition, and reset when the row response signal and the column response signal have been received simultaneously,

wherein the first state storage module is further configured to transmit the resetting signal to the light intensity change amplifier when the first latch has been set or reset.

7. The pixel collection circuit according to claim 6, wherein a second input end of the second state storage module is coupled to the row response line and a third input end of the second state storage module is coupled to the column response line, and the second state storage module is further configured to enable the pixel collection circuit to be deactivated upon the receipt of the row response signal from the row response line and the column response signal from the column response line simultaneously,

wherein the second state storage module is further configured to transmit a resetting signal to the light intensity change amplifier through the first state storage module when the pixel collection circuit is in the deactivated state.

8. An optical flow sensor, comprising:

a pixel collection circuit array comprising a plurality of pixel collection circuits according to claim 7;

a pixel row signal communication unit configured to respond to each row request signal from the pixel collection circuit array, and output a row address of a row which has responded to the row request signal;

a pixel column signal communication unit configured to respond to each column request signal from the pixel collection circuit array, and output a column address of a column which has responded to the column request signal, a corresponding light intensity signal, and a corresponding time signal;

a time control unit configured to output a first time signal via a time signal line to the pixel collection circuit array; and

a central adjustment and control unit configured to control the output of the row address, the column address, the time signal and the light intensity signal.

9. The optical flow sensor according to claim 8, wherein the pixel row signal communication unit comprises:

a row signal response communication module configured to receive the row request signal from at least one pixel collection circuit in the pixel collection circuit array, and output a row response signal with respect to one of the row request signals; and

a row address collection unit configured to output encoding information about the row address of a row which has responded to the row request signal.

10. The optical flow sensor according to claim 9, wherein the pixel column signal communication unit comprises:

a column signal response communication module configured to receive the column request signal from at least one pixel collection circuit in the pixel collection circuit array, and output a column response signal with respect to one of the column request signals;

a column address collection module configured to output encoding information about the column address of a column which has responded to the row request signal; and

a column output selection control module configured to control an output sequence of the light intensity signal, the time signal and the column address.