

ABSTRACT
HIGH FREQUENCY CMOS DIFFERENTIAL AMPLIFIERS WITH FULLY
COMPENSATED LINEAR-IN- δ B VARIABLE GAIN CHARACTERISTIC

A high frequency CMOS differential amplifier which comprises: a variable gain amplifier which amplifies differential input; a temperature sensing circuit; a gain-slope correction circuit which produces an intermediate control voltage as a function of temperature, thereby compensating for a change in slope of the gain control characteristics with temperature of the variable gain amplifier; a gain compensation circuit which is used to correct temperature/process variations of MOS transistors in high frequency differential amplifiers; and a bias control circuit.

(Fig.1 is suggested for publication.)



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