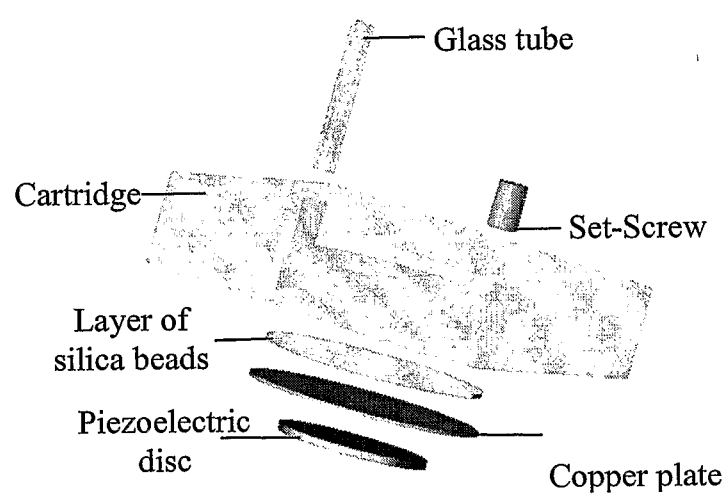
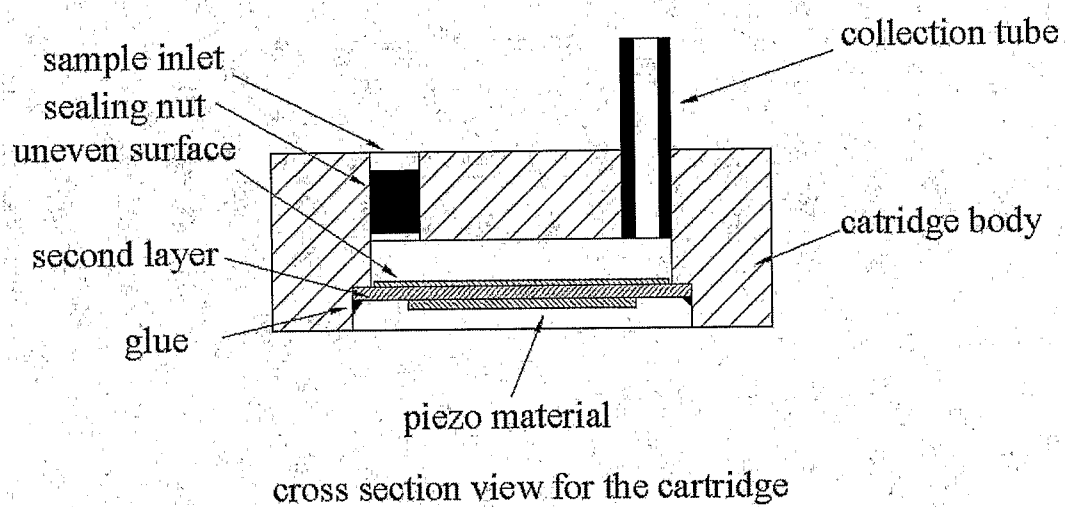


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FIGURE 1A

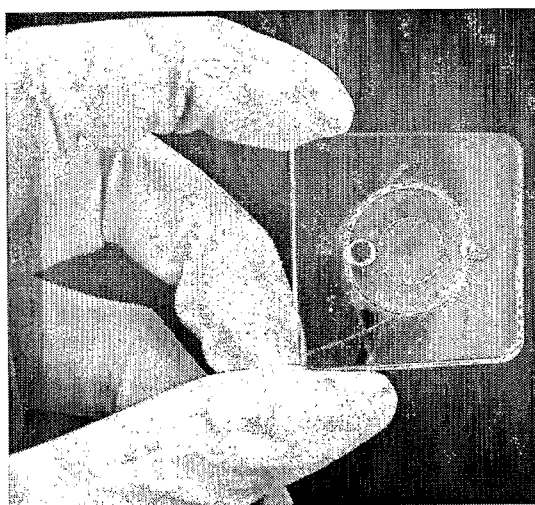


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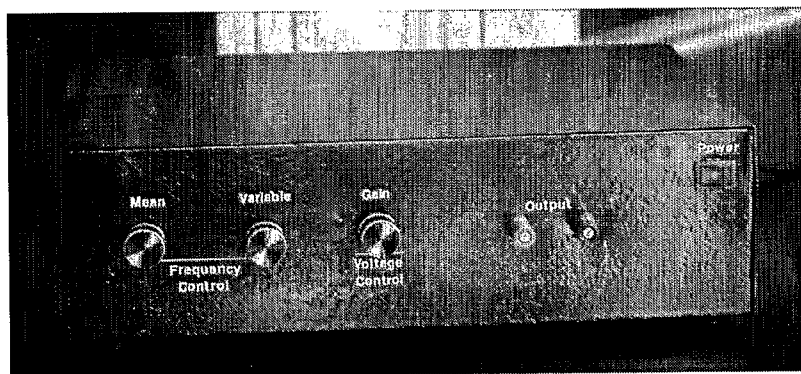
FIGURE 1B

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FIGURE 2

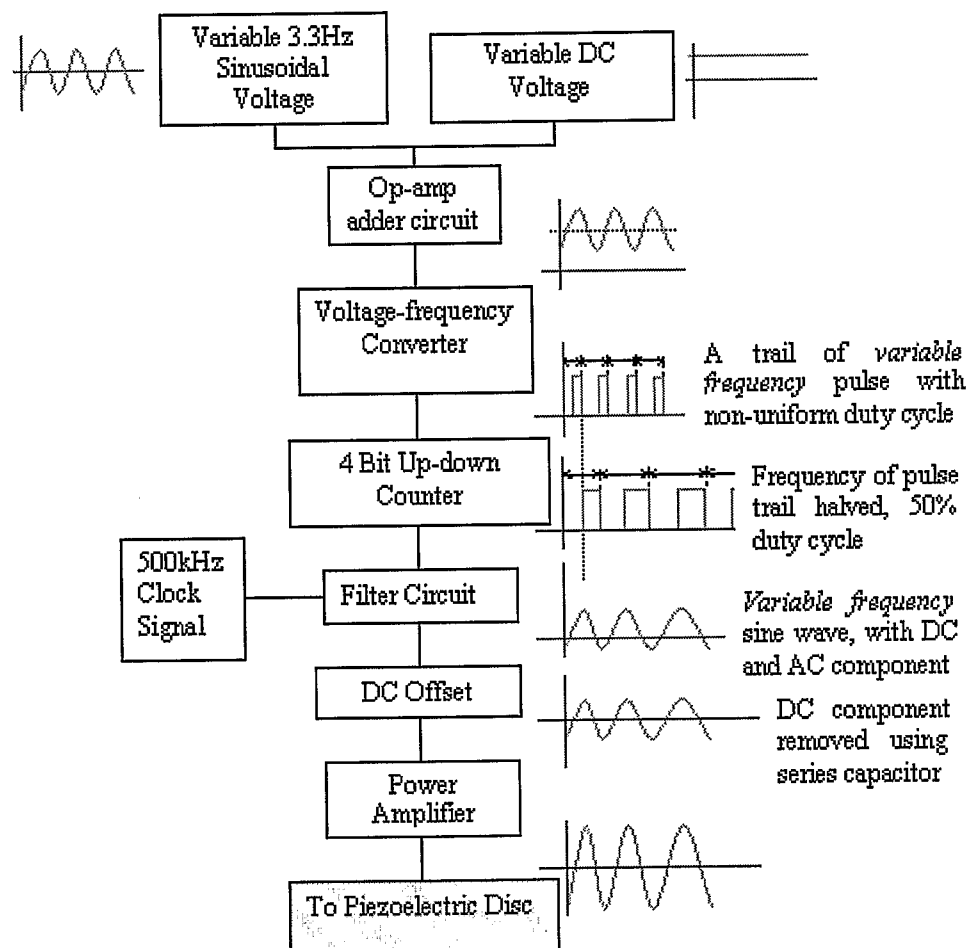


(A)



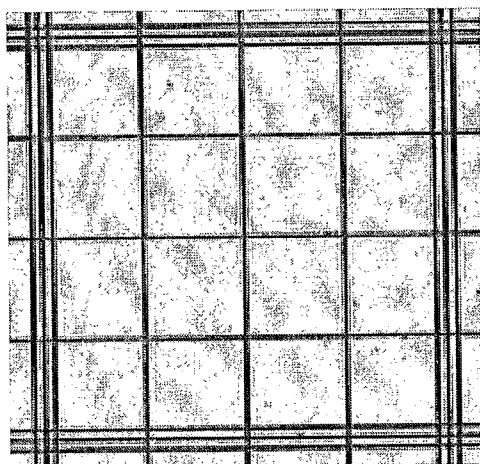
(B)

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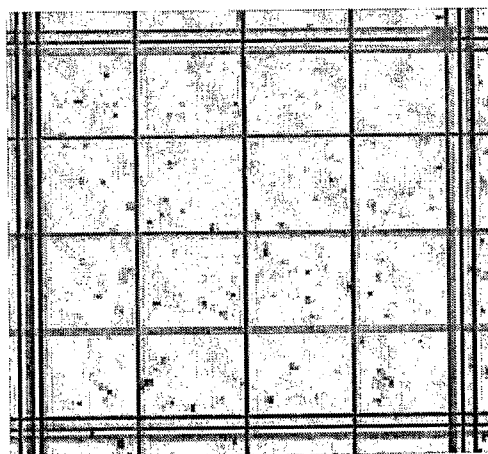
FIGURE 3

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FIGURE 4

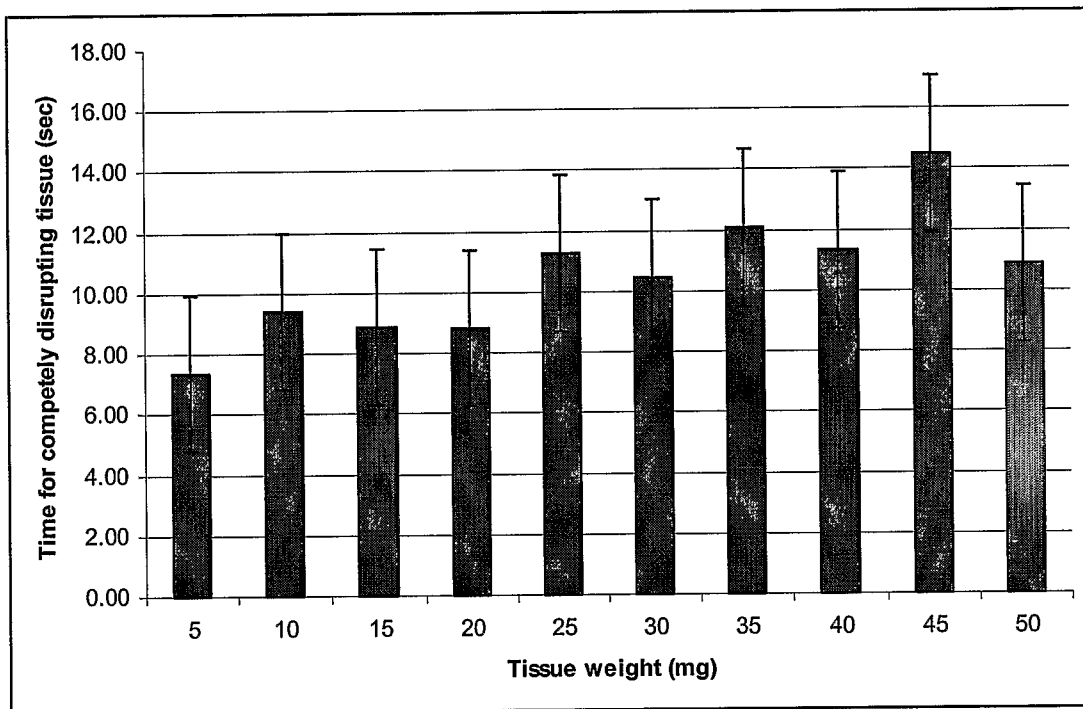


(A)



(B)

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FIGURE 5

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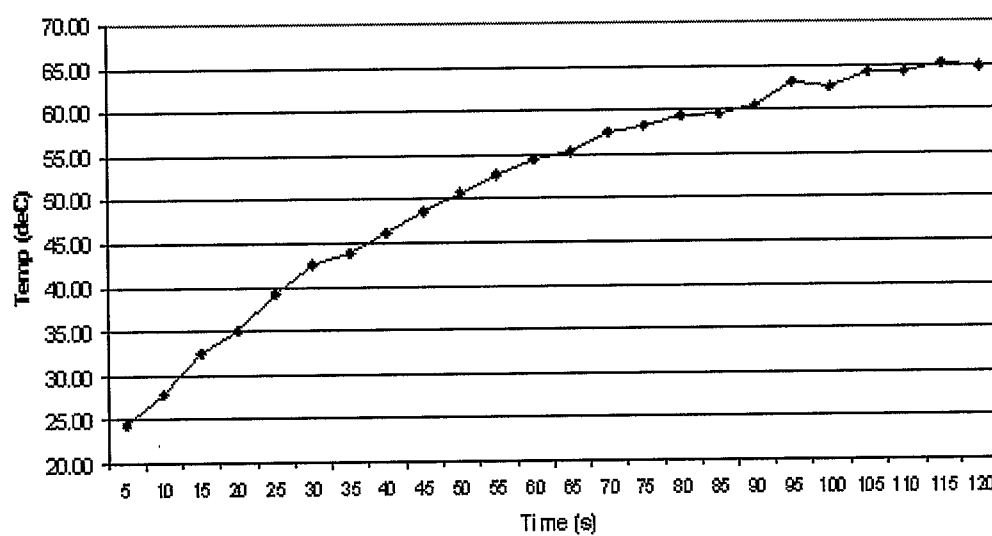
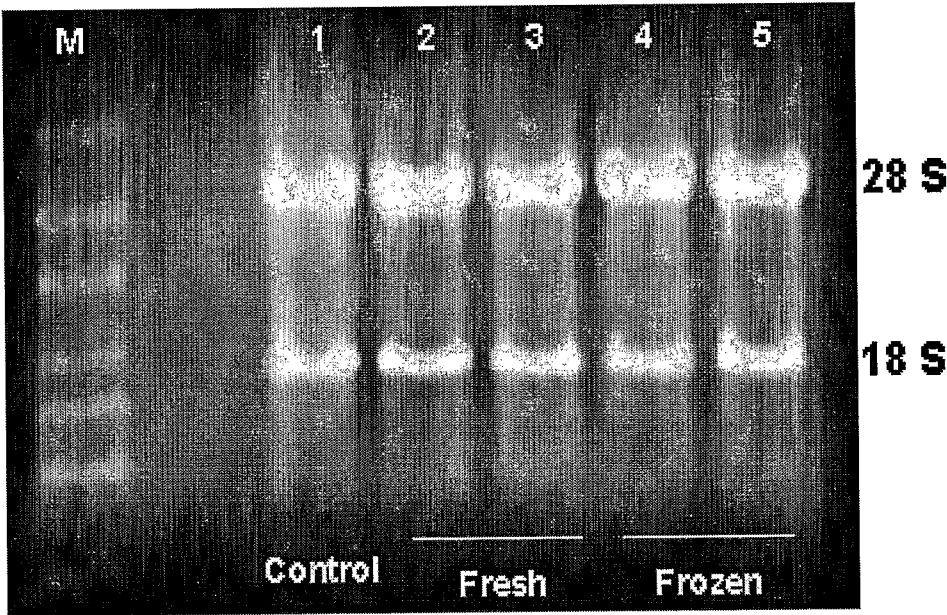
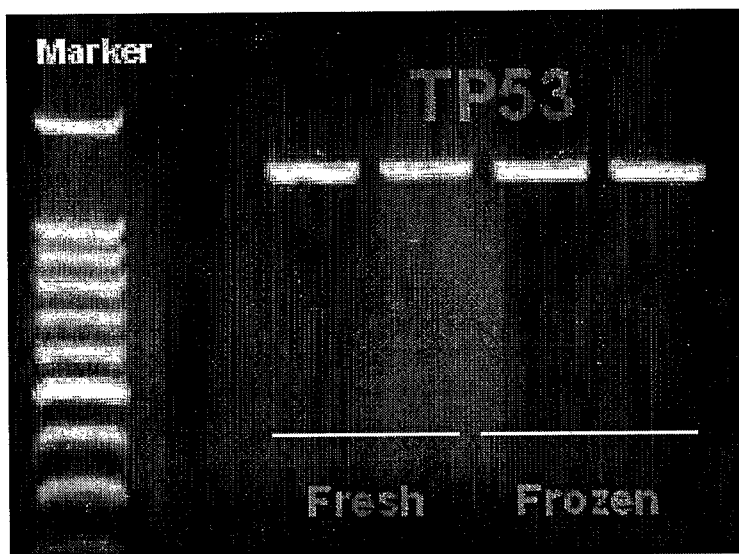
FIGURE 6

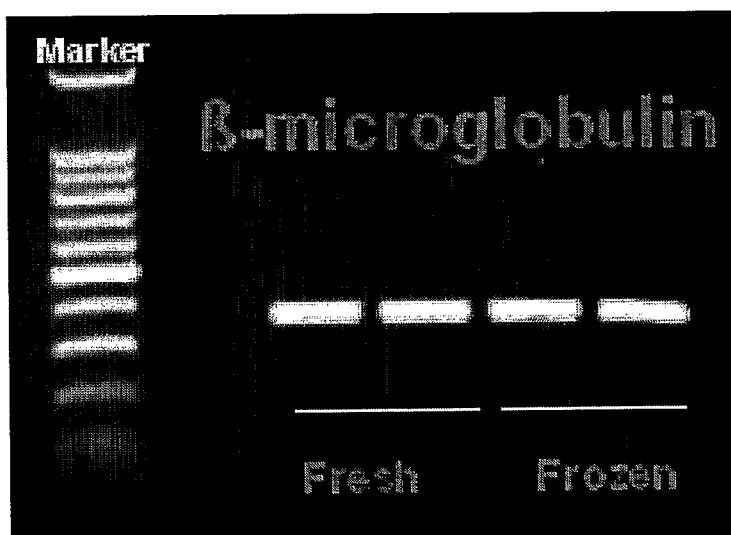
FIGURE 7



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FIGURE 8

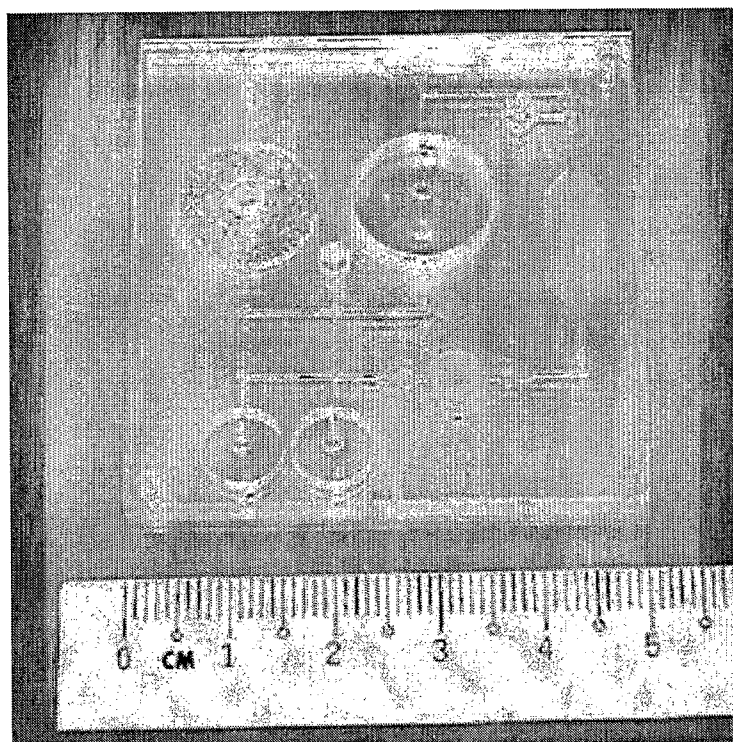
(A)



(B)

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FIGURE 9



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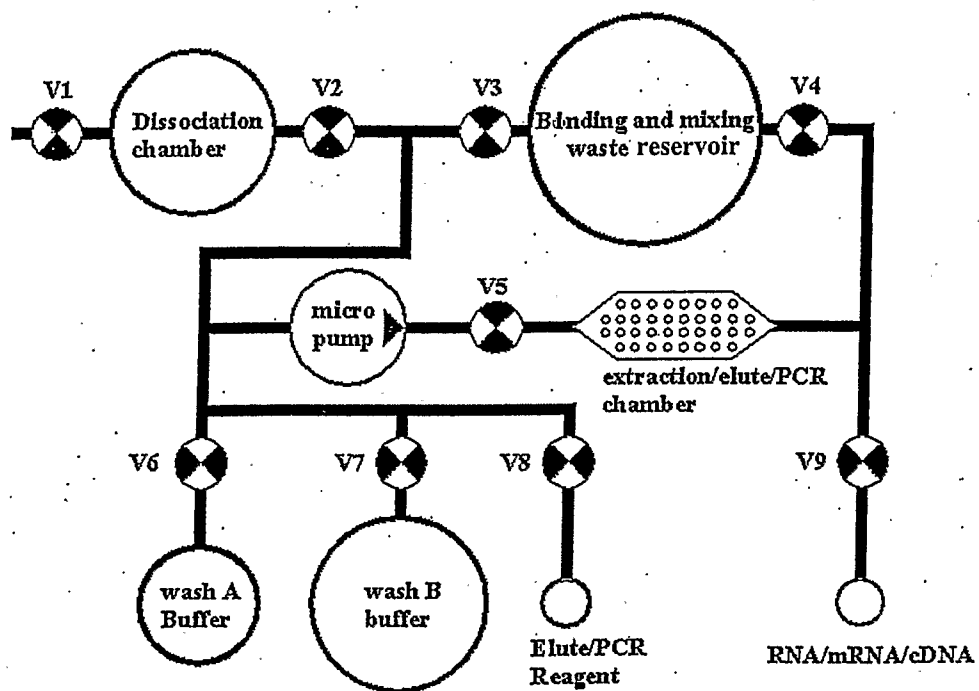
FIGURE 10

FIGURE 11A

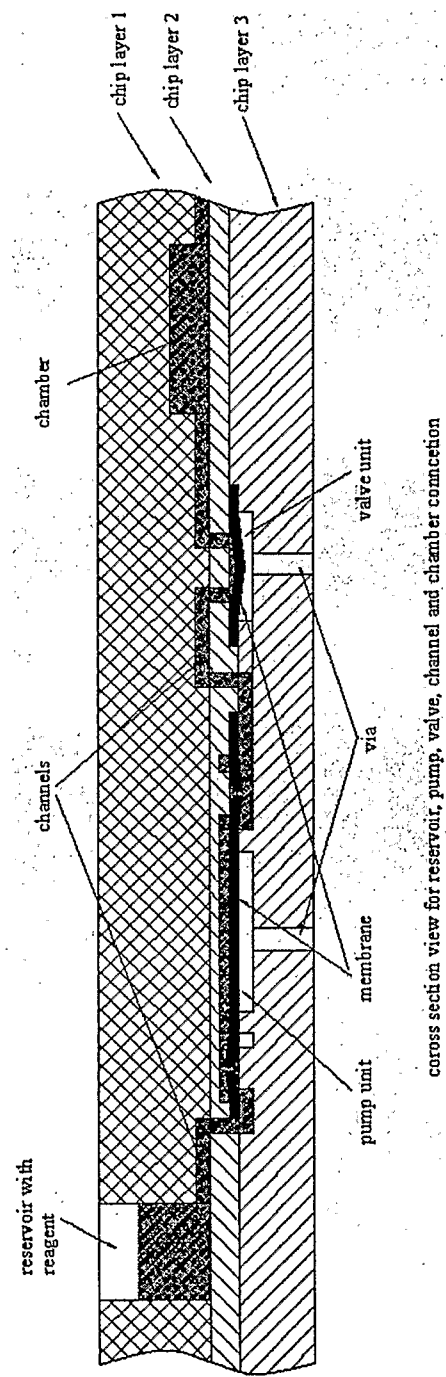
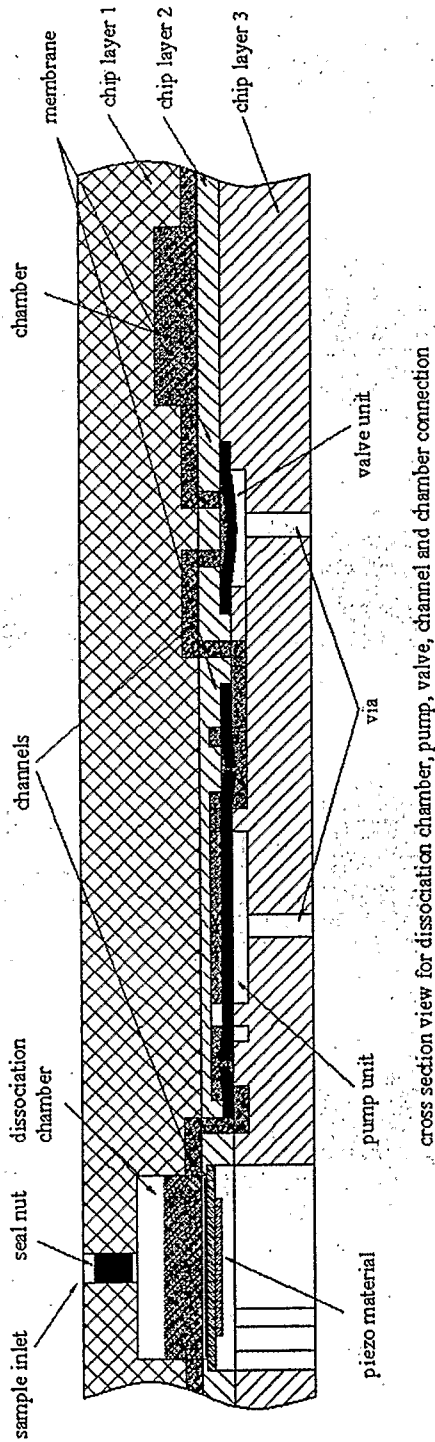
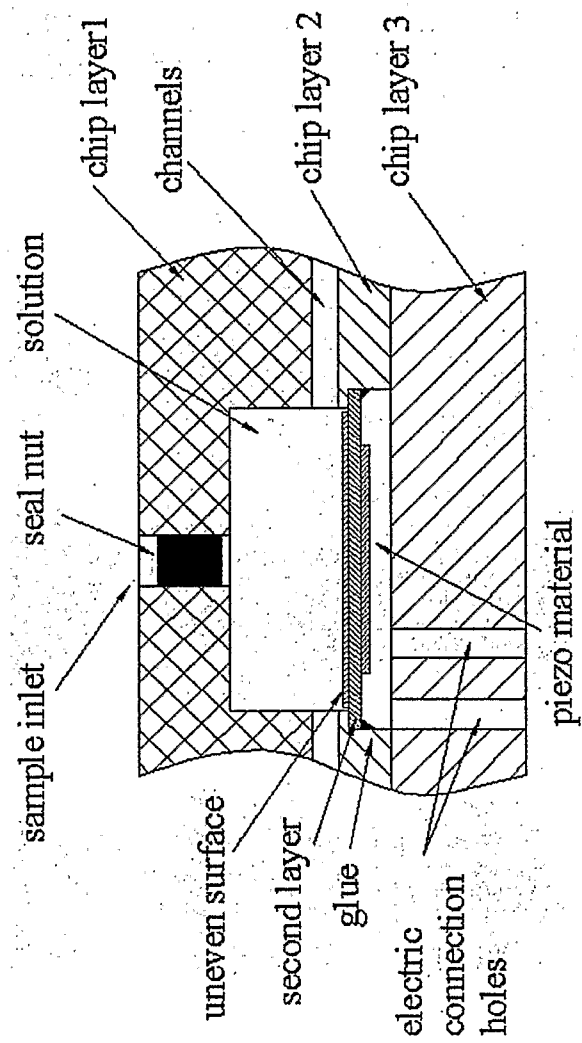


FIGURE 11B

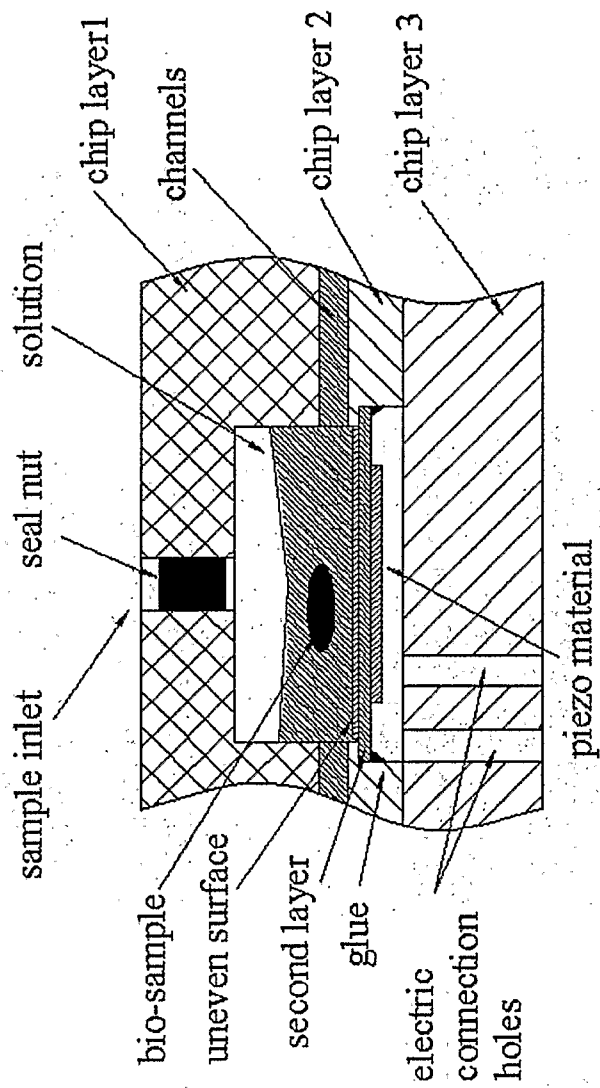


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FIGURE 12A

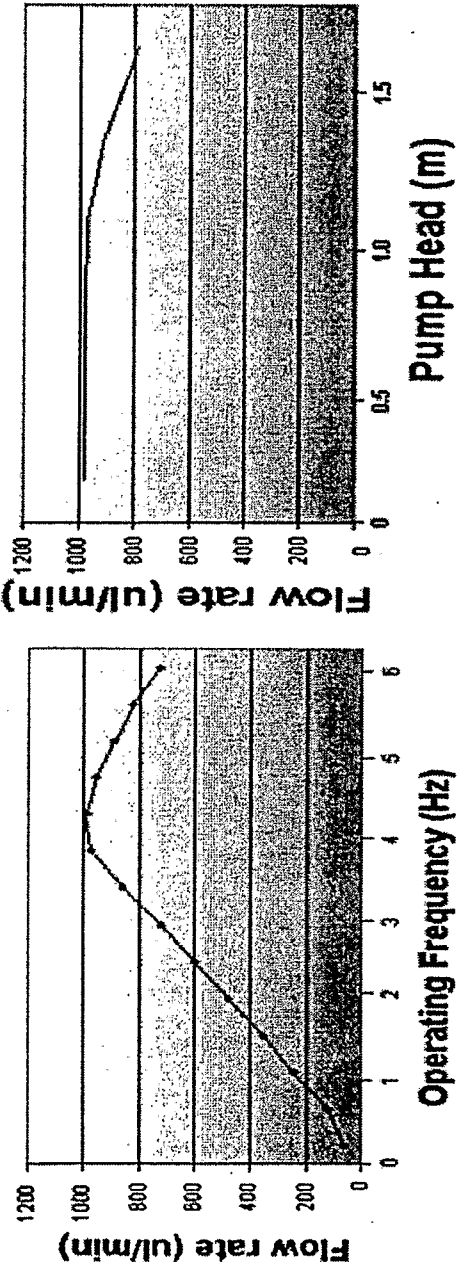
detail of the dissociation chamber integrated in the chip
(without sample inside)

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FIGURE 12B

detail of the dissociation chamber integrated in the chip

FIGURE 13



(B)

(A)

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FIGURE 14