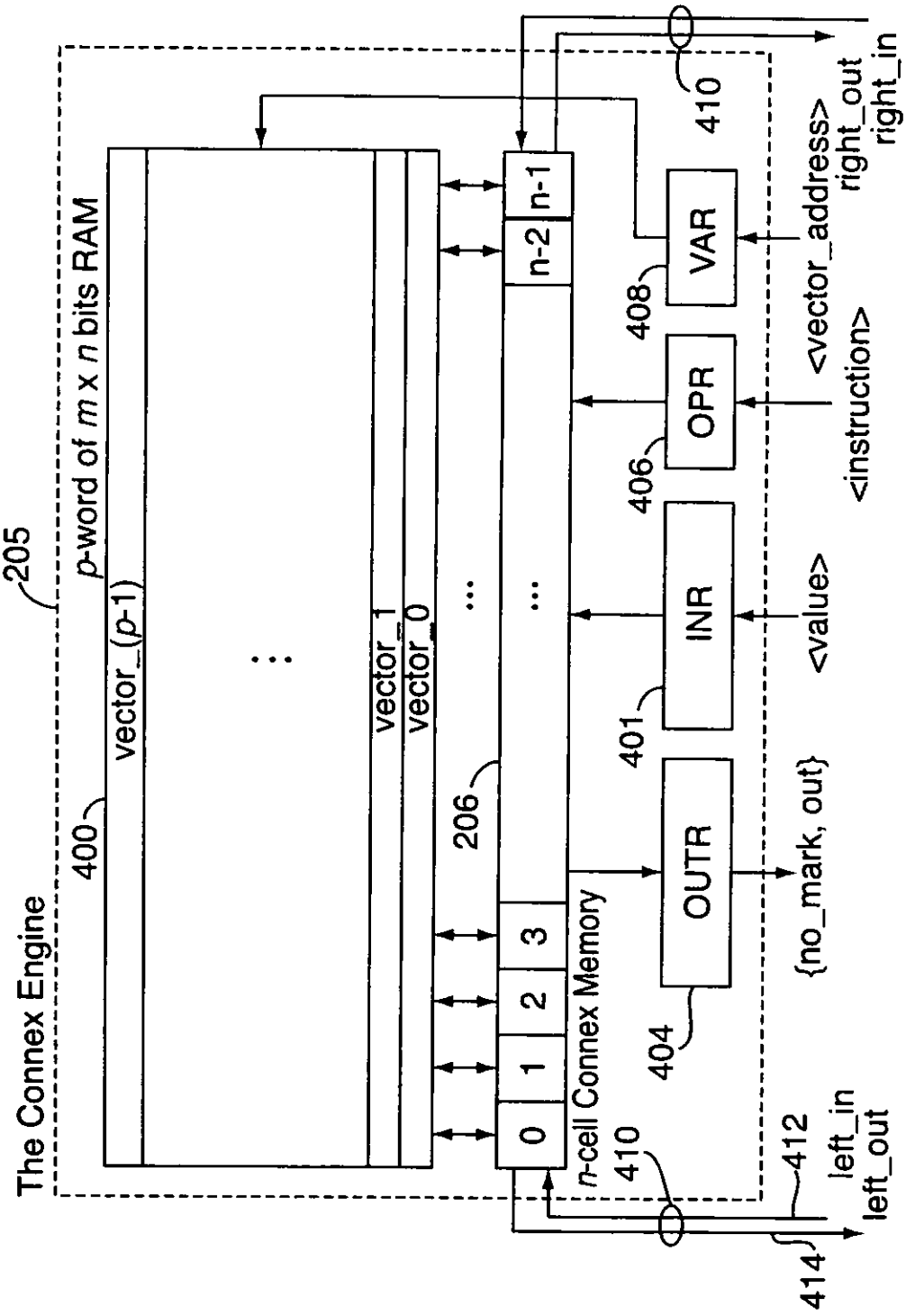

The Connex Engine
205
p-word of m x n bits RAM
vector_(p-1)
vector_1
vector_0
400
206
n-cell Connex Memory
0
1
2
3
n-2
n-1
OUTR
INR
OPR
VAR
401
404
406
408
410
412
414
{no_mark, out}
<value>
<instruction>
<vector_address>
right_out
right_in
left_in
left_out

FIG. 18

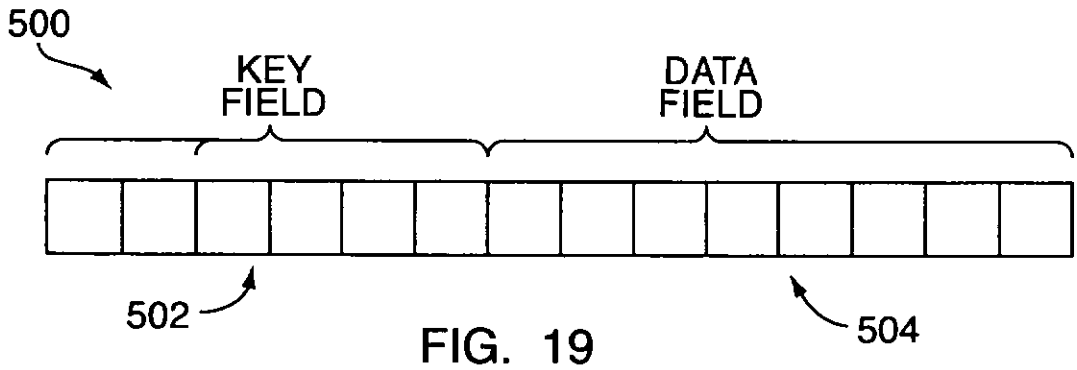

500
KEY FIELD
DATA FIELD
502
504

FIG. 19

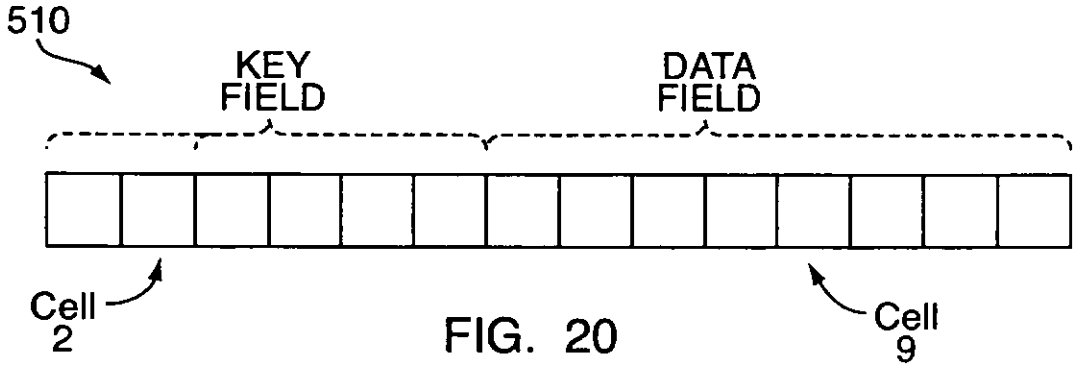

510
KEY FIELD
DATA FIELD
Cell 2
Cell 9

FIG. 20