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(54) Title: METHOD AND APPARATUS FOR LOW LATENCY COMMUNICATION IN AN AUTOMATIC TESTING SYSTEM

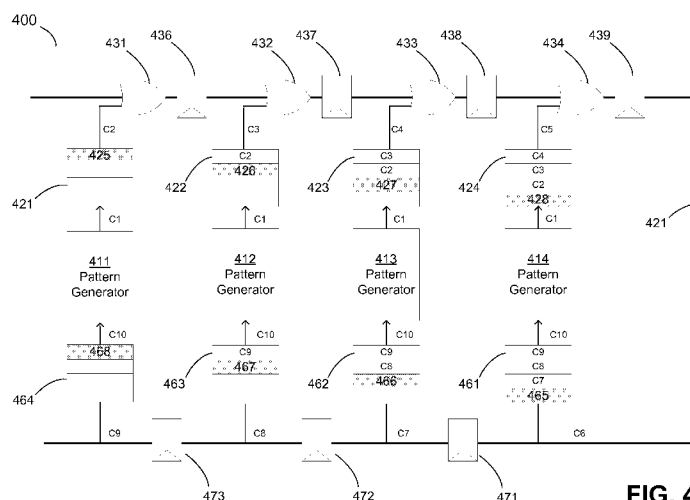


FIG. 4

(57) Abstract: According to some aspects, a system and method for processing messages in a plurality of successive cycles is provided. One such system comprises a plurality of first circuits, each first circuit configured to output a message, the plurality of first circuits configured to operate synchronously, a first plurality of buffers, each buffer associated with a respective first circuit and configured to store a message output by the respective first circuit, a communication path configured to receive the plurality of messages from the buffers and to perform aggregation of the messages, thereby generating an aggregated indication, and one or more second circuits. The one or more second circuits are configured to operate synchronously and to receive the aggregated indication, wherein buffers of the first plurality of buffers are configured to store messages from respective first circuits for different times.