

FIG.1

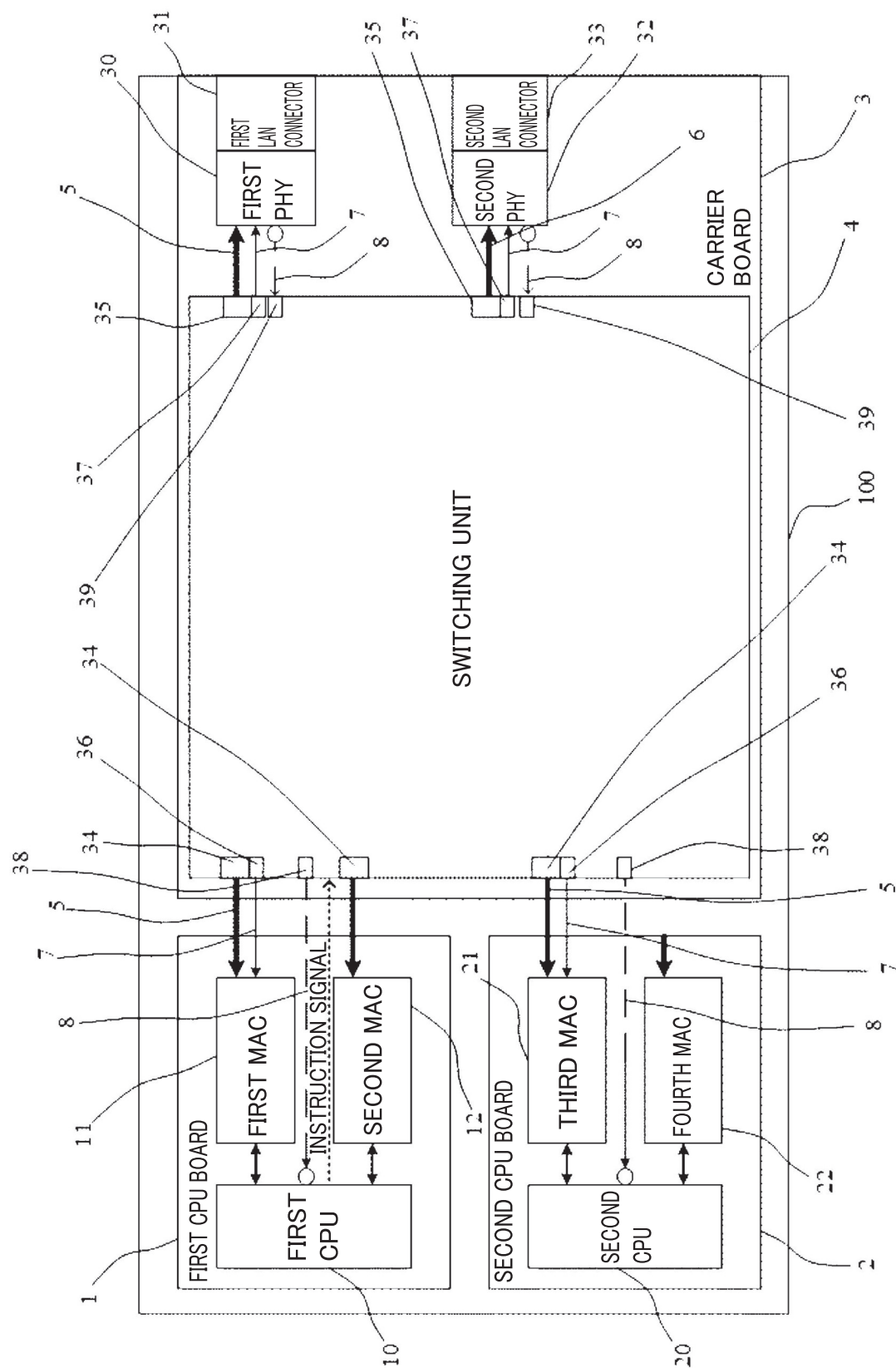


FIG.2

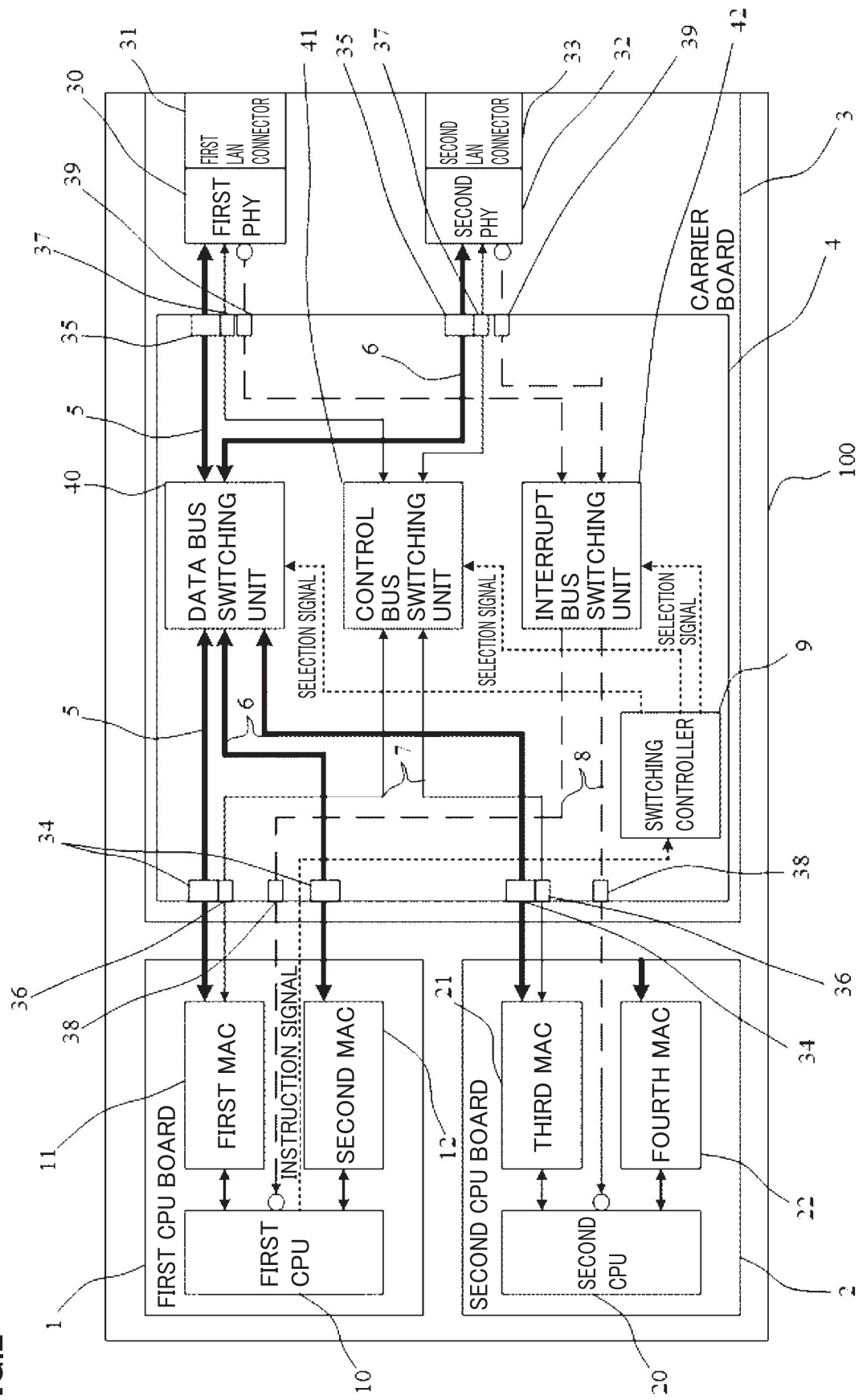


FIG.3

SELECTION SIGNAL	CONNECTION OF MAC-PHY			
	FIRST CPU BOARD 1		SECOND CPU BOARD 2	
	FIRST MAC 11	SECOND MAC 12	THIRD MAC 21	FOURTH MAC 22
0	FIRST PHY 30	SECOND PHY 32	UNCONNECTED	UNCONNECTED
1	FIRST PHY 30	UNCONNECTED	SECOND PHY 32	UNCONNECTED

FIG.4

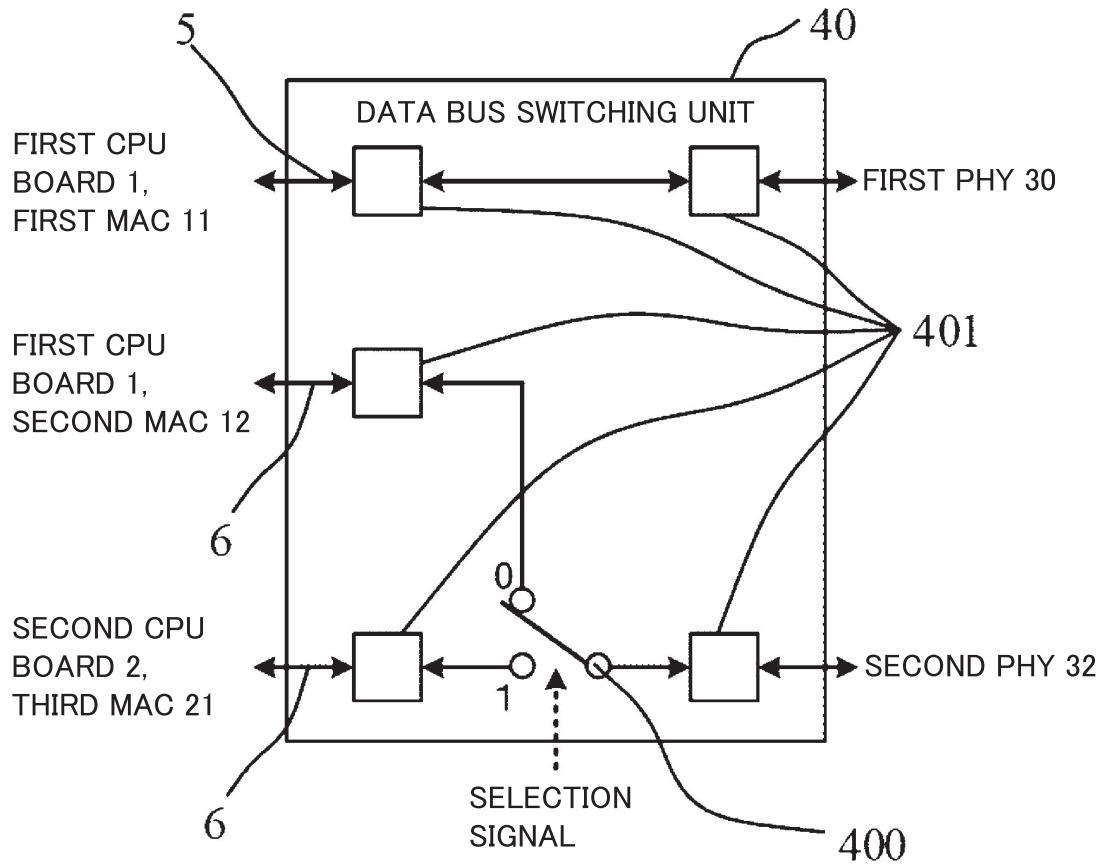


FIG.5

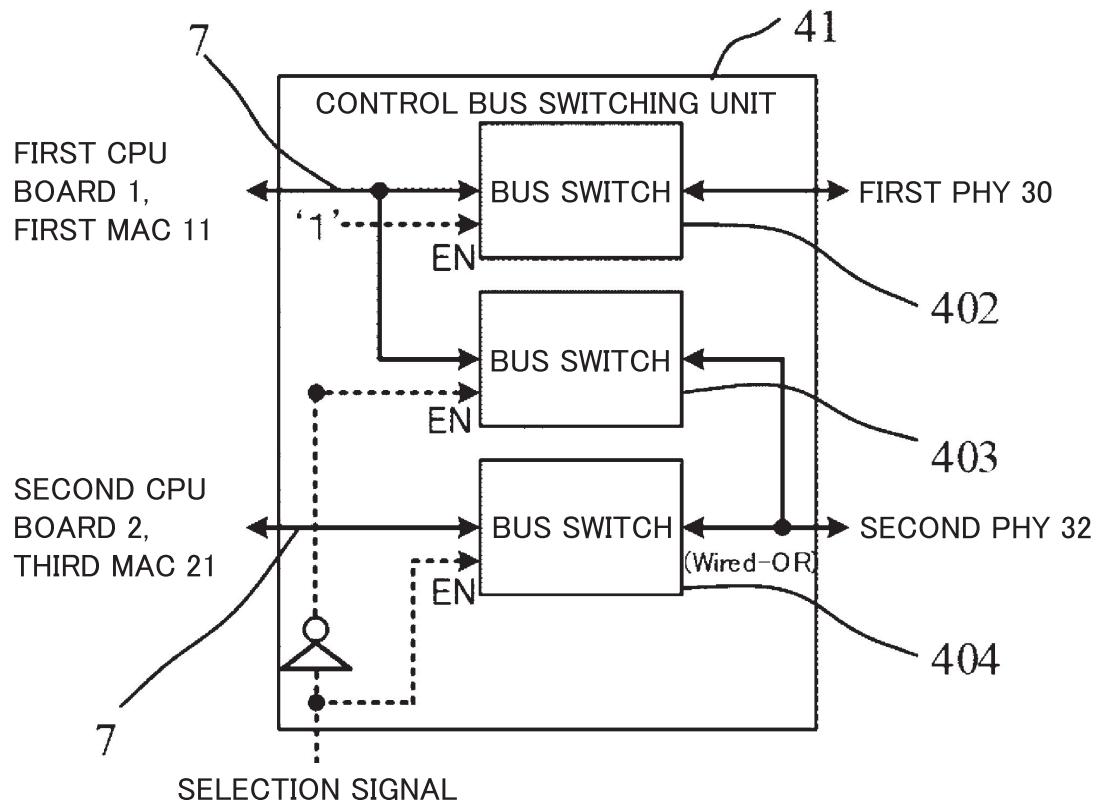


FIG.6

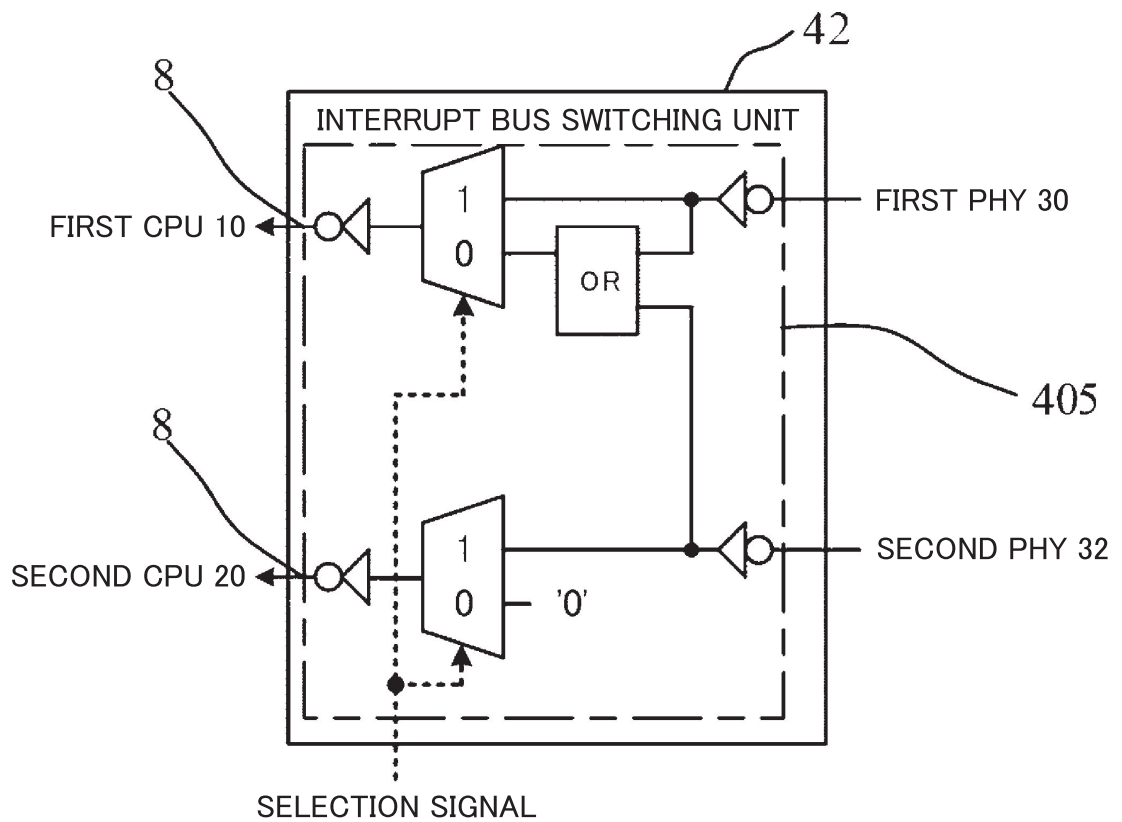


FIG.7

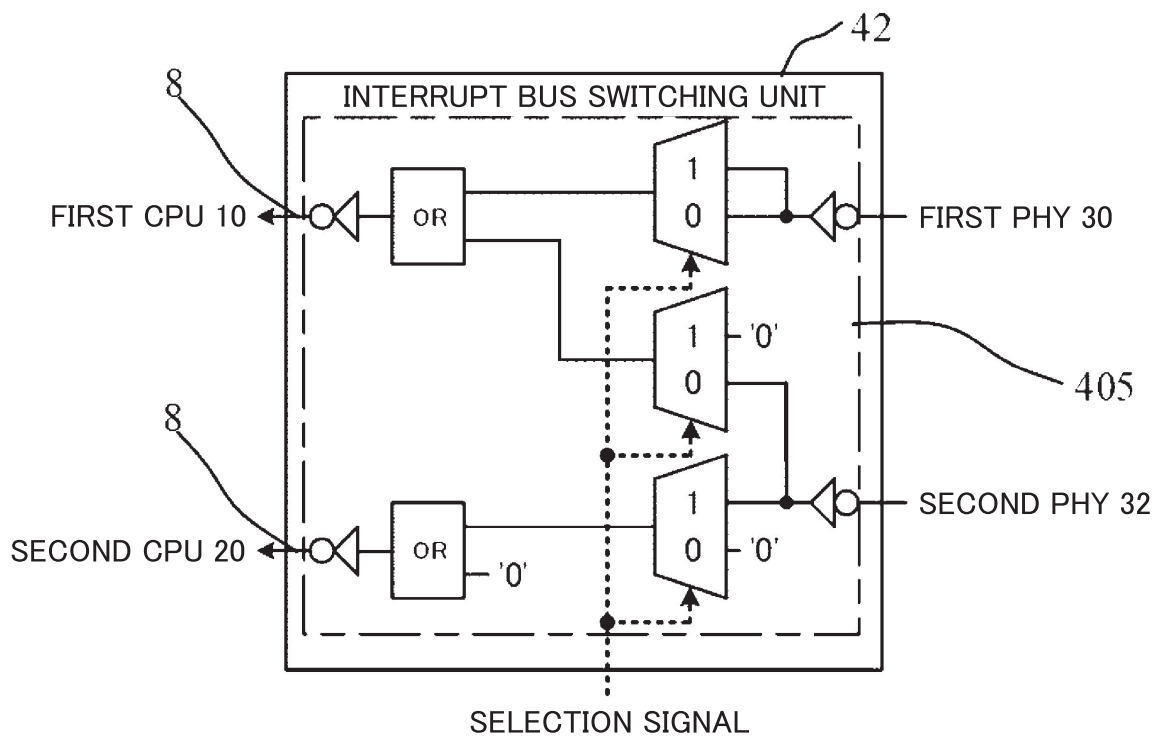


FIG.8

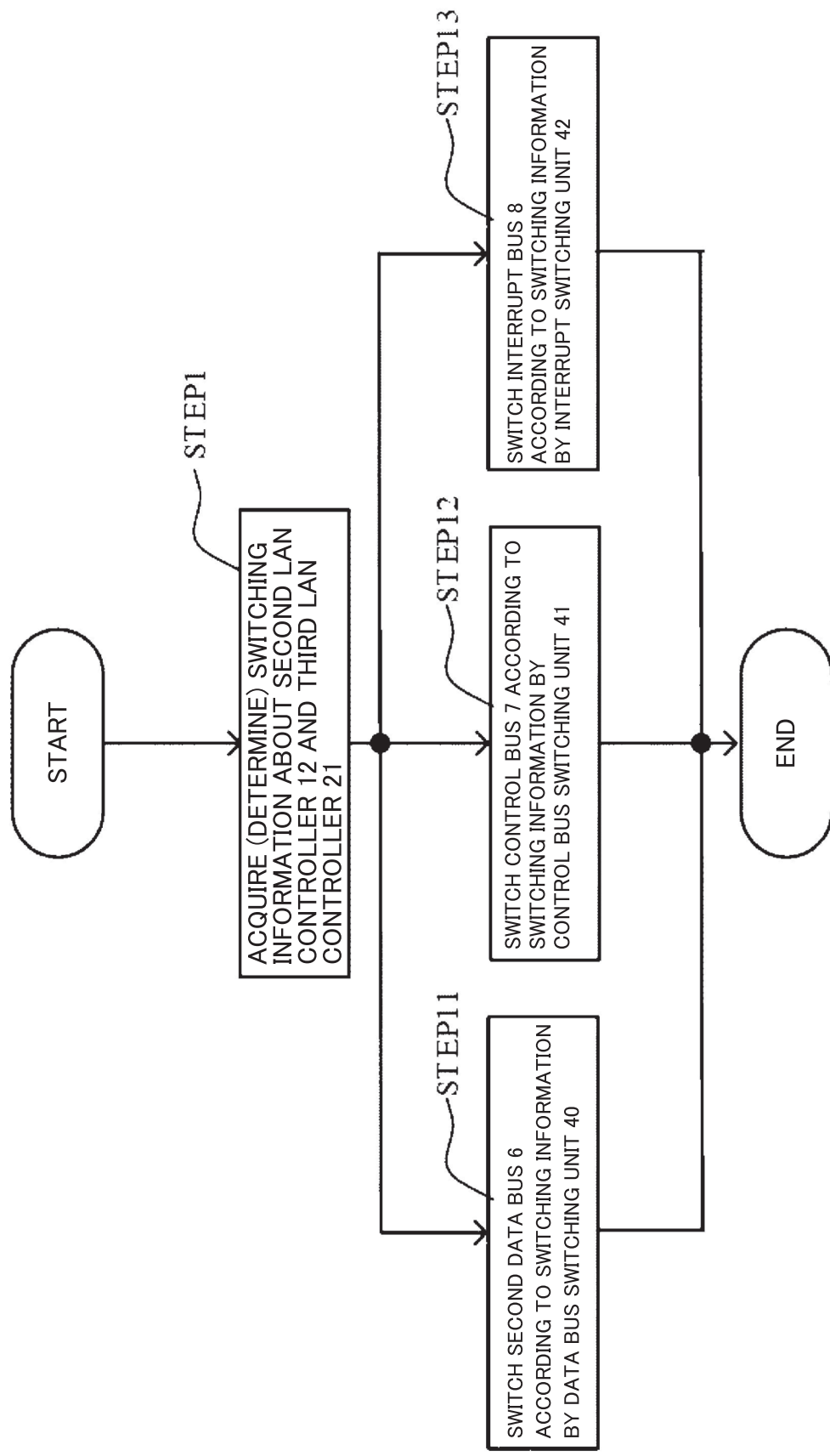


FIG.9

