Abstract

An Electronic Circuit Having A Resistor Integrated Therein And A Method of Reducing the Effects Of Parasitic Capacitances In Such A Circuit

An electronic circuit (30) on a semiconductor chip includes a resistor (31) formed by a resistance layer formed over an insulating layer (34) formed over a first cancellation layer (32). A duplicating circuit is coupled to duplicate an ac input signal (V_{IN}), which is applied to the resistor (31) and injects the duplicated signal into the cancellation layer (32) below the resistor (31) so as to minimise the potential difference and therefore the effects of the parasitic capacitance (35) between the resistance layer and the cancellation layer.

FIG. 3