Abstract

Net block assembly for an integrated circuit dicing system

A dicing system dices substrates to singulate integrated circuits formed in the substrates. The singulated units (7a) are placed onto a net block (10a) having a square array of pockets defined by ridges (10e, 10f). The integrated circuits are inserted into the respective pockets, and urged into them. During this process the ridges (10e, 10f), which have rounded upper edges, direct the integrated circuits laterally to correctly position them.

[Fig. 10(g)]

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