

FIG. 1

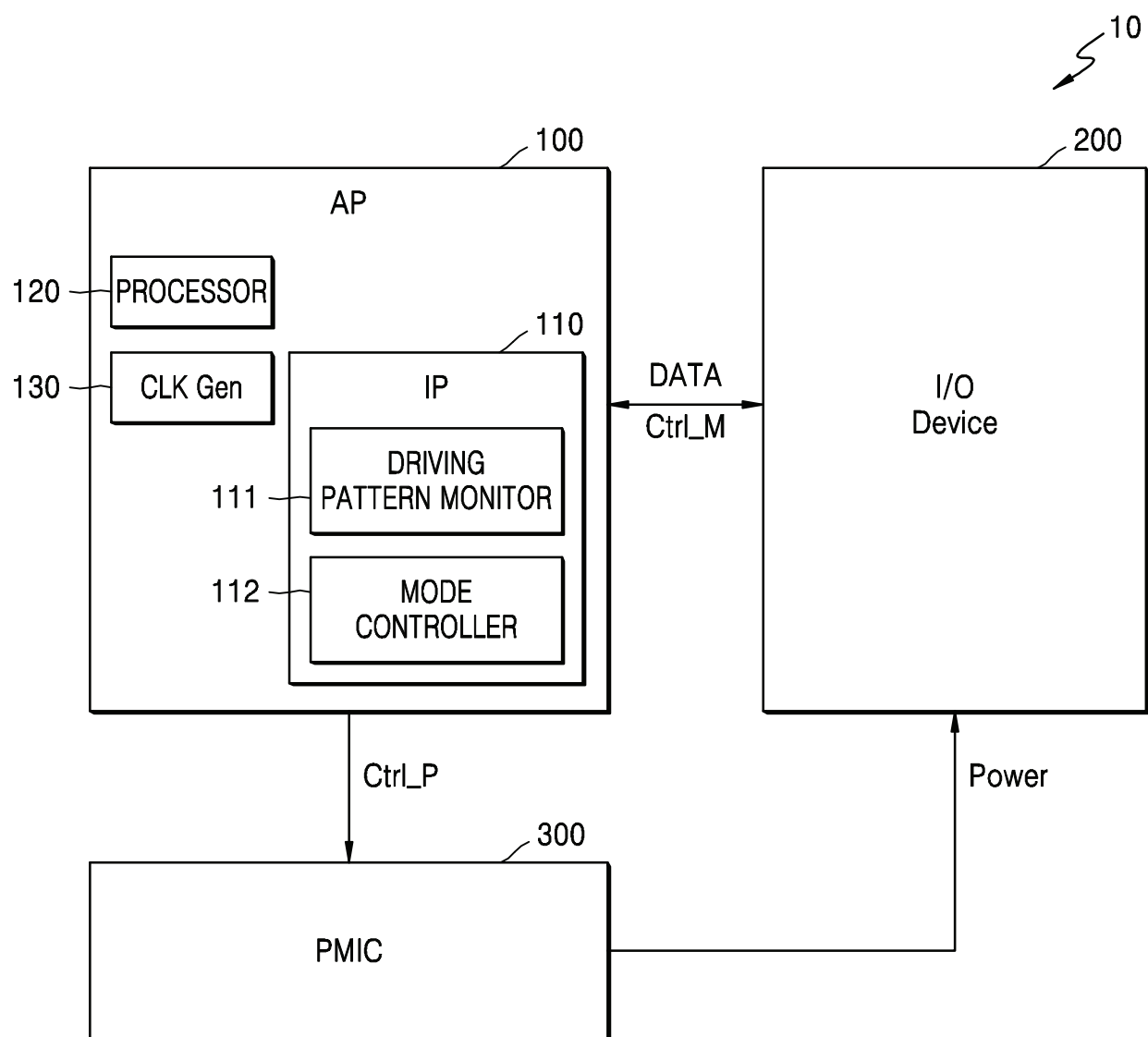


FIG. 2

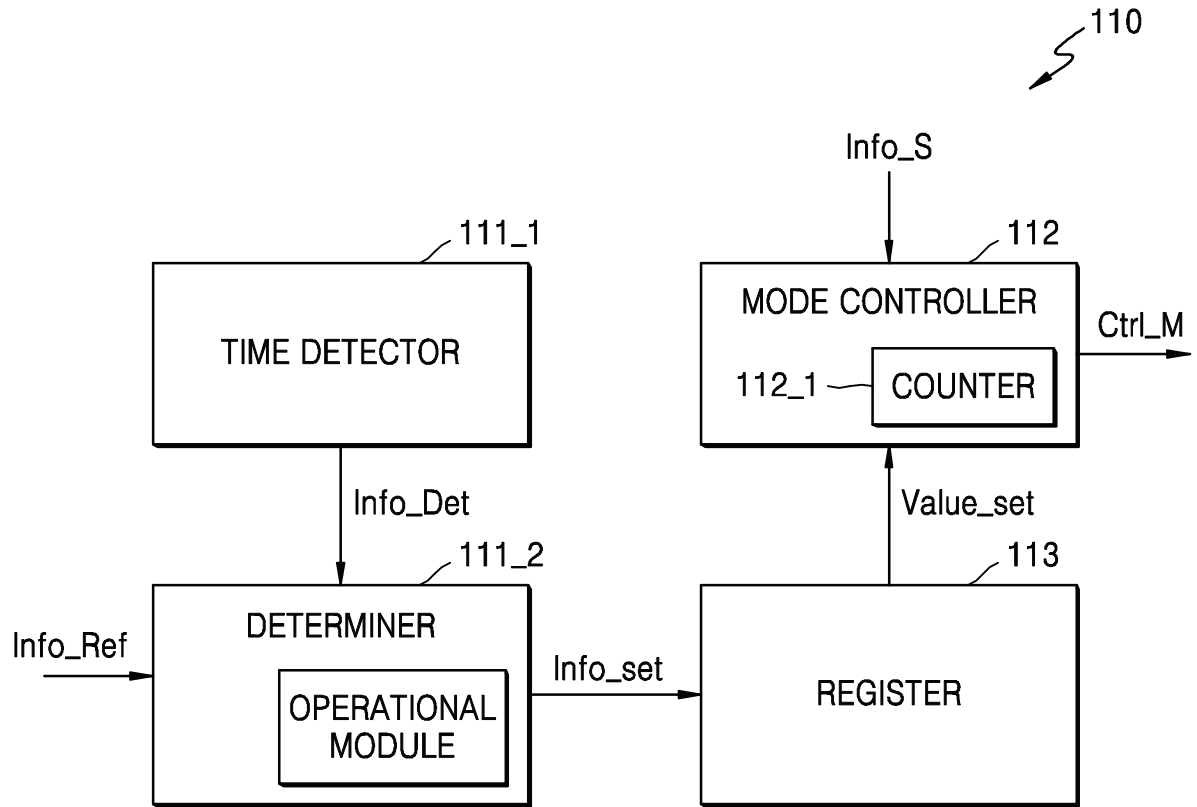


FIG. 3

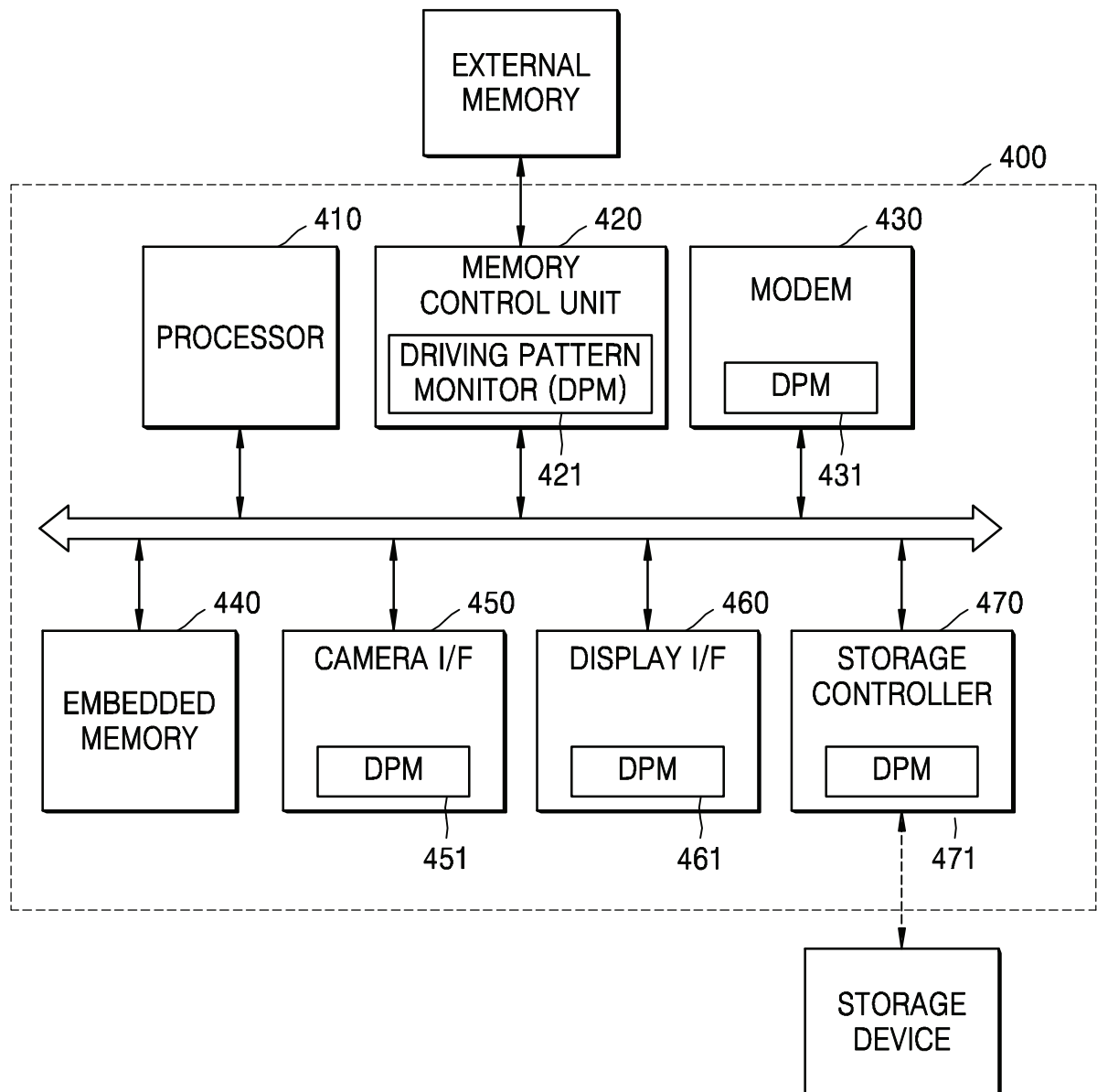


FIG. 4A

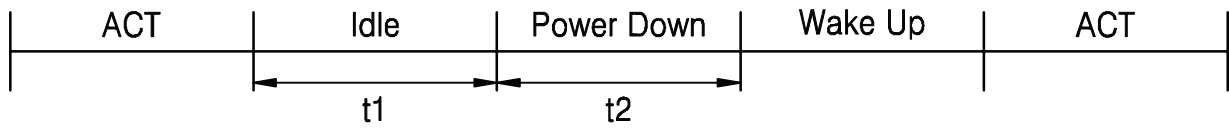


FIG. 4B

PD MODE MAINTENANCE TIME	SET IDLE PERIOD
$t_2 > \text{Ref } 1$	DECREASE t_1 PERIOD
$\text{Ref } 2 < t_2 \leq \text{Ref } 1$	MAINTAIN t_1 PERIOD
$t_2 \leq \text{Ref } 2$	INCREASE t_1 PERIOD

FIG. 5

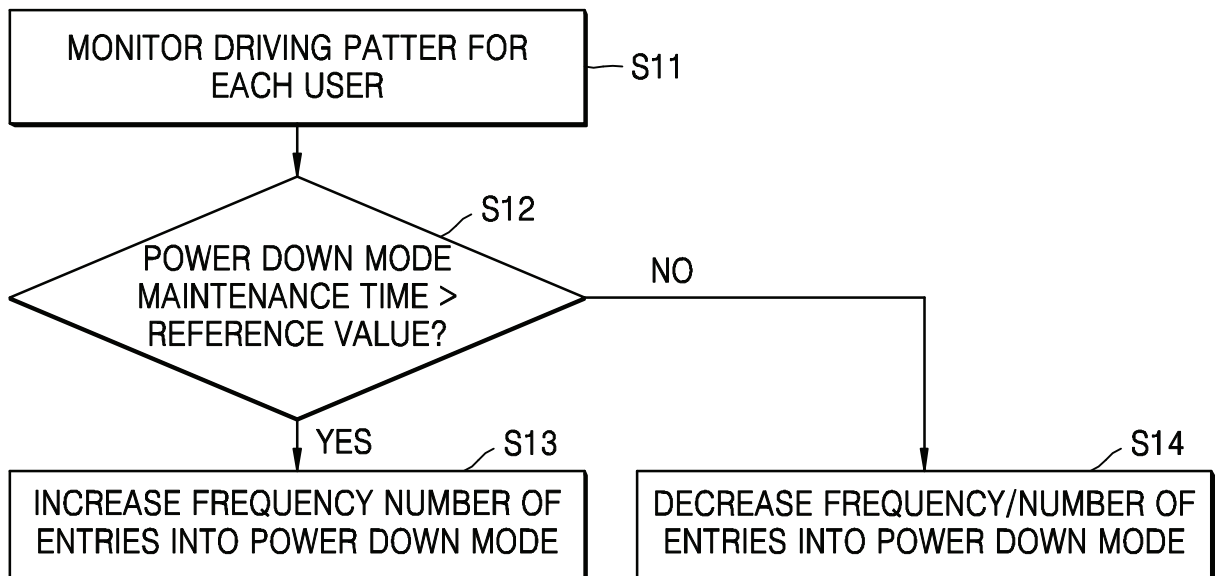


FIG. 6

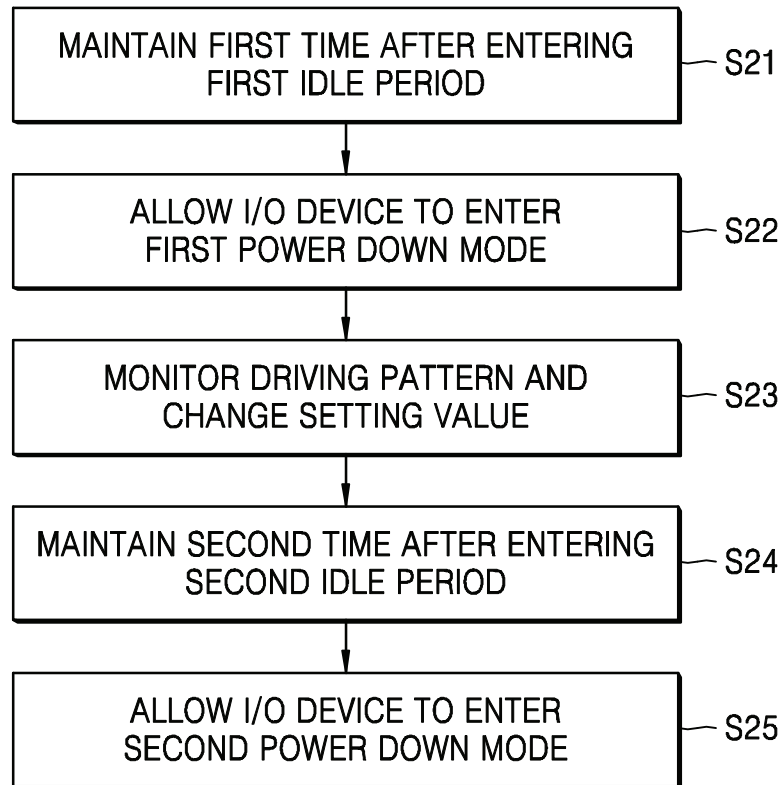


FIG. 7

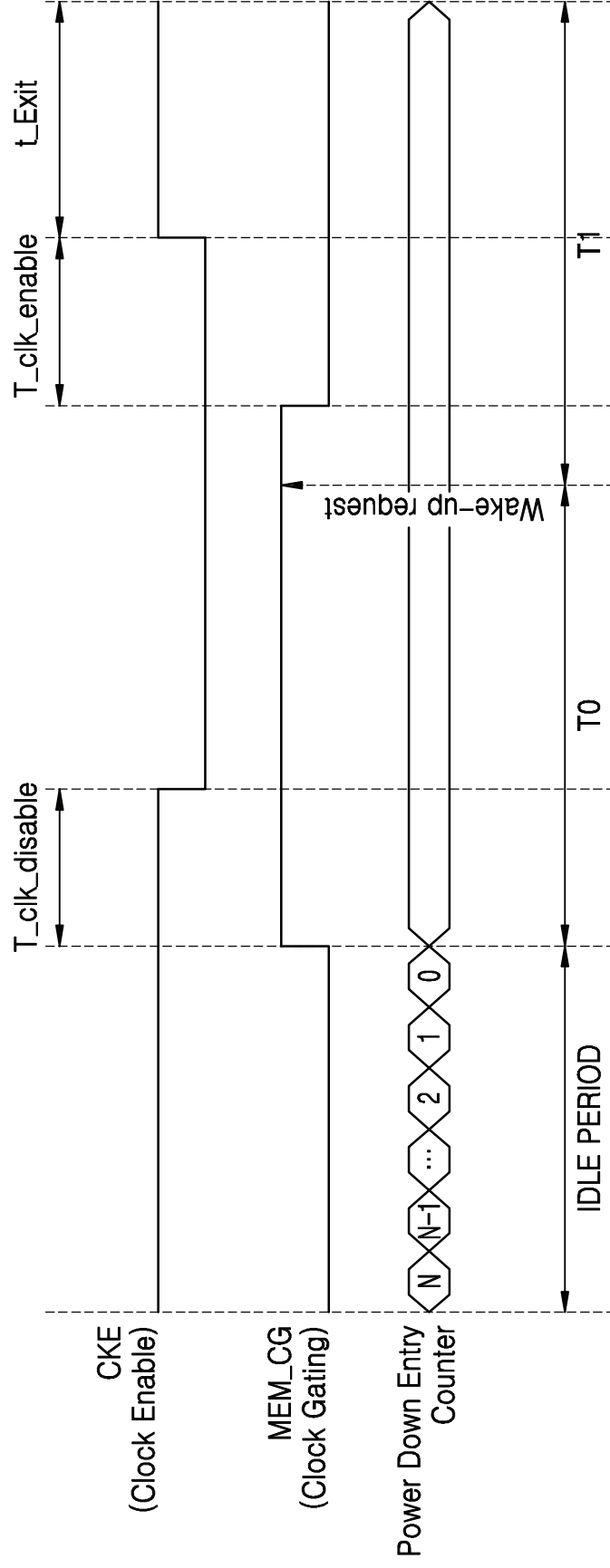


FIG. 8

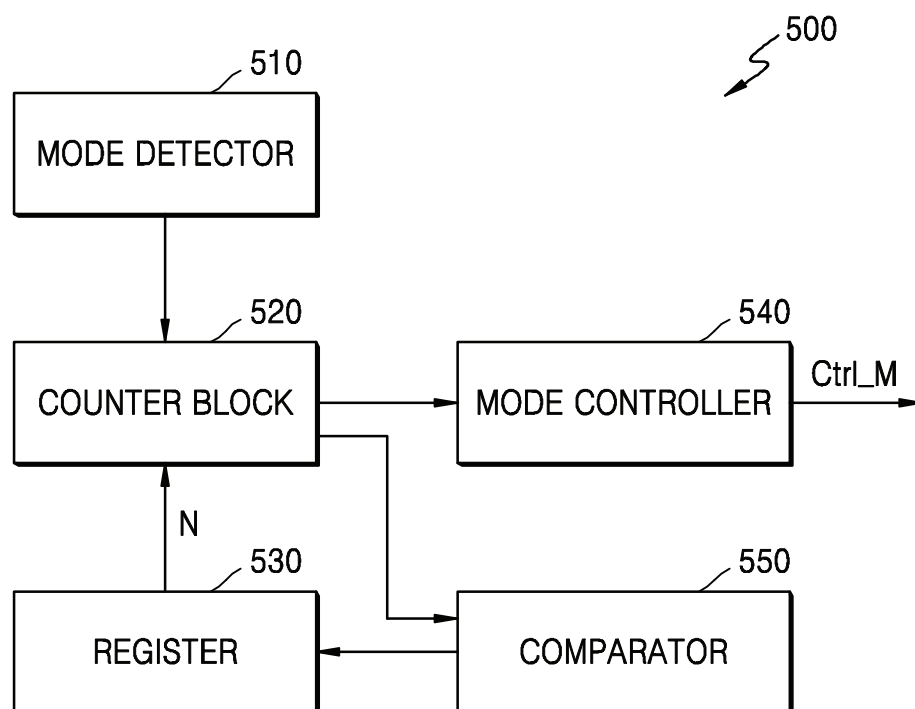


FIG. 9

PERIOD COMPARISON	REGISTER SETTING VALUE
$T_0 > T_1$	$N - \alpha$
$T_0 \doteq T_1$	N
$T_0 < T_1$	$N + \alpha$

N = CURRENT SETTING VALUE

α = INTEGER EQUAL TO OR MORE THAN ONE

FIG. 10

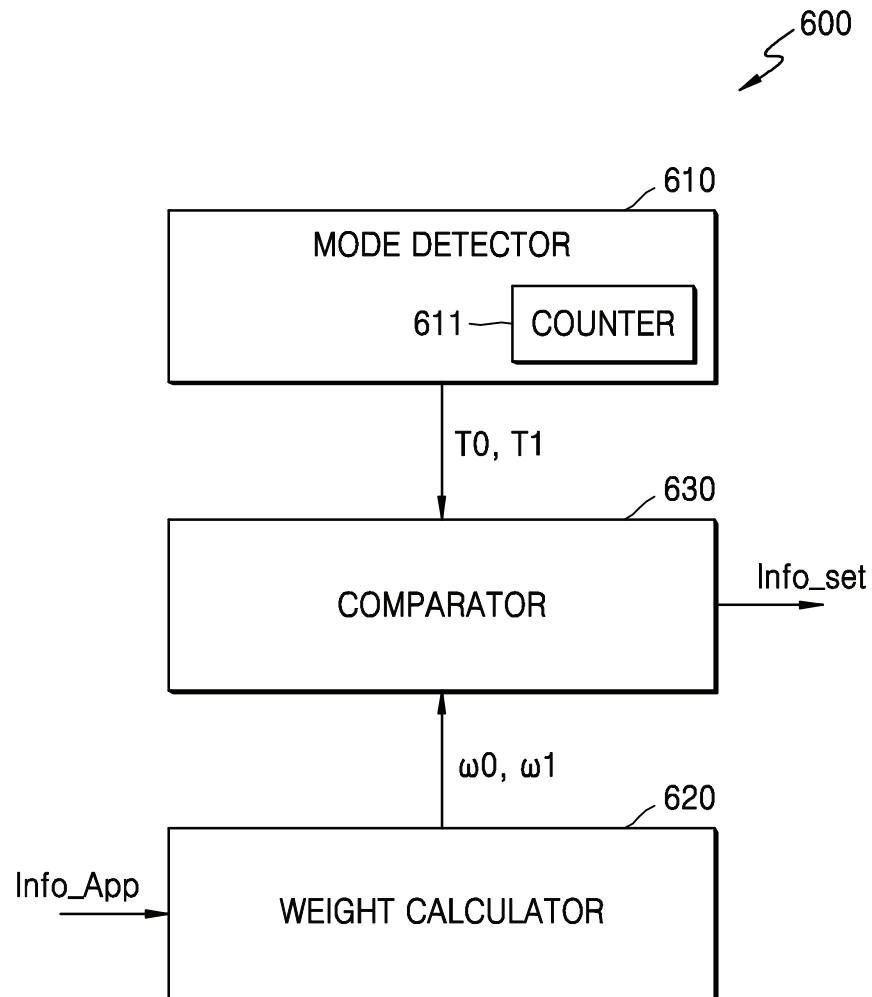


FIG. 11A

PERIOD COMPARISON	REGISTER SETTING VALUE
$\omega_0 \times T_0 > \omega_1 \times T_1$	$N - \alpha$
$\omega_0 \times T_0 \doteq \omega_1 \times T_1$	N
$\omega_0 \times T_0 < \omega_1 \times T_1$	$N + \alpha$

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FIG. 11B

KIND OF EXECUTED PROGRAM	WEIGHT VALUE
FREQUENCY/NUMBER OF HIGH-PERFORMANCE PROGRAM ↑	DECREASE WEIGHT VALUE ω_0 AND/OR INCREASE WEIGHT VALUE ω_1
FREQUENCY/NUMBER OF LOW-PERFORMANCE PROGRAM ↑	INCREASE WEIGHT VALUE ω_0 AND/OR DECREASE WEIGHT VALUE ω_1

FIG. 12

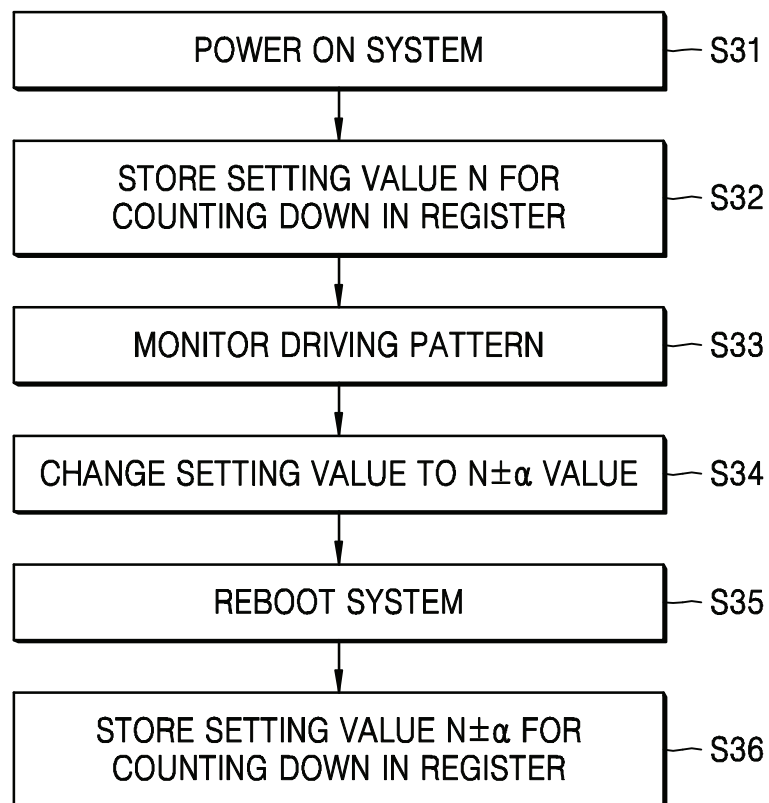


FIG. 13

PERIOD COMPARISON (T_0-T_1) OR ($\omega_0 \times T_0 - \omega_1 \times T_1$)	REGISTER SETTING VALUE
FIRST STAGE	$N \pm \alpha$
SECOND STAGE	$N \pm 2\alpha$
$\vdots$	$\vdots$
KTH STAGE	$N \pm k\alpha$

FIG. 14

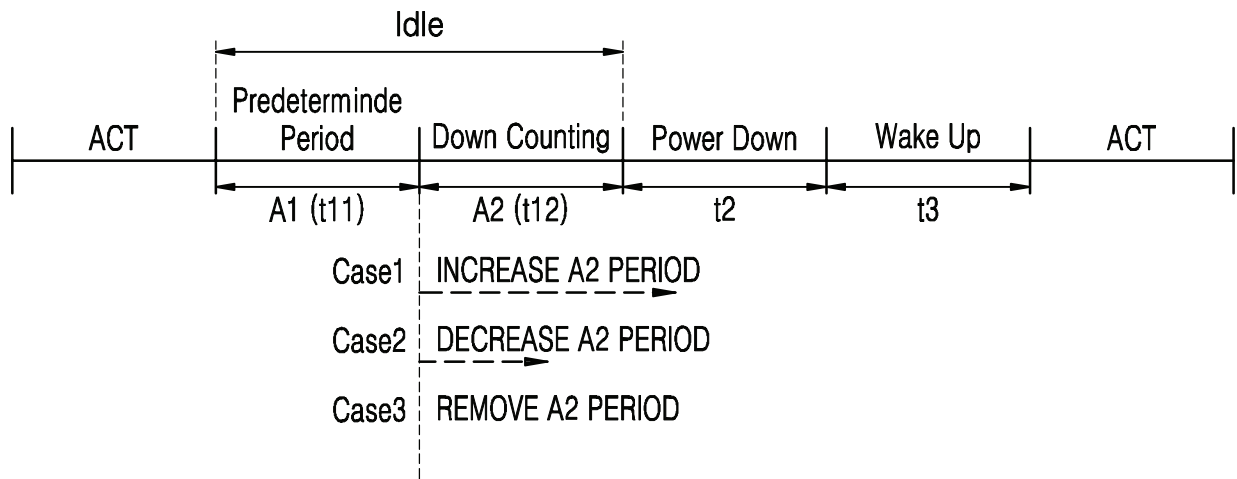


FIG. 15

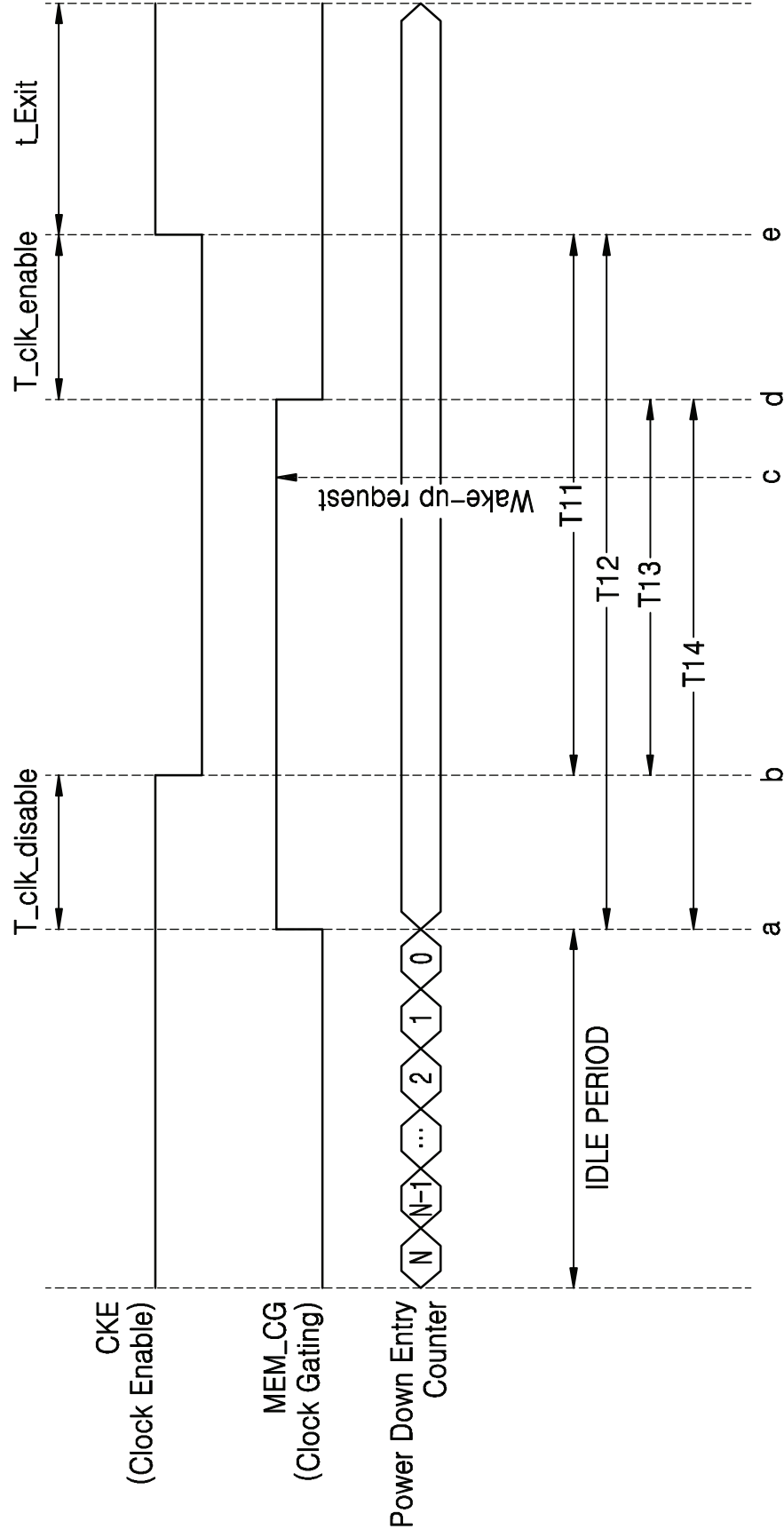


FIG. 16A

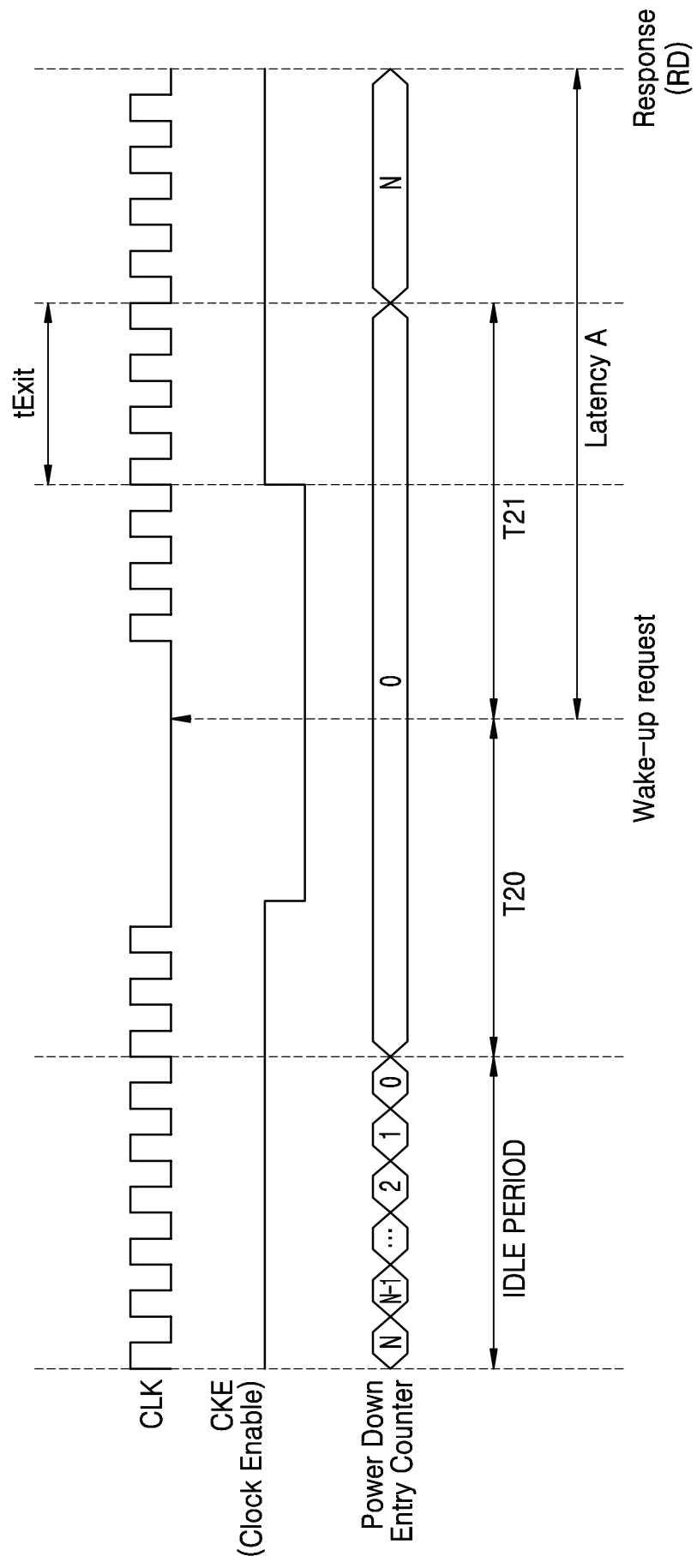


FIG. 16B

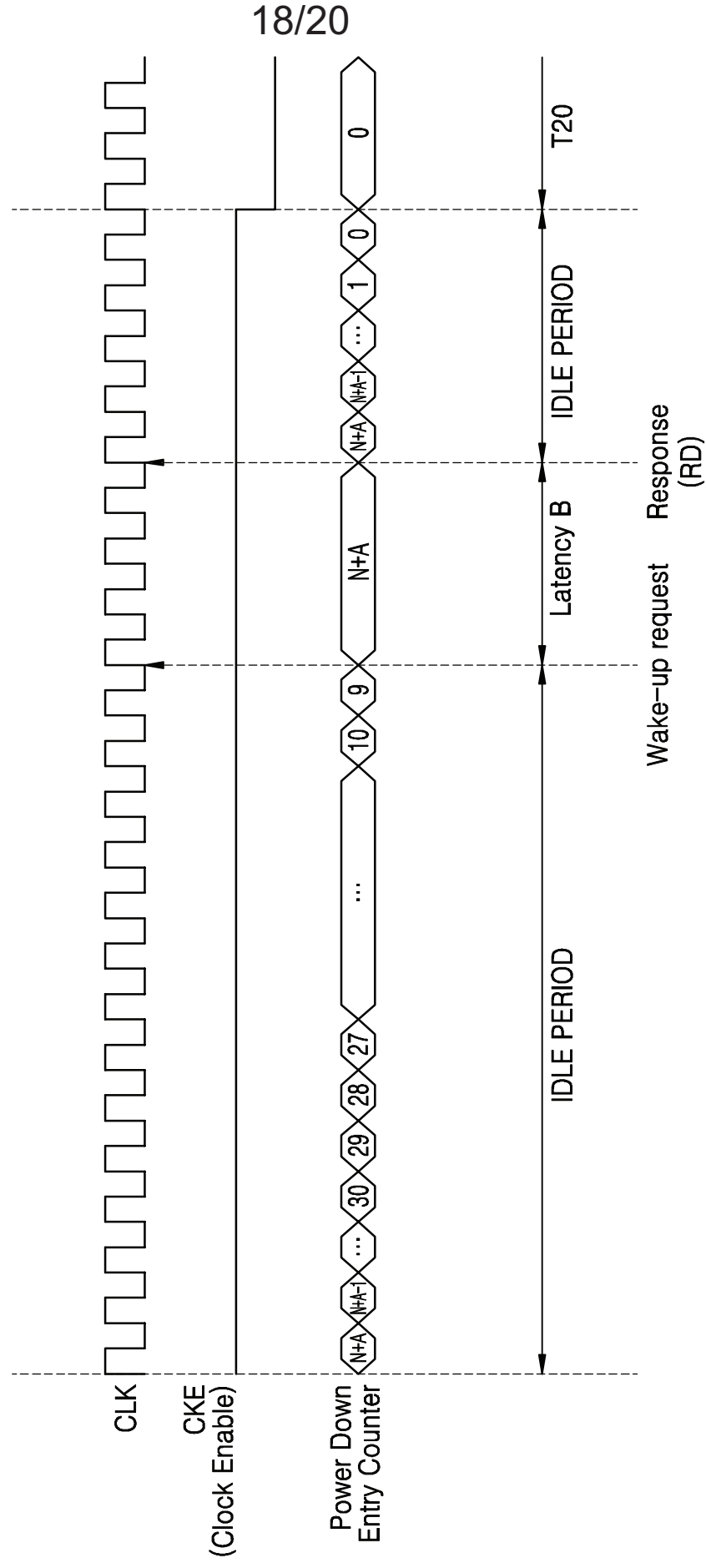


FIG. 17

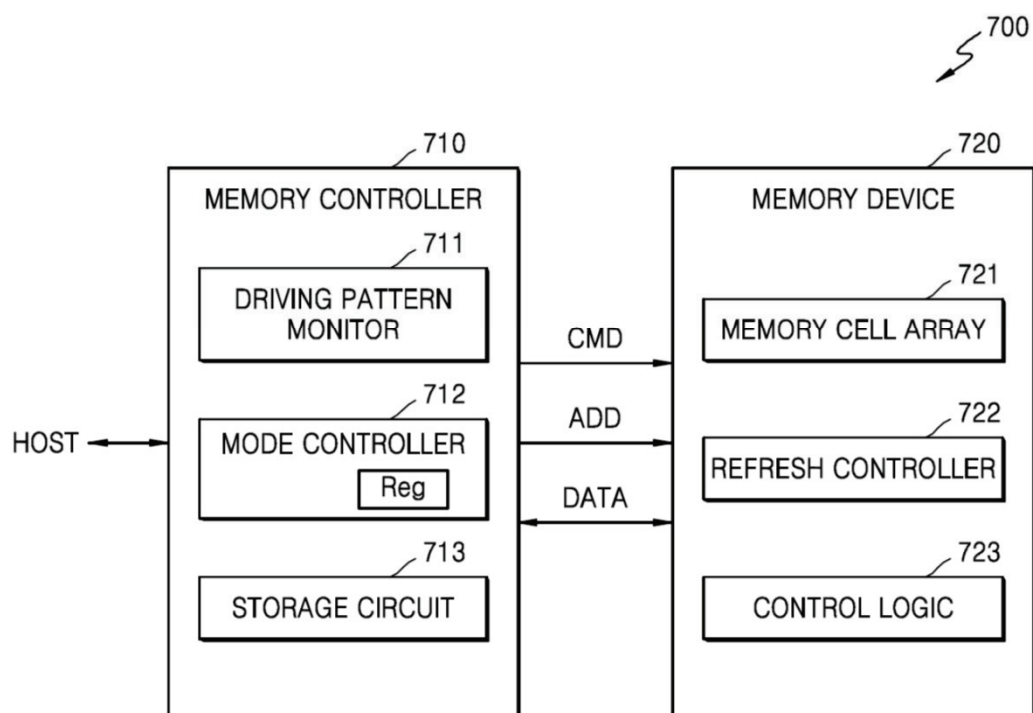


FIG. 18

