

DUAL REFERENCE VOLTAGE GENERATOR, EQUALIZATION CIRCUIT, AND MEMORY

ABSTRACT

The present application provides a dual reference voltage generator, an equalization circuit, and a memory. The dual reference voltage generator receives an original code, a first code, and a second code, generates a first reference voltage according to the received original code and first code, and generates a second reference voltage according to the received original code and second code, wherein the first reference voltage differs from the second reference voltage. That is, the dual reference voltage generator provided in the present application can output two different reference voltages according to the original code, the first code, and the second code, such that requirements for different reference voltages during equalization processing can be better satisfied, and the equalization processing effect of an equalization circuit can be improved. In addition, the values of the original code, the first code, and the second code are preconfigured, such that the voltage margin and timing margin in an eye diagram corresponding to received data of the equalization circuit are both at the maximum values, and the quality of a received data signal can also be improved.