

INVERTER BASED (RETURN-TO-ZERO) RZ + NON-RZ (NRZ) SIGNALING

ABSTRACT

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Embodiments of the invention are generally directed to systems, methods, and apparatuses for inverter based return-to-zero (RZ) + non-RZ (NRZ) signaling. The interface circuit contains multiple ganged drivers (some or all of them are turned on at one point of time) and edge detection circuitry (to configure / modulate edges of the input data
10 signal). These two circuits together generate weighted return-to-zero (RZ) + non-RZ (NRZ) signal.

(Fig. 2)