A METHOD AND APPARATUS FOR DETECTING AND CONCEALING DATA ERRORS IN STORED DIGITAL DATA

ABSTRACT

A method and apparatus is disclosed for detecting and concealing errors in stored digital samples. A multibit digital input sample is received and an error detecting code, corresponding to that input sample is calculated. Then a multibit digital storage sample is generated by substituting the error detecting code for the same number of least significant bits of the input sample. The storage sample is then stored in a memory device (20). A previously stored sample is retrieved from the memory device (20) and is analyzed to detect whether an error has occurred. If an error is detected, a substitute sample produced for the retrieved sample, otherwise the retrieved sample is produced.

Figure 2