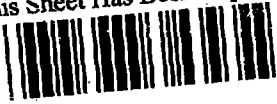


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RCA 86,166

Abstract of the Disclosure

5 A signal processor (43,44) responsive to a video input signal (Y1) provides a first video output signal (Y4) linearly related to the input signal and a second video output signal (Y5) non-linearly related to the video input
10 signal. A first combining circuit (45) combines the first and second video output signals in predetermined proportions to provide a third video output signal (Y6) having both linear and non-linear components. A second combining means (46) combines the second and third video output signals in proportions determined by a picture level representative control signal (VC) to provide a processed video output signal having enhanced contrast for all values of the average picture level of displayed images.

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