

FIG. 1

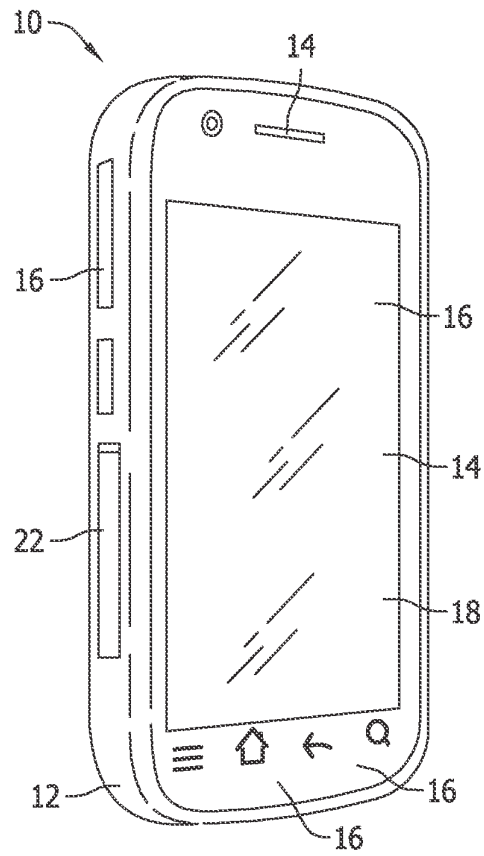
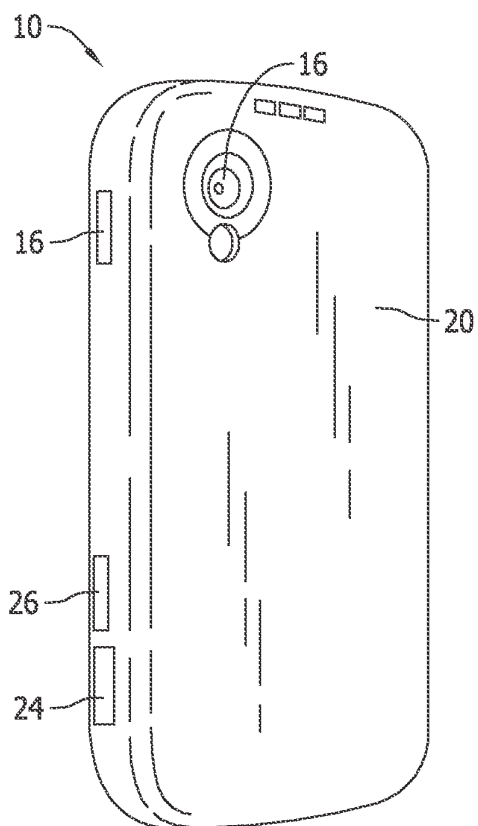


FIG. 2



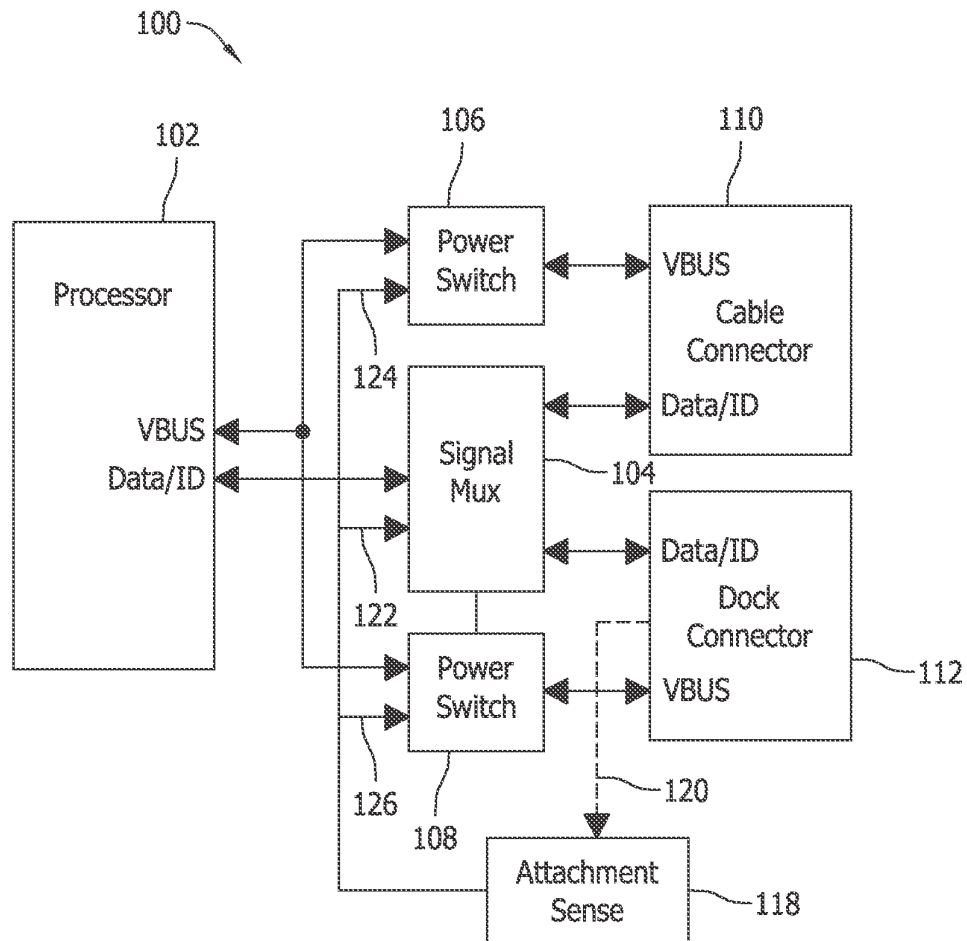


FIG. 3

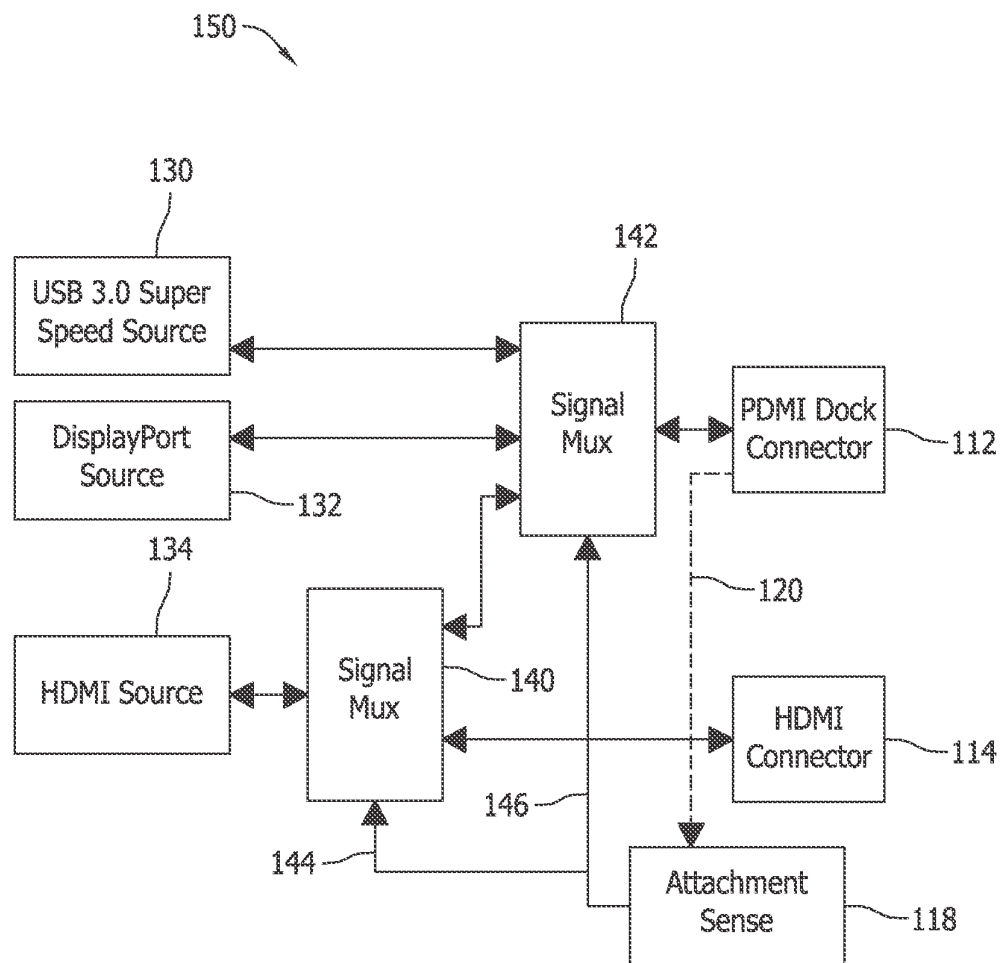


FIG. 4

PDMI: 2-lane DisplayPort pinout			
Pin No.	Pin Name	Interface Grouping	Pin Description
1	USB 5V	USB 2.0 Interface	USB Power (VBUS)
2	USB DGND		USB Ground
3	USB D+		Data +
4	USB OTG		On-The-Go
5	USB D-		Data -
6	HC 5V	High Current Power (Output on Host)	High current 5V supply (1.8A, or 3.6A if combined with pin 15)
7	VDET	Video Mode Detect	Video Mode Detect = Open
8	AUDIO RIGHT OUT	Analog Audio (Input on Host)	Right Audio Out
9	AUDIO LEFT OUT		Left Audio Out
10	AUDIO OUT GND		Audio output ground
11	CONFIG2	CEC	
12	SSR-	USB 3.0 Data Device Receive	USB 3.0 SSRX- signal
13	GND		Signal Ground
14	SSR+		USB 3.0 SSRX+ signal
15	HC 5V	High Current Power (Output from Host)	High current 5V supply (1.8A, or 3.6A if combined with pin 6)
16	HC GND		High current Ground
17	SST-	USB 3.0 Data Device Transmit	USB 3.0 SSTX- signal
18	GND		Signal Ground
19	SST+		USB 3.0 SSTX+ signal
20	HPD	DisplayPort v1.1a Interface, 2 Lane (Host is Sink, Device is Source)	Hot Plug Detect
21	DAUX+		AUX Channel +
22	DAUX-		AUX Channel -
23	AP		3.3V DisplayPort Power (Power from portable device)
24	D1-		Main Link Lane 1 (-)
25	GND		Signal Ground
26	D1+		Main Link Lane 1 (+)
27	CONFIG1		(was Signal Ground)
28	D0-		Main Link Lane 0 (-)
29	GND		Signal Ground
30	D0+		Main Link Lane 0 (+)

FIG. 5

PDMI: HDMI Mode pinout			
Pin No.	Pin Name	Interface Grouping	Pin Description
1	USB 5V	USB 2.0 Interface	USB Power (VBUS)
2	USB DGND		USB Ground
3	USB D+		Data +
4	USB OTG		On-The-Go
5	USB D-		Data -
6	HC 5V	High Current Power (Output on Host)	High current 5V supply (1.8A, or 3.6A if combined with pin 15)
7	VDET	Video Mode Detect	Video Mode Detect = GND
8	AUDIO RIGHT OUT	Analog Audio (Input on Host)	Right Audio Out
9	AUDIO LEFT OUT		Left Audio Out
10	AUDIO OUT GND		Audio output ground
11	CEC	HDMI Interface	Consumer Electronic Control
12	Clock-		TMDS Clock-
13	GND		Signal Ground
14	Clock+		TMDS Clock+
15	HC 5V	High Current Power (Output from Host)	High current 5V supply (1.8A, or 3.6A if combined with pin 6)
16	HC GND		High current Ground
17	Data0-	HDMI Interface (Host is Sink, Device is Source)	TMDS Data0-
18	GND		Signal Ground
19	Data0+		TMDS Data0+
20	HPD / HEAC+		Hot Plug Detect / HEAC+
21	SCL		I2C serial clock for DDC
22	SDA		I2C serial data for DDC
23	+5V		+5V power (max 50mA)
24	Data1-		TMDS Data1-
25	GND		Signal Ground
26	Data1+		TMDS Data1+
27	UTIL / HEAC-		
28	Data2-		TMDS Data2-
29	GND		Signal Ground
30	Data2+		TMDS Data2+

FIG. 6

PDMI: 4-lane DisplayPort pinout			
Pin No.	Pin Name	Interface Grouping	Pin Description
1	USB 5V	USB 2.0 Interface	USB Power (VBUS)
2	USB DGND		USB Ground
3	USB D+		Data +
4	USB OTG		On-The-Go
5	USB D-		Data -
6	HC 5V	High Current Power (Output on Host)	High current 5V supply (1.8A, or 3.6A if combined with pin 15)
7	VDET	Video Mode Detect	Video Mode Detect = 3.3V
8	AUDIO RIGHT OUT	Analog Audio (Input on Host)	Right Audio Out
9	AUDIO LEFT OUT		Left Audio Out
10	AUDIO OUT GND		Audio output ground
11	CONFIG2	DisplayPort v1.1a Interface, 4-Lane	
12	D3-		Main Link Lane 3 (-)
13	GND		Signal Ground
14	D3+	High Current Power (Output from Host)	Main Link Lane 3 (+)
15	HC 5V		High current 5V supply (1.8A, or 3.6A if combined with pin 6)
16	HC GND		High current Ground
17	D2-	DisplayPort v1.1a Interface, 4-Lane (Host is Sink, Device is Source)	Main Link Lane 2 (-)
18	GND		Signal Ground
19	D2+		Main Link Lane 2 (+)
20	HPD		Hot Plug Detect
21	DAUX+		AUX Channel +
22	DAUX-		AUX Channel -
23	AP		3.3V DisplayPort Power (Power from portable device)
24	D1-		Main Link Lane 1 (-)
25	GND		Signal Ground
26	D1+		Main Link Lane 1 (+)
27	CONFIG1		
28	D0-		Main Link Lane 0 (-)
29	GND		Signal Ground
30	D0+		Main Link Lane 0 (+)

FIG. 7