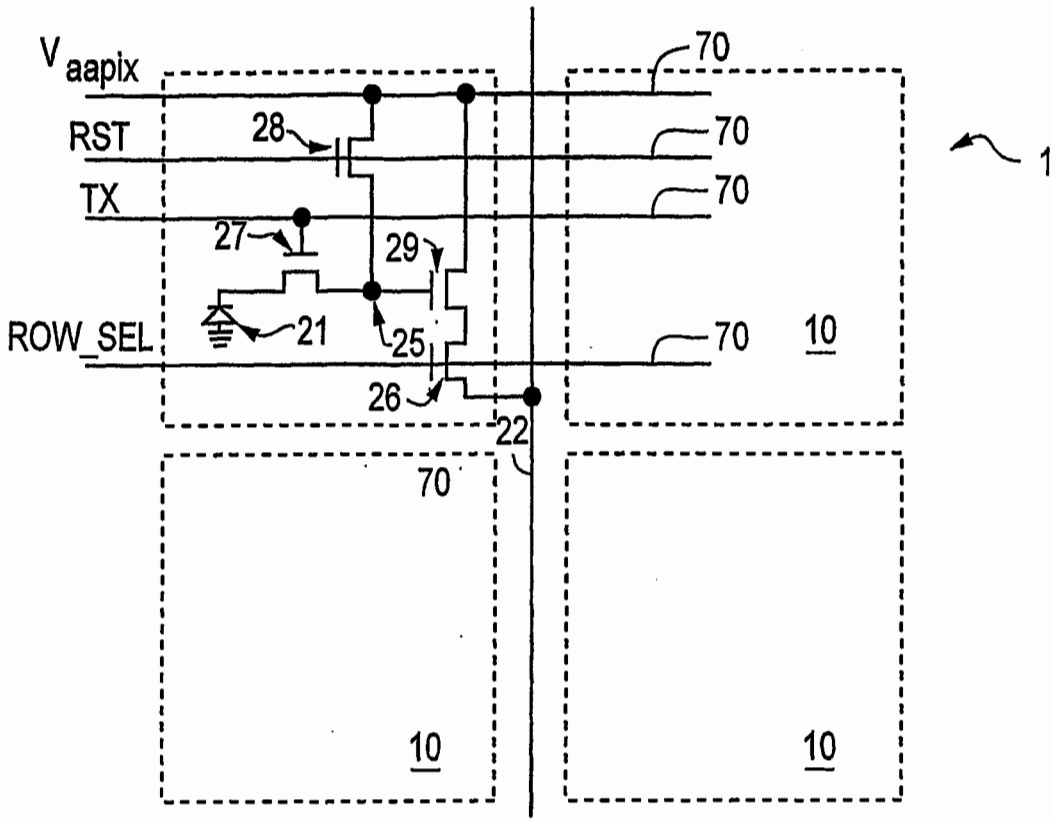


FIG. 1A
(PRIOR ART)

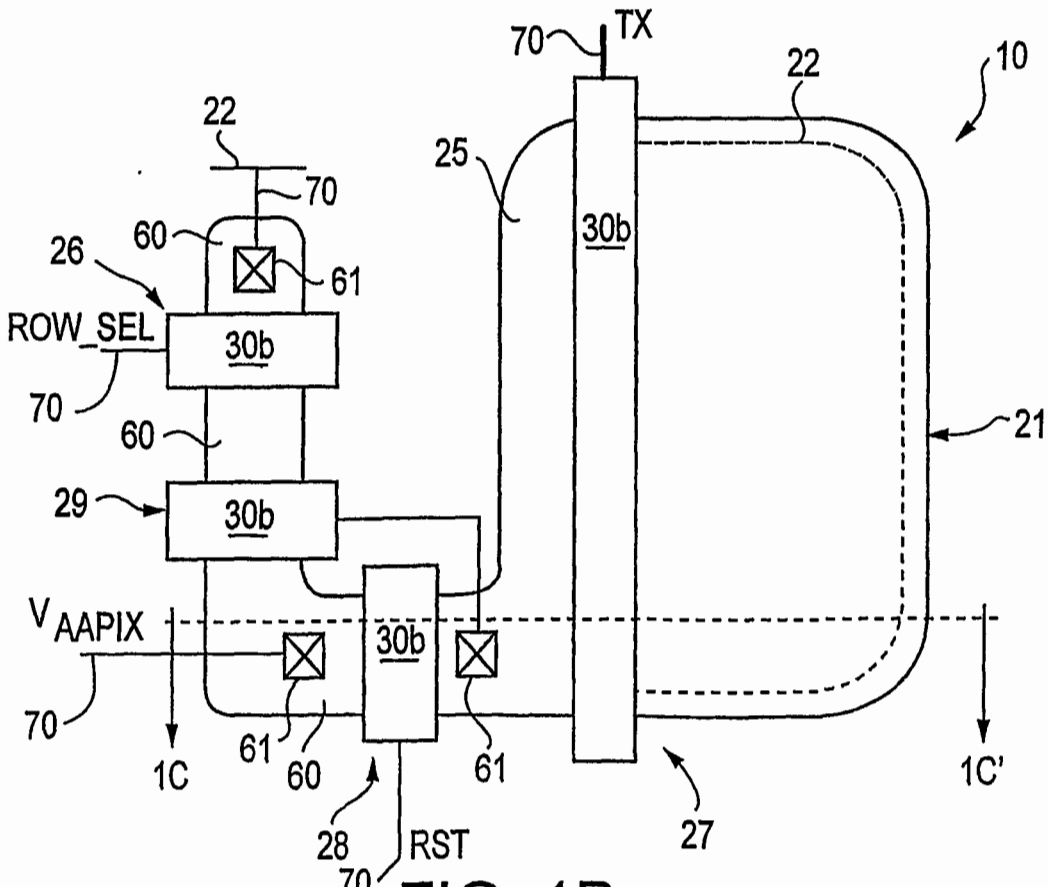


FIG. 1B
(PRIOR ART)

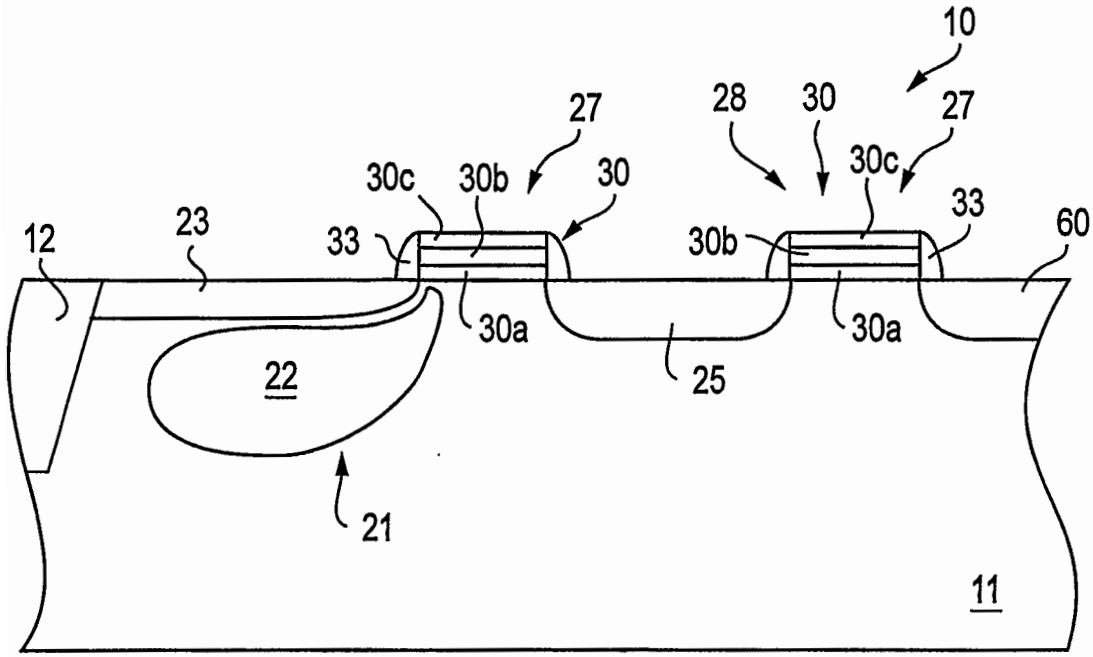


FIG. 1C
(PRIOR ART)

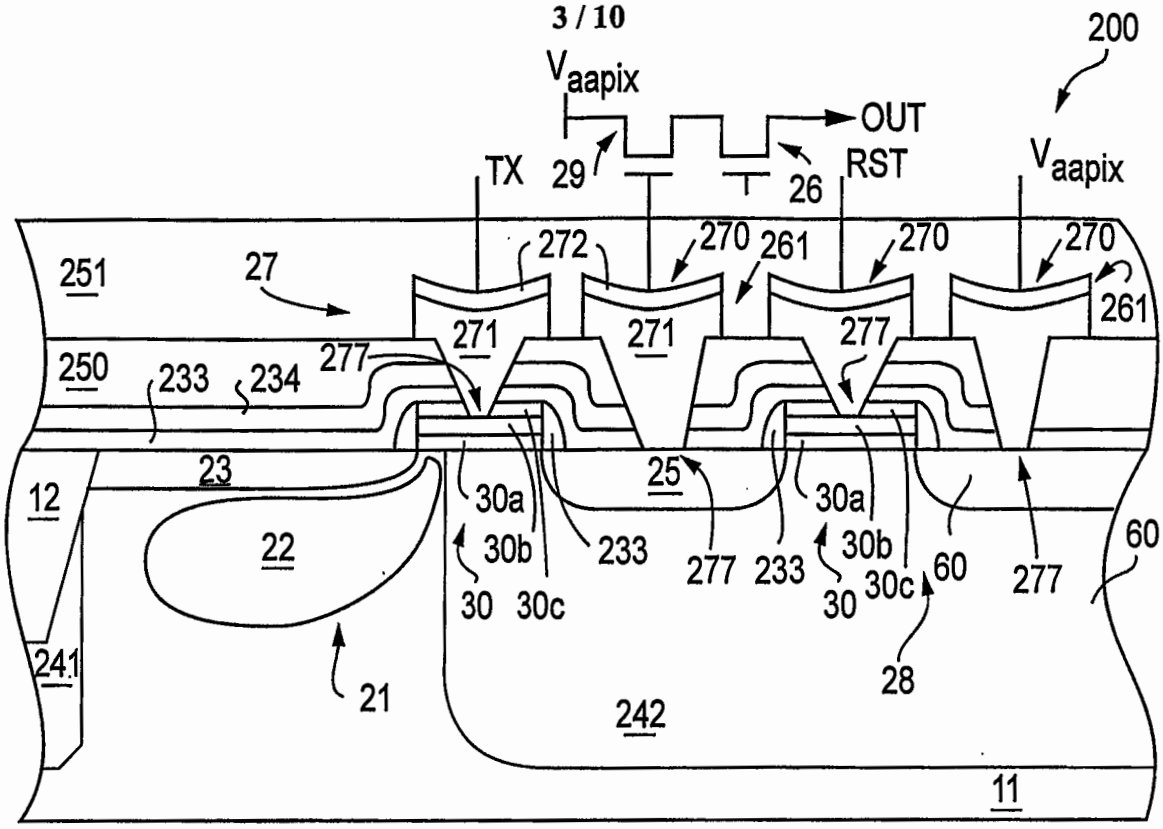

200
V_{aapix}
OUT
TX
29
26
RST
V_{aapix}
251
27
272
270
261
270
270
261
271
271
277
277
250
233
234
277
23
12
25
30a
30a
22
30b
30b
233
277
233
60
277
60
30
30c
30
30c
28
241
21
242
11

FIG. 2

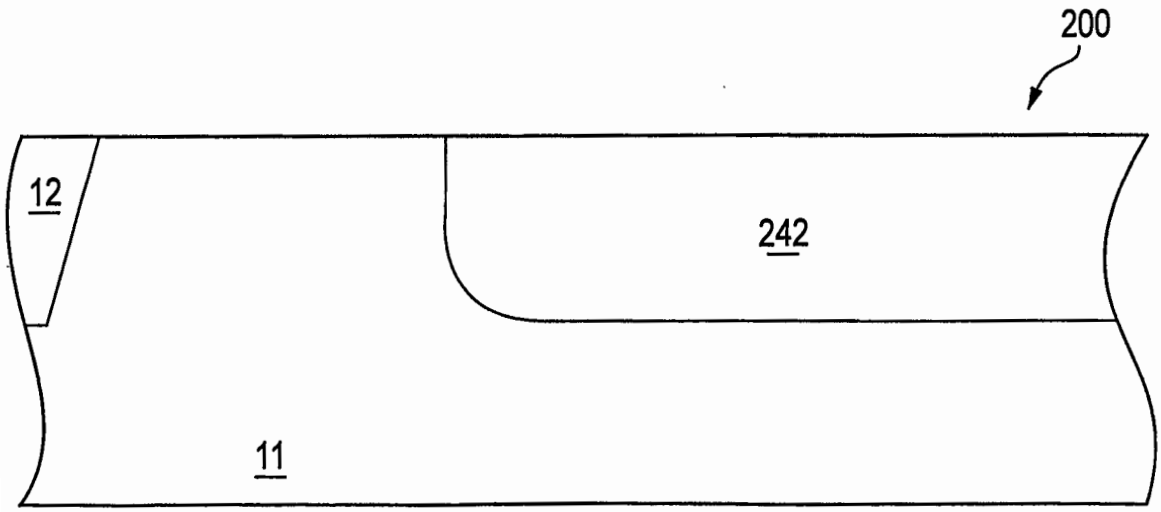

200
12
242
11

FIG. 3A

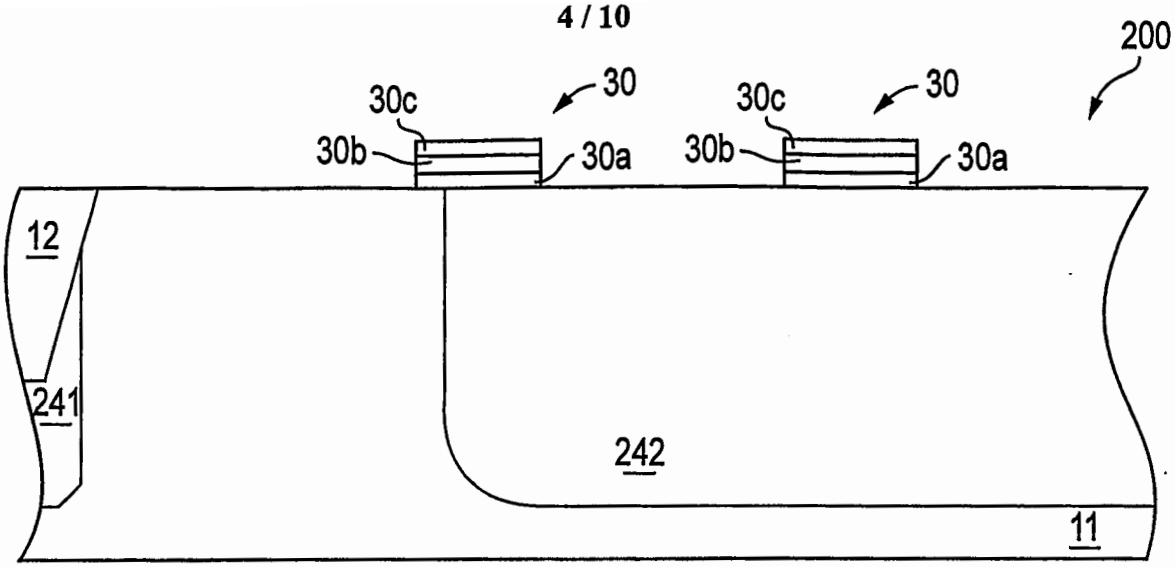


FIG. 3B

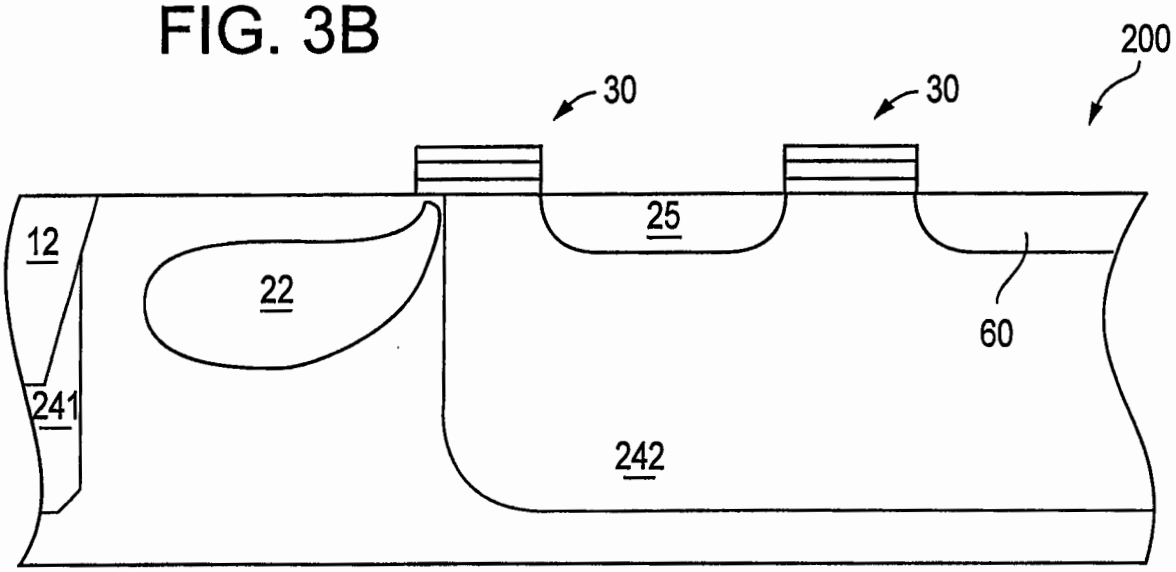


FIG. 3C

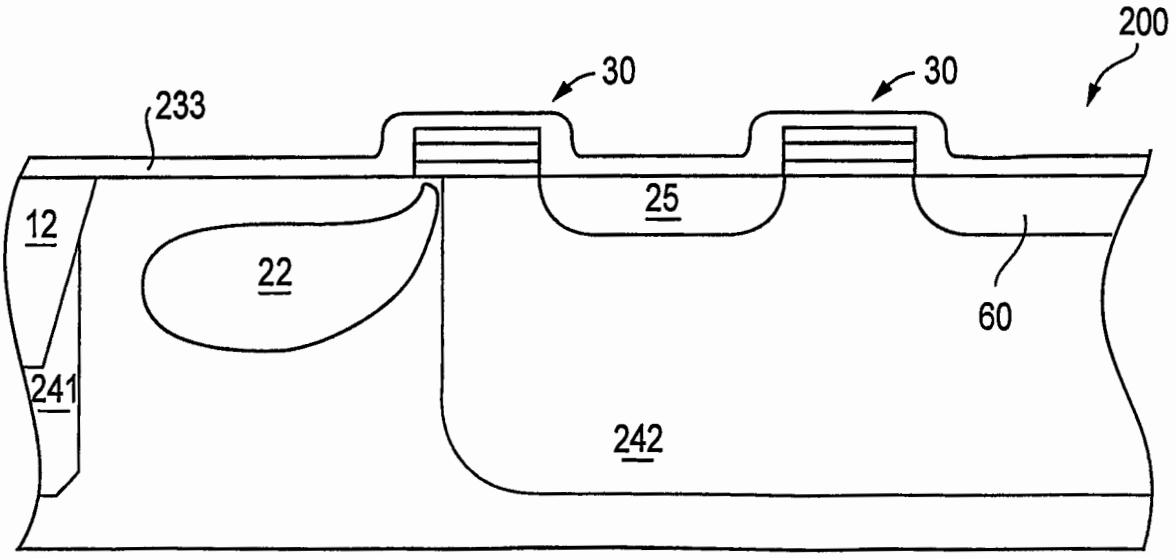


FIG. 3D

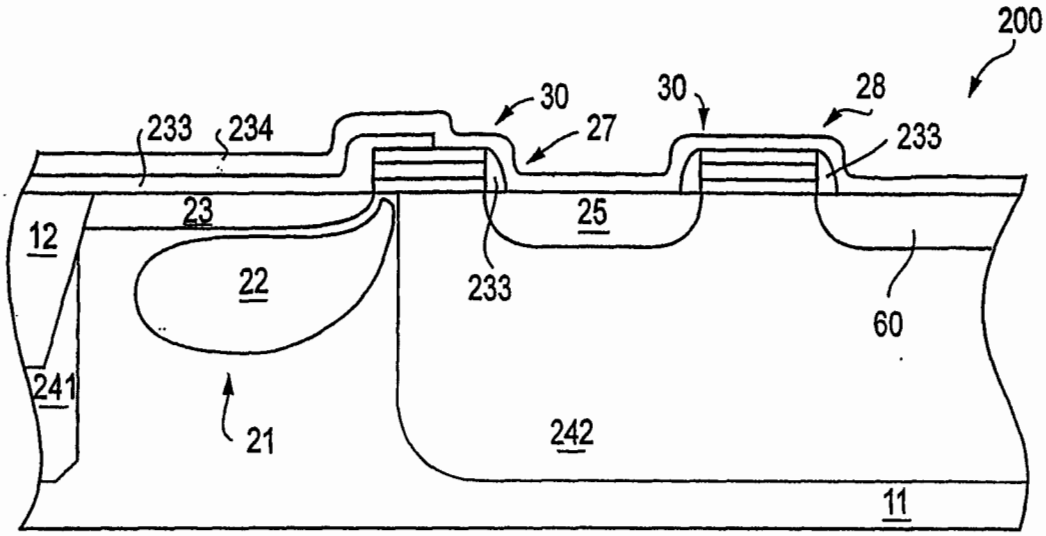


FIG. 3E

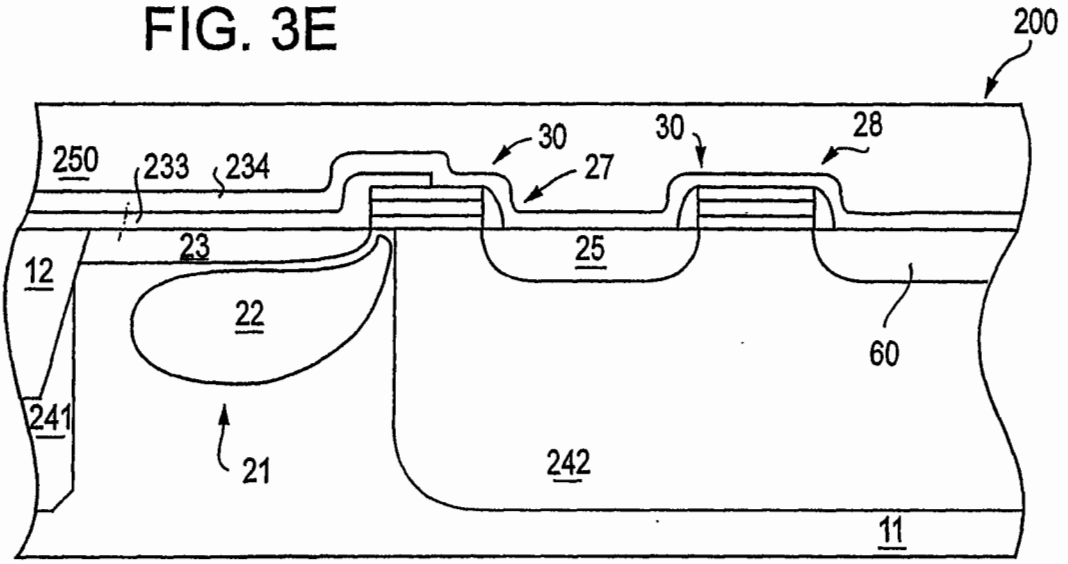


FIG. 3F

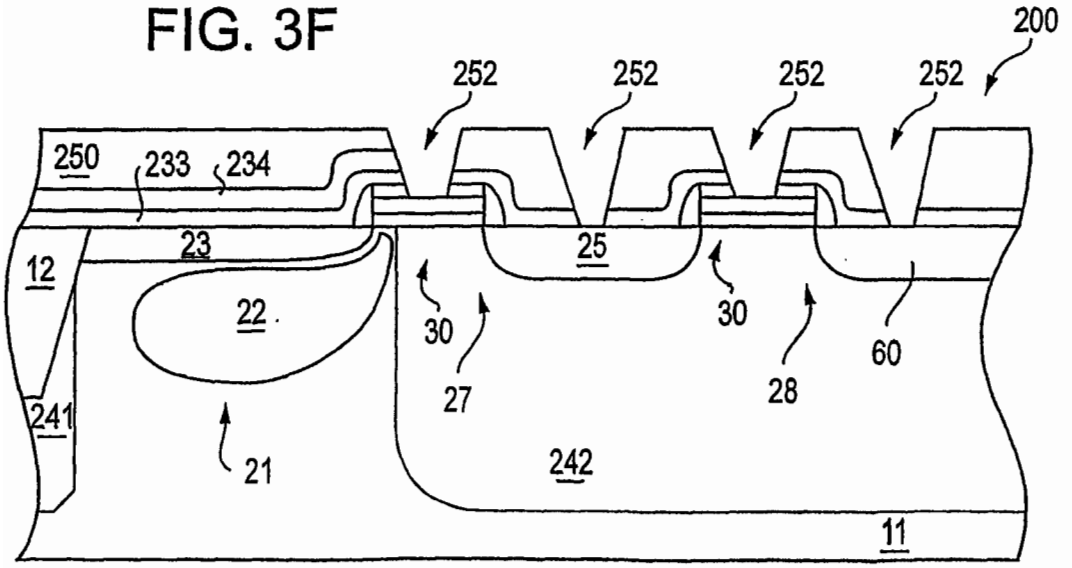


FIG. 3G

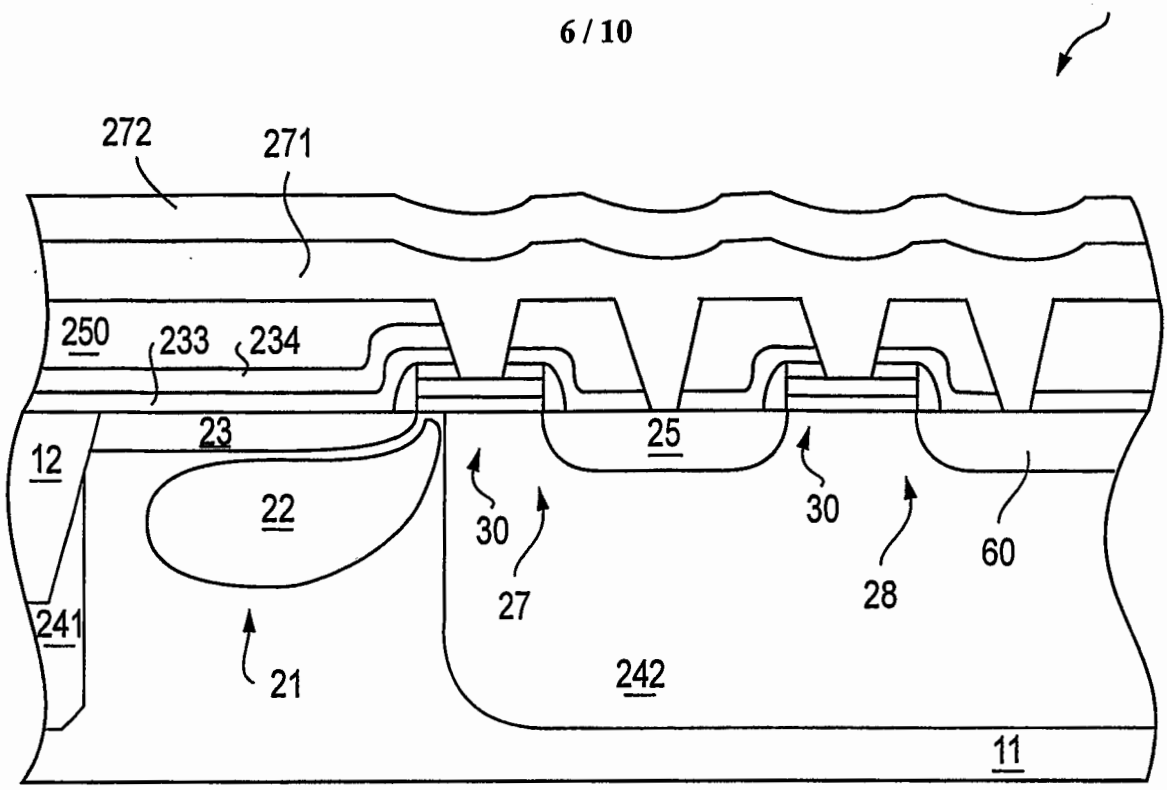

272
271
250
233
234
23
25
12
22
30
30
60
27
28
241
21
242
11

FIG. 3H

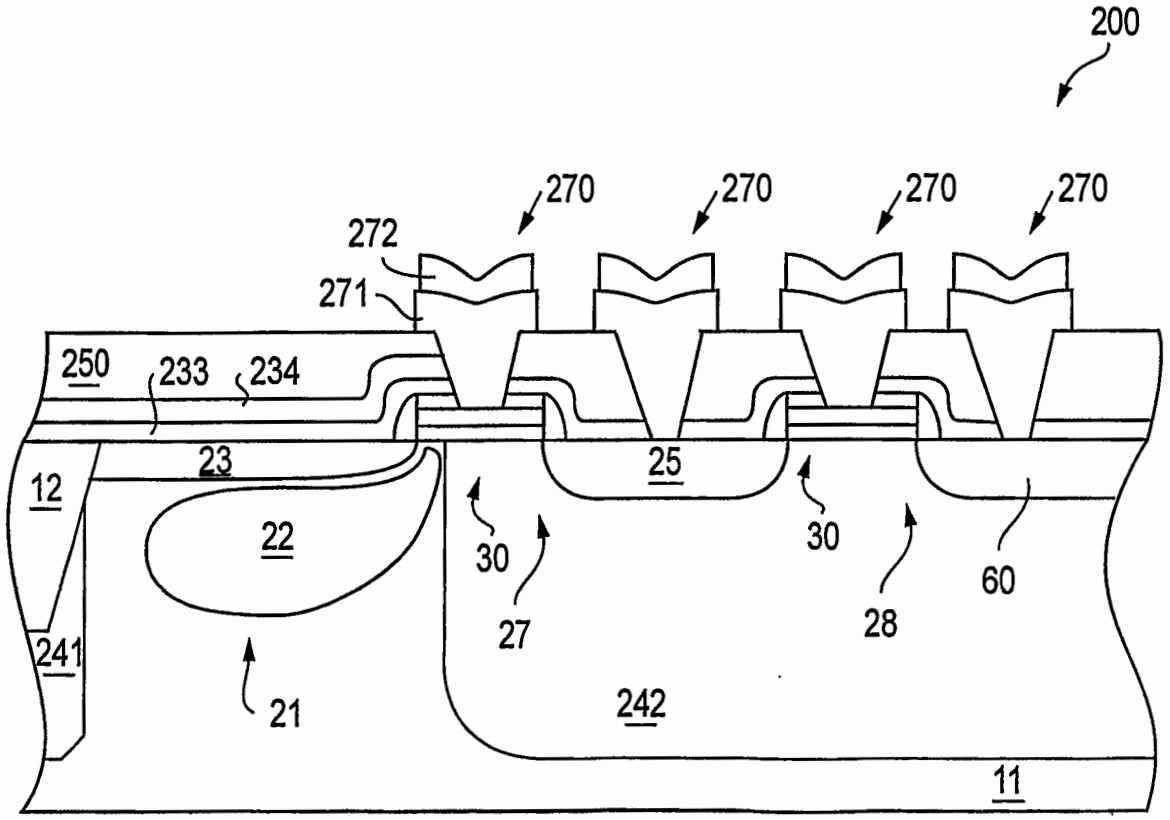

200
270
270
270
270
272
271
250
233
234
23
25
12
22
30
30
60
27
28
241
21
242
11

FIG. 3I

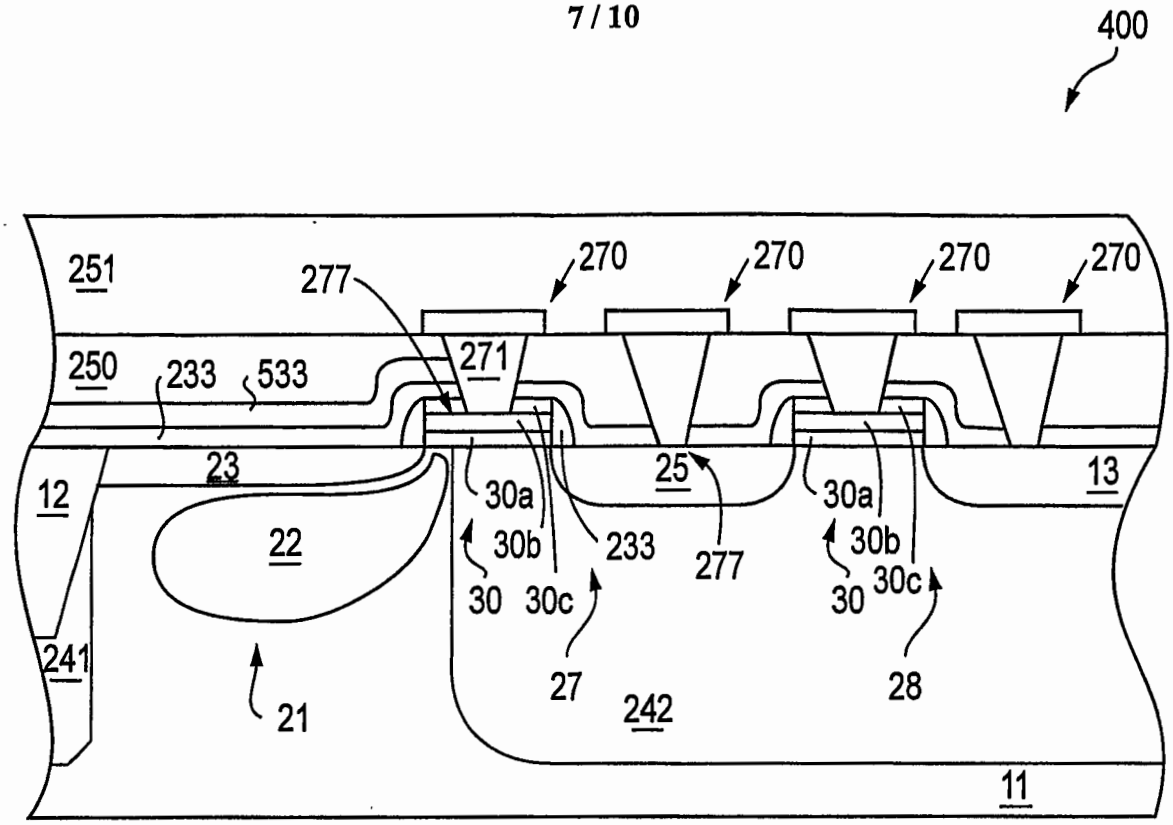

400
251
277
270
270
270
270
250
233
533
271
23
25
13
12
30a
30b
233
277
30a
30b
22
30
30c
30
30c
241
27
28
21
242
11

FIG. 4

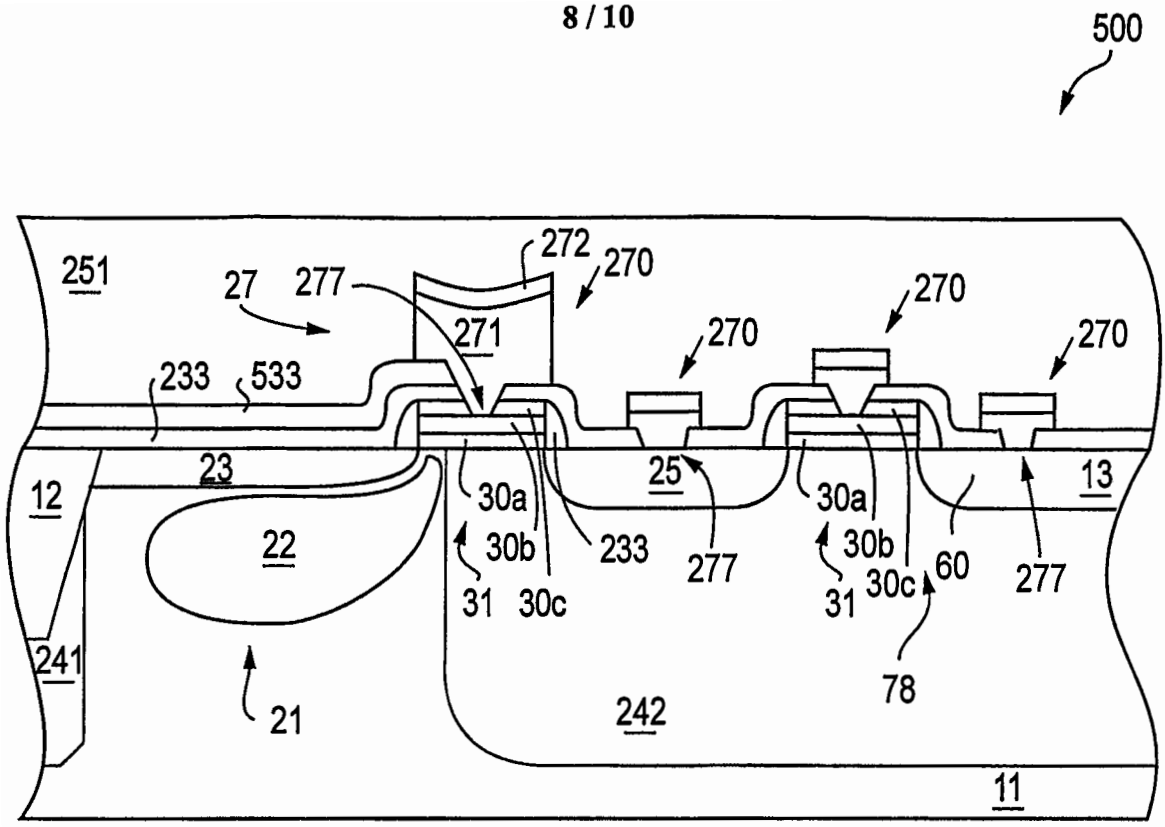

500
251
27
277
272
270
271
270
270
270
233
533
23
25
13
12
30a
30b
233
277
30a
30b
60
277
22
31
30c
31
30c
241
78
21
242
11

FIG. 5

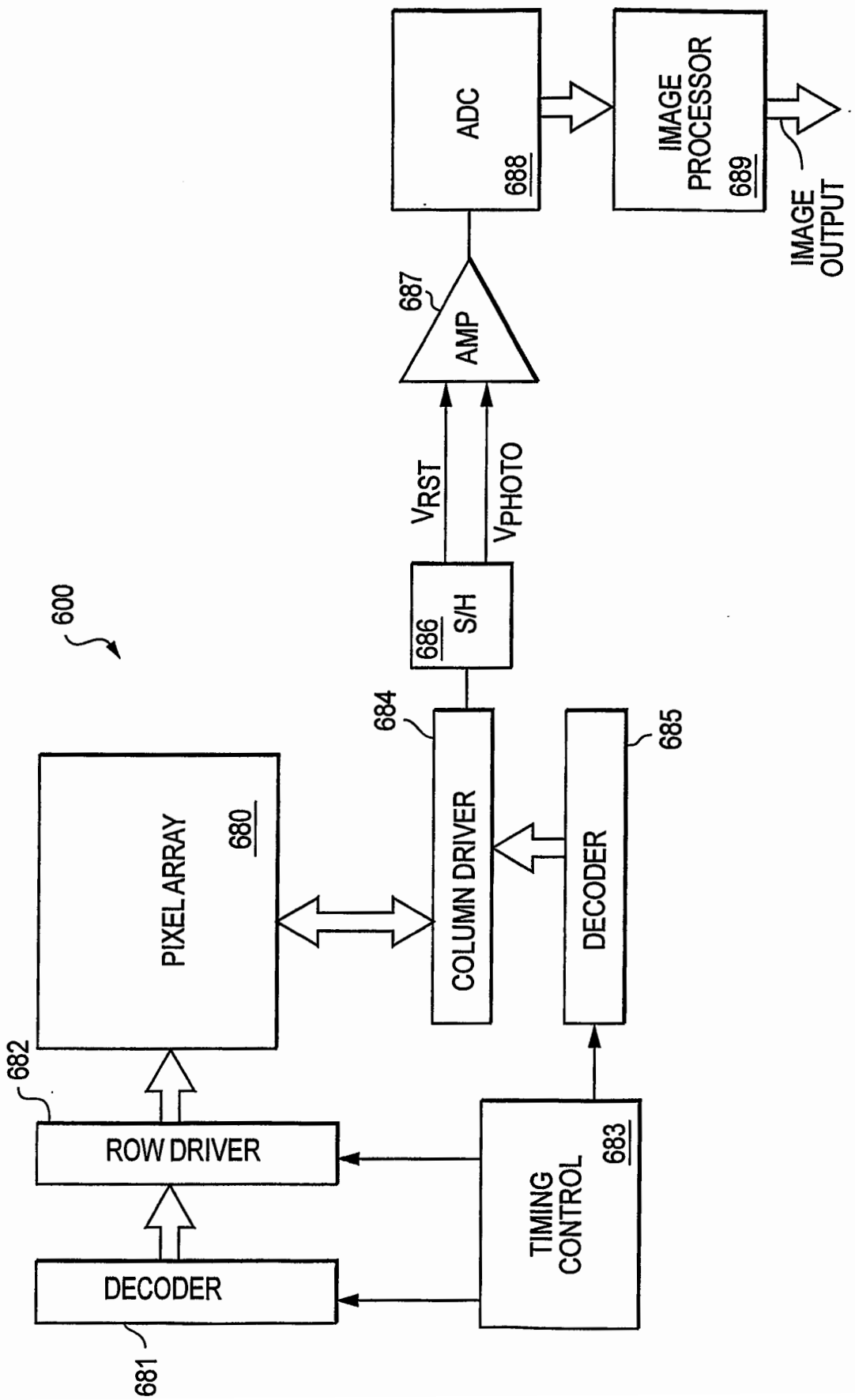

600
PIXEL ARRAY
680
682
ROW DRIVER
DECODER
681
TIMING CONTROL
683
COLUMN DRIVER
684
DECODER
685
S/H
686
AMP
687
VRST
VPHOTO
ADC
688
IMAGE PROCESSOR
689
IMAGE OUTPUT

FIG. 6

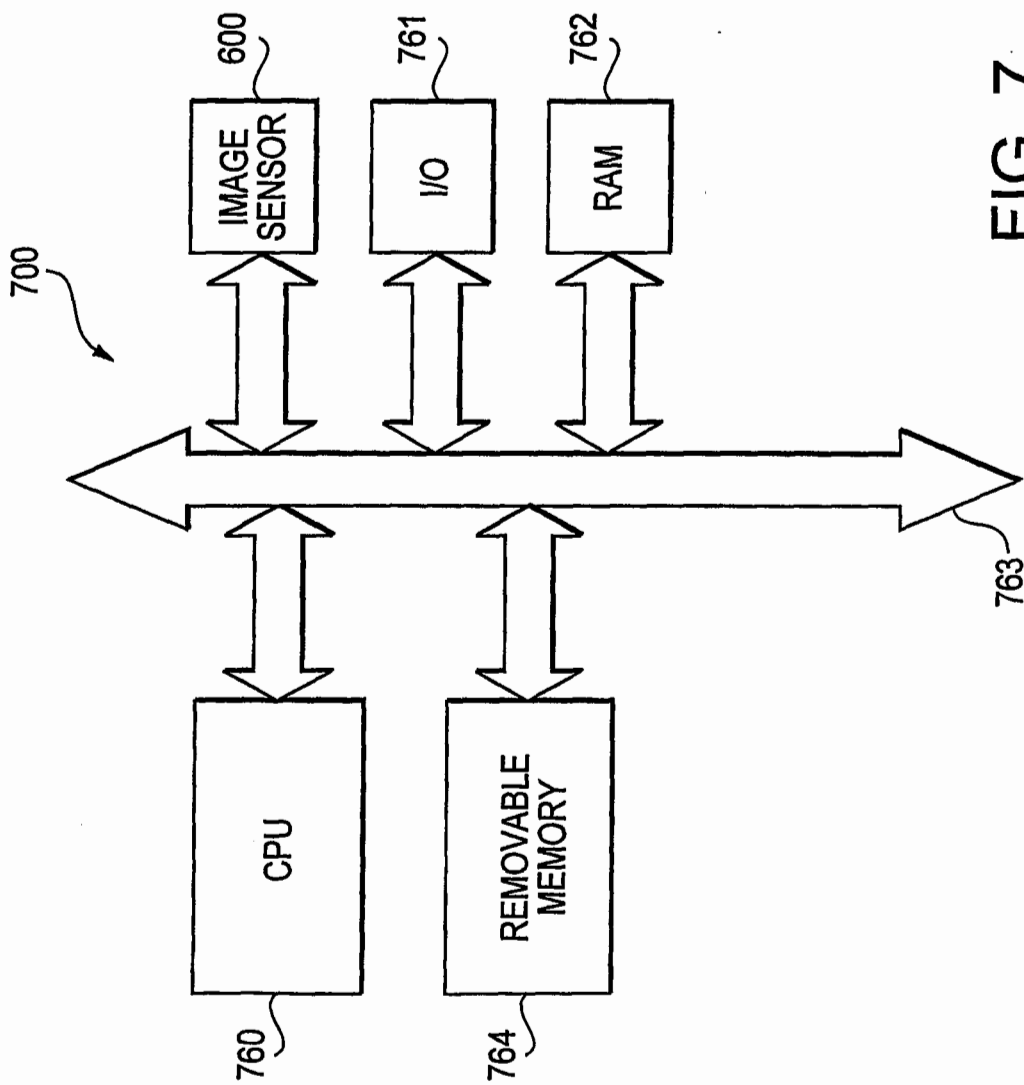

700
600
IMAGE SENSOR
761
I/O
762
RAM
763
CPU
760
REMOVABLE MEMORY
764

FIG. 7