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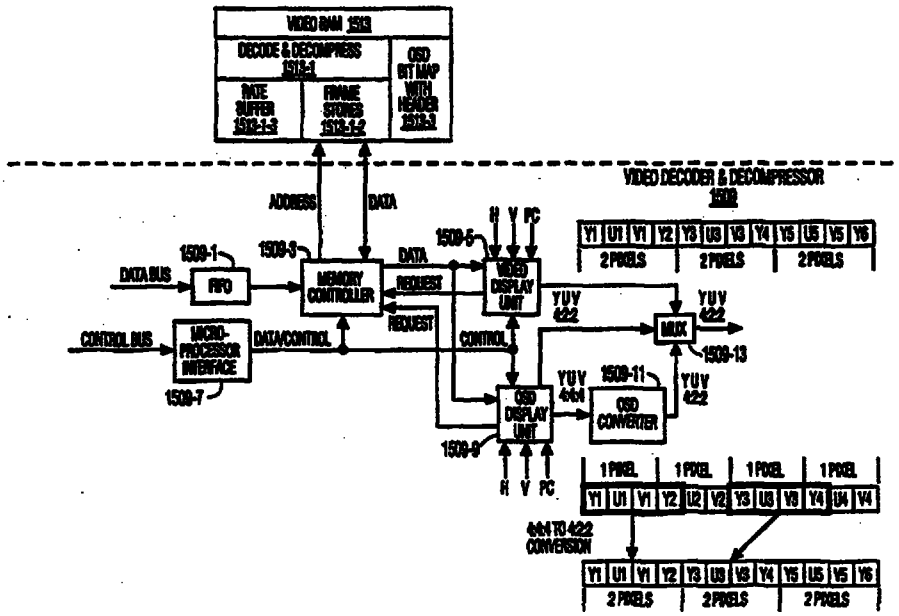
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(54) Title: ON SCREEN DISPLAY ARRANGEMENT FOR A DIGITAL VIDEO SIGNAL PROCESSING SYSTEM



(57) Abstract

A digital video signal processing system receives encoded packets of data representing video image information in compressed form. A video decoder (1509) and an associated video RAM (1513) are used to decode and decompress the video packets to form groups of video image representative digital words. The video decoder (1509) also includes an on-screen display (OSD) unit (1509-9) which converts a graphics bit map stored in a section of the video RAM (1513) to groups of graphic image representative digital words. The image representative digital words and the graphic representative digital words are multiplexed together under the control of the OSD display unit (1509-9).

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