

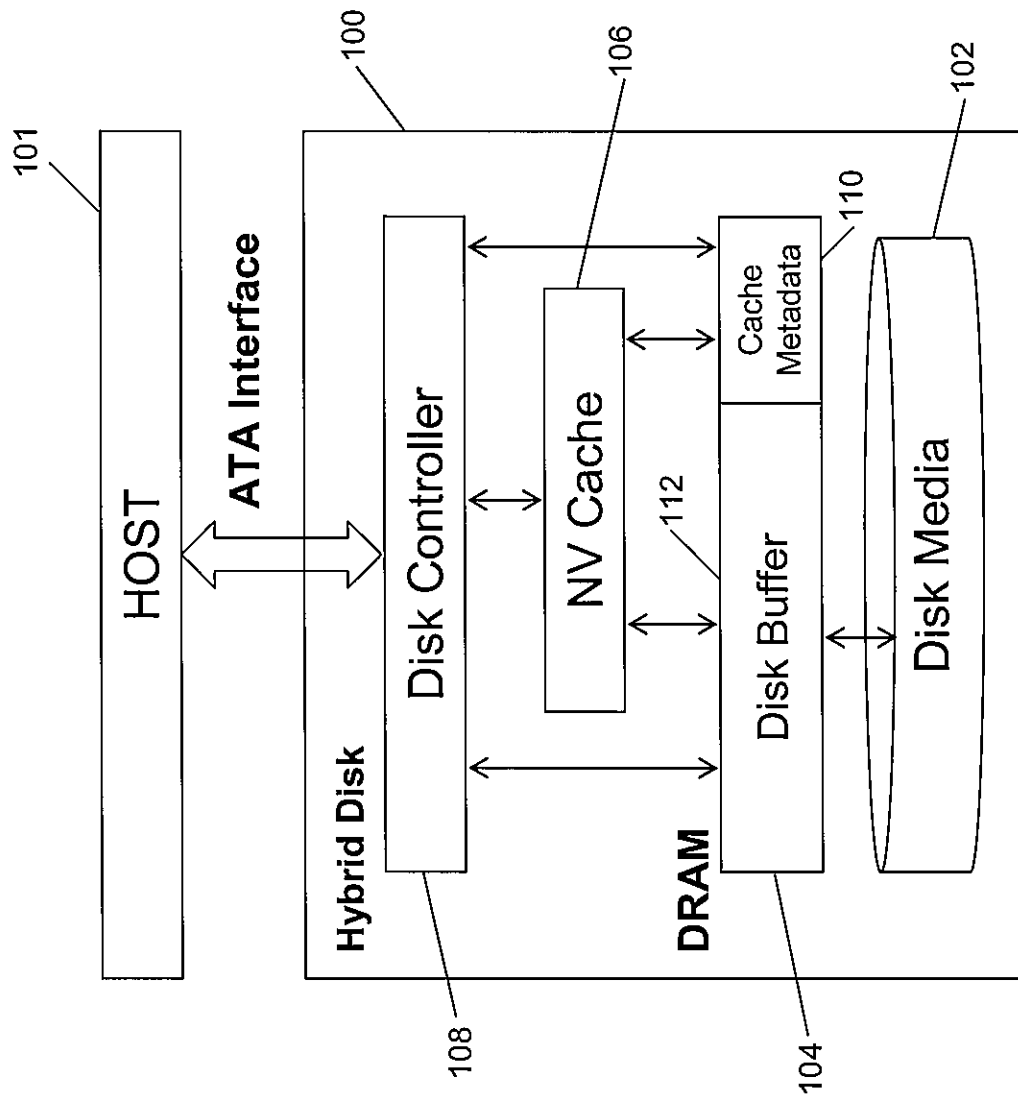


FIG. 1

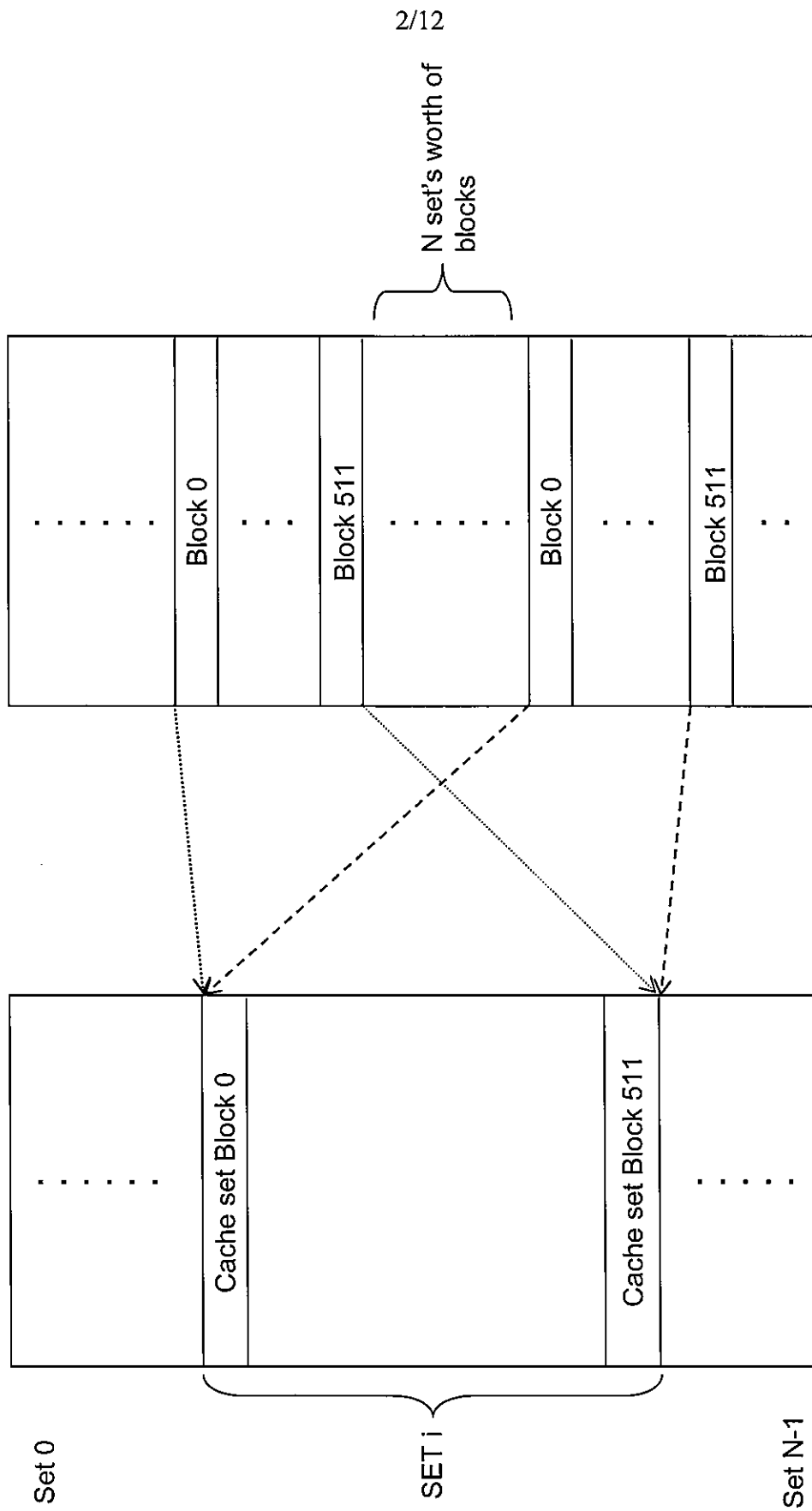


FIG. 2

CBA 0

CBA max 2²¹

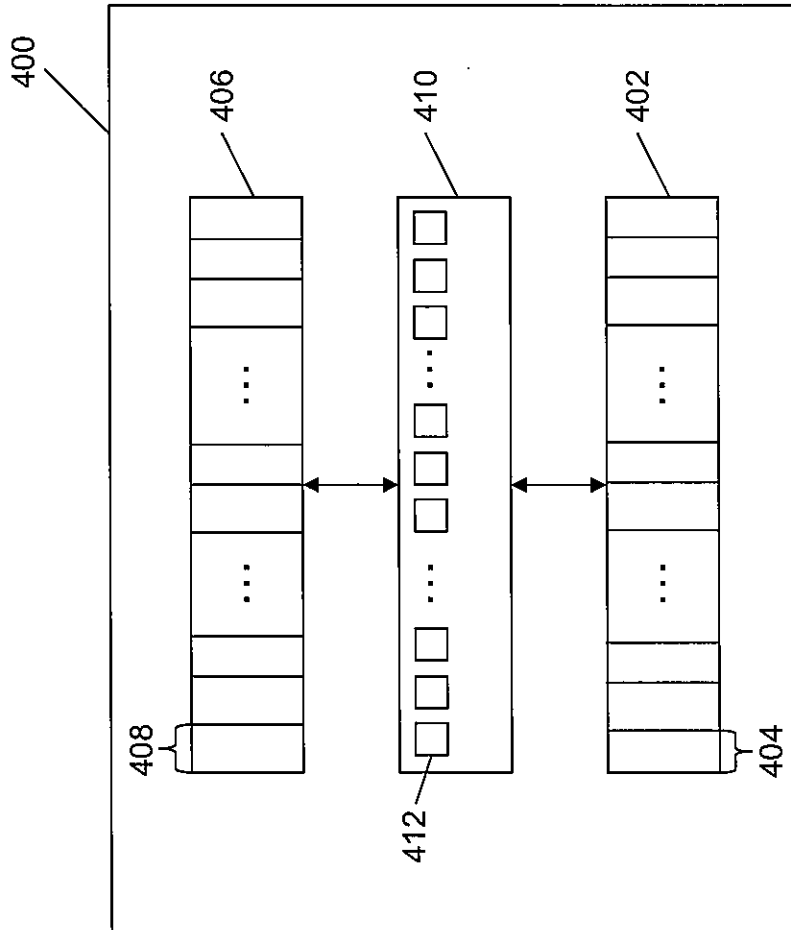


FIG. 4

500

	502	504	506
	Cache Block Address (21 bits for 8GB)	Status (2 bits)	Padding (1 bit)
LBA 0	CBA i	clean	
			
		dirty	
			
LAB max 2 ²⁸	CBA j	free	

FIG. 5

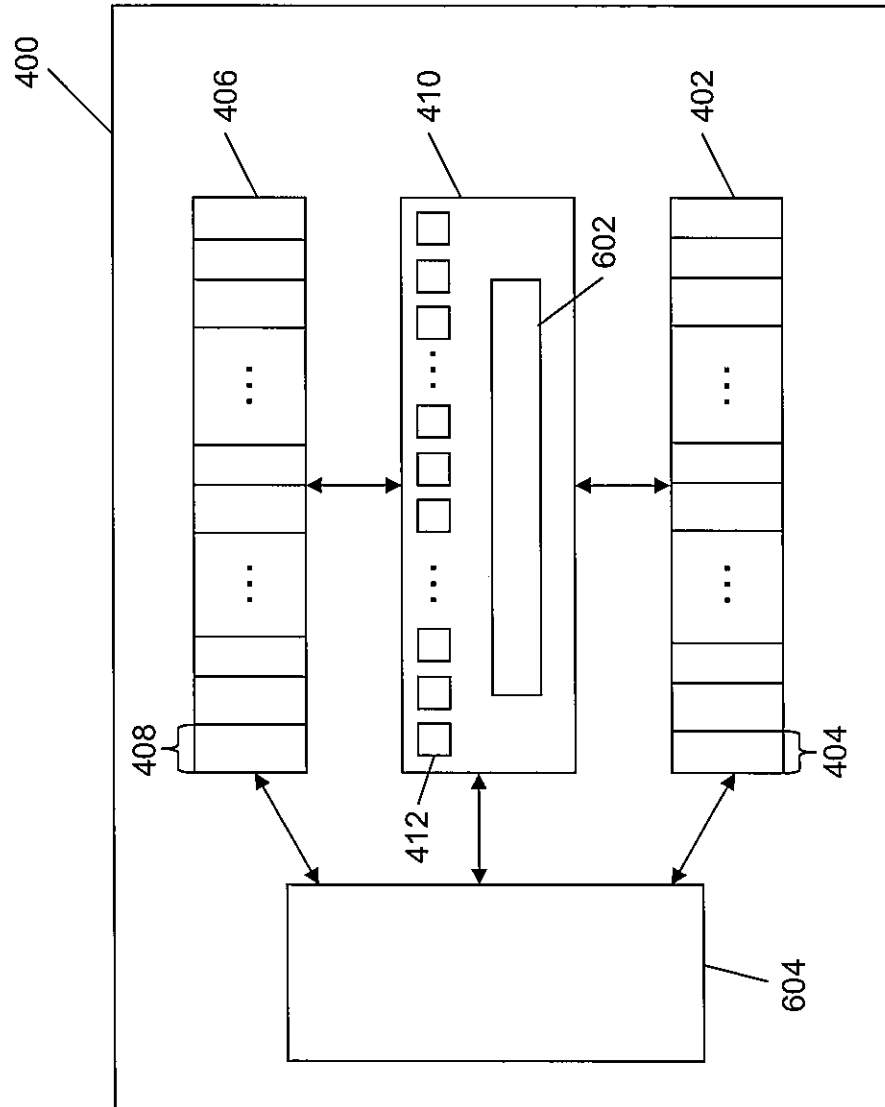


FIG. 6

700 ↘

2 bits for CBA 0 status		
.....		
.....		
.....		
.....		2 bits for CBA max 2 ²¹ status

FIG. 7

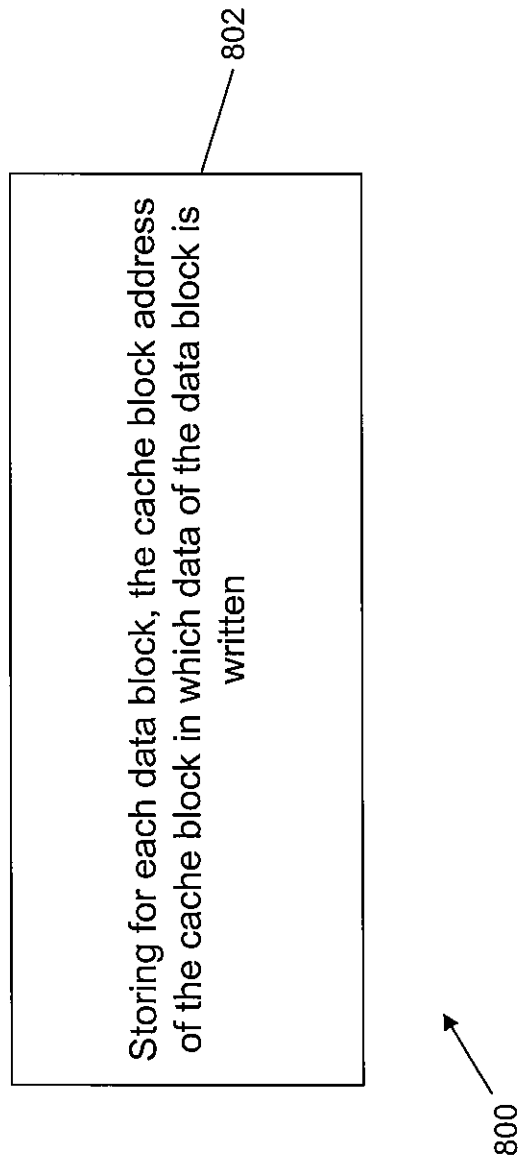


FIG. 8

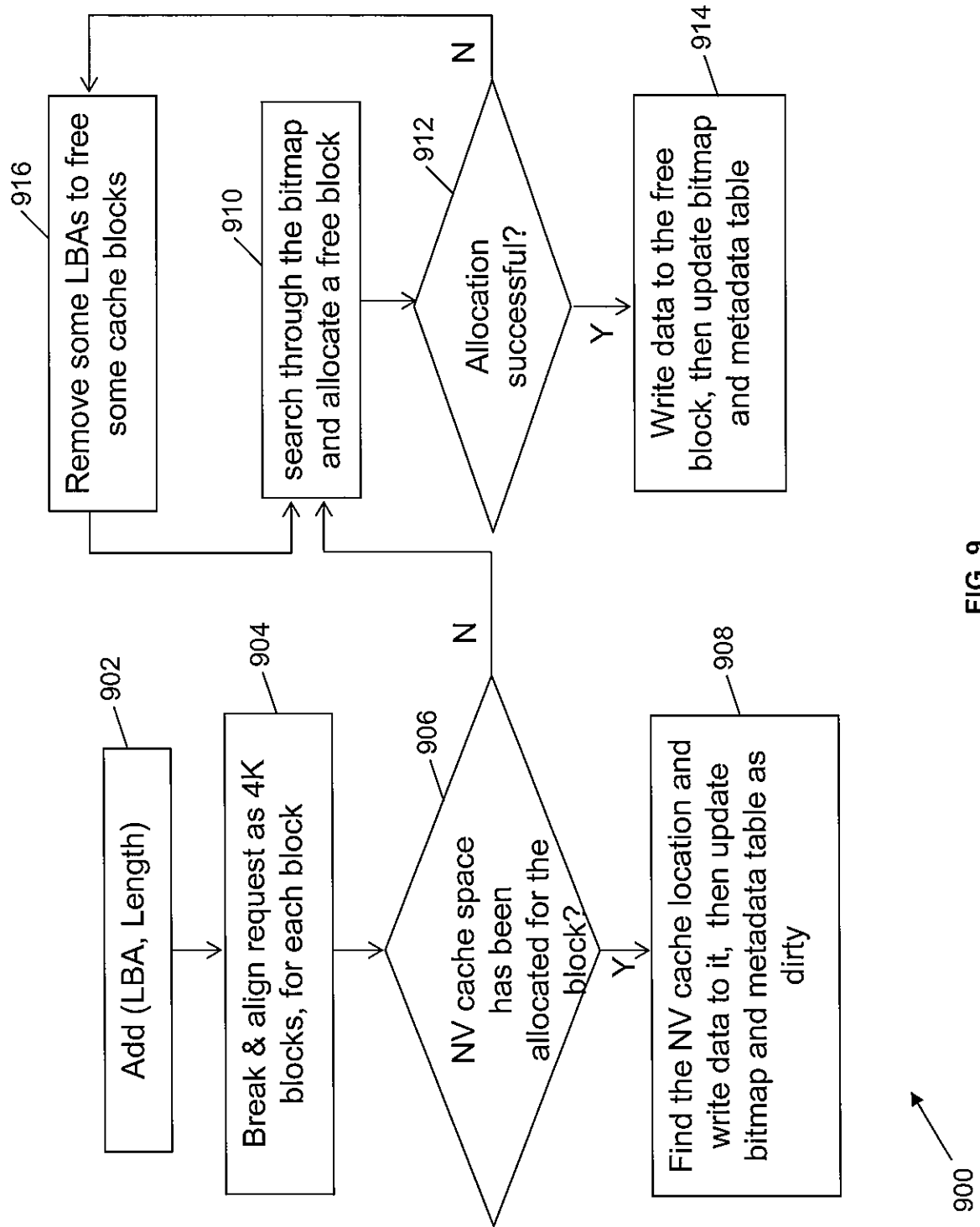


FIG. 9

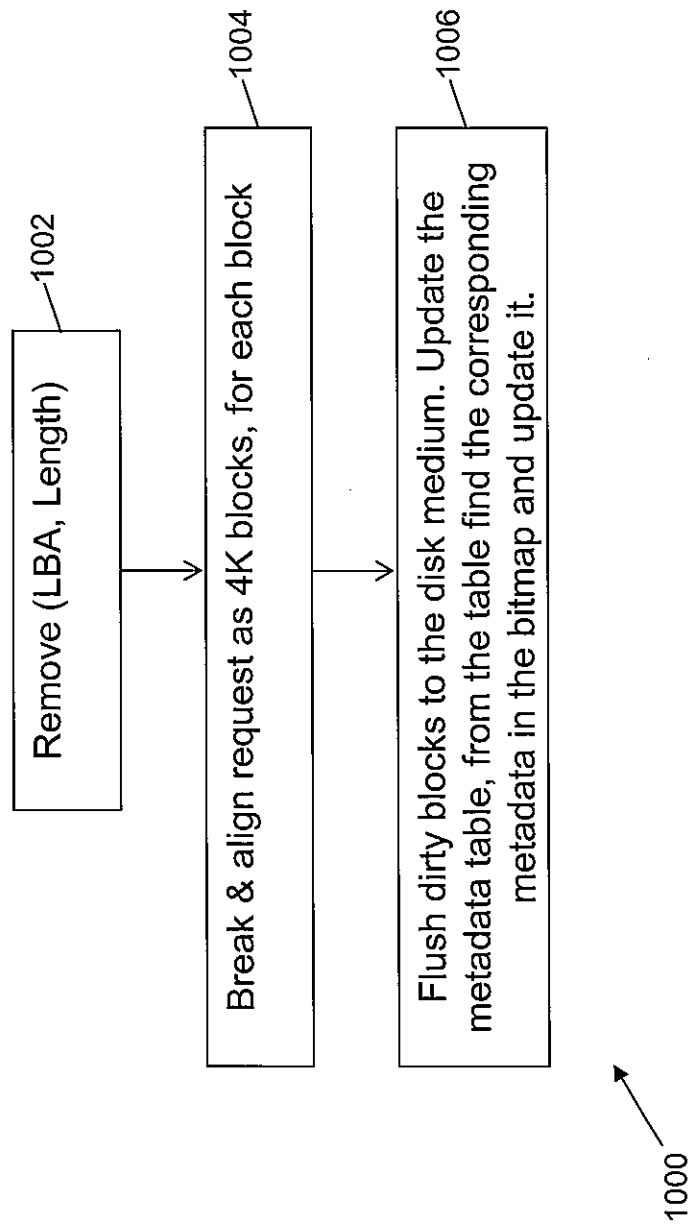


FIG. 10

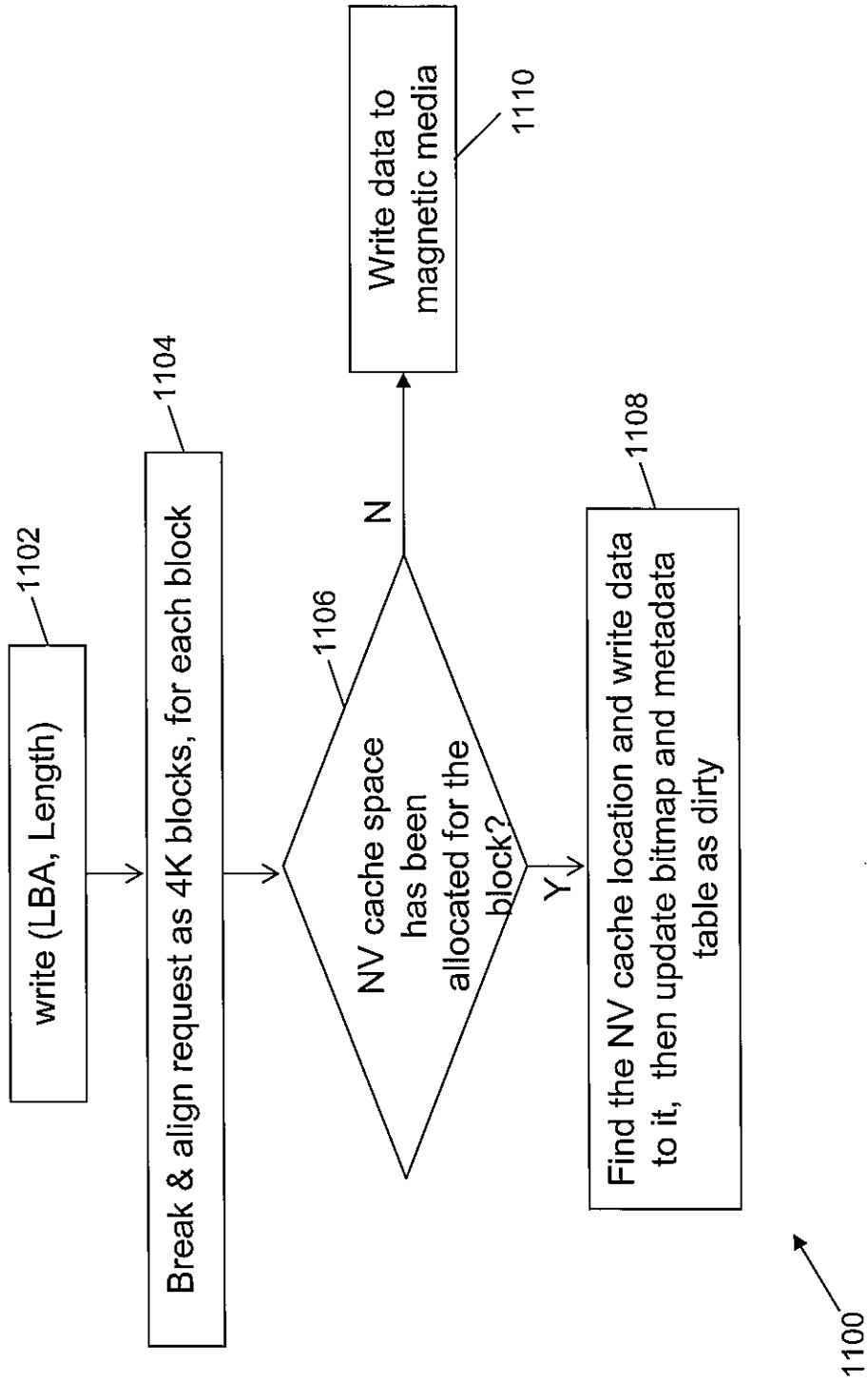


FIG. 11

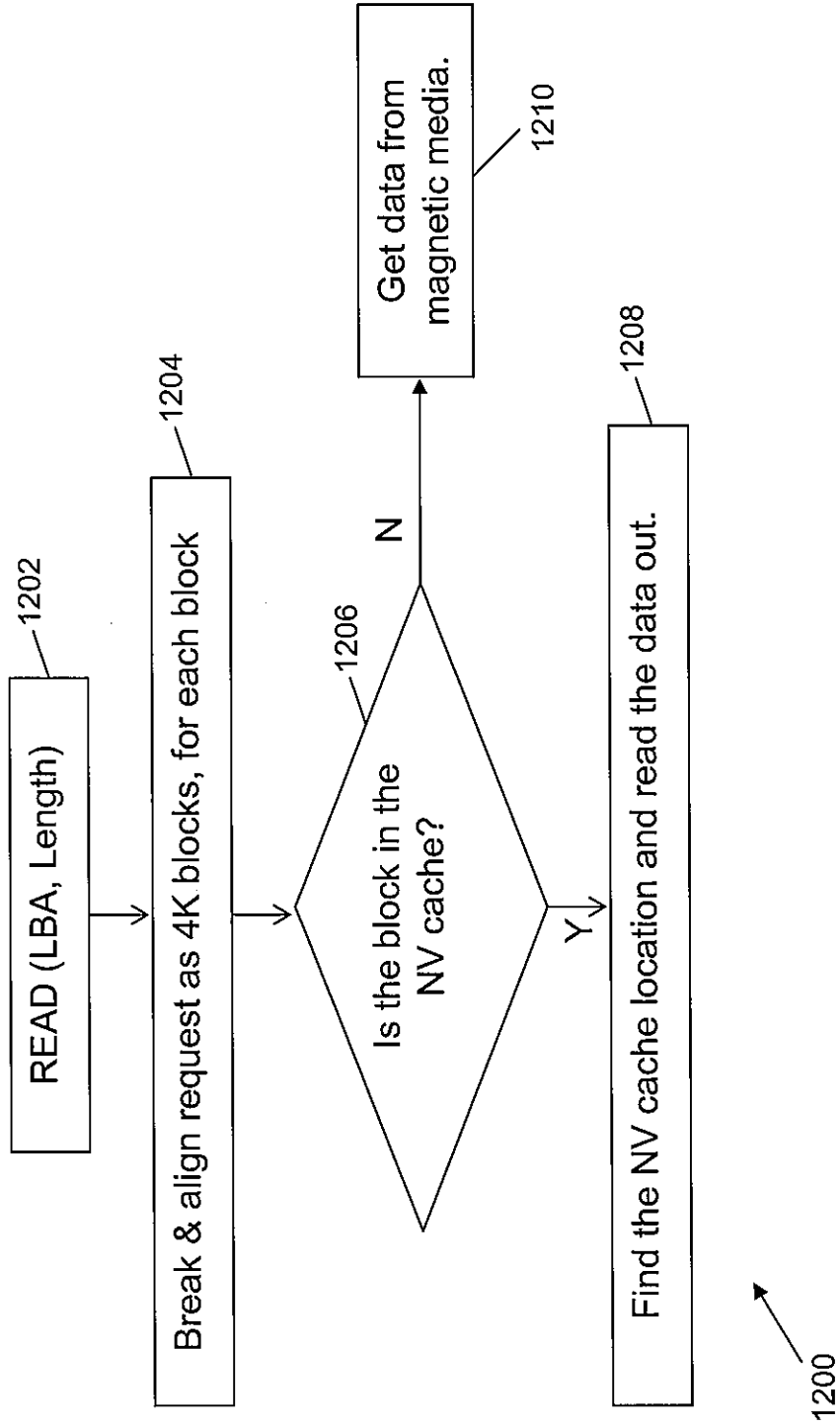


FIG. 12