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(57) Abstract

A leading read apparatus (401) and a trailing read apparatus (403) include all of the necessary front-end electronics to provide a square wave output. Each of the leading read apparatus (401) and the trailing read apparatus (403) includes a current amplifier (403) and a zero crossing detector (420). The processing device (404) includes a transition detector (406), a counter (412), and a second transition detector (406). The processing device (404) is connected to a memory (410).

