

A LOW BIAS VOLTAGE WRITE DRIVER**ABSTRACT OF THE DISCLOSURE**

A low head bias voltage write driver is described. The present invention comprises a positive supply terminal coupled to a first terminal of a first current source. A first write driver head terminal is coupled to the second terminal of the first current source. A first terminal of a first switch is coupled to the first write driver head terminal. The second terminal of the first switch is coupled to the negative supply terminal. A second current source is coupled to the positive supply terminal. A second write driver head terminal is coupled to the second terminal of the second current source. A first terminal of a second switch is coupled to the second write driver head terminal. The second terminal of the second switch is coupled to the negative supply terminal.

Figure 5