INTEGRATED HIGH-PERFORMANCE DECOUPLING CAPACITOR

ABSTRACT

An integrated high-performance decoupling capacitor, formed on a semiconductor chip (10), using the substrate of (10) the chip itself in conjunction with a metallic deposit (34) formed on the presently unused chip back surface (14) and electrically connected to the active chip circuit to result in a significant and very effective decoupling capacitor in close proximity to the active circuit on the chip requiring such decoupling capacitance.

Fig. 1

This Sheet Has Been Replaced



THIS SHEET HAS BEEN REPLACED