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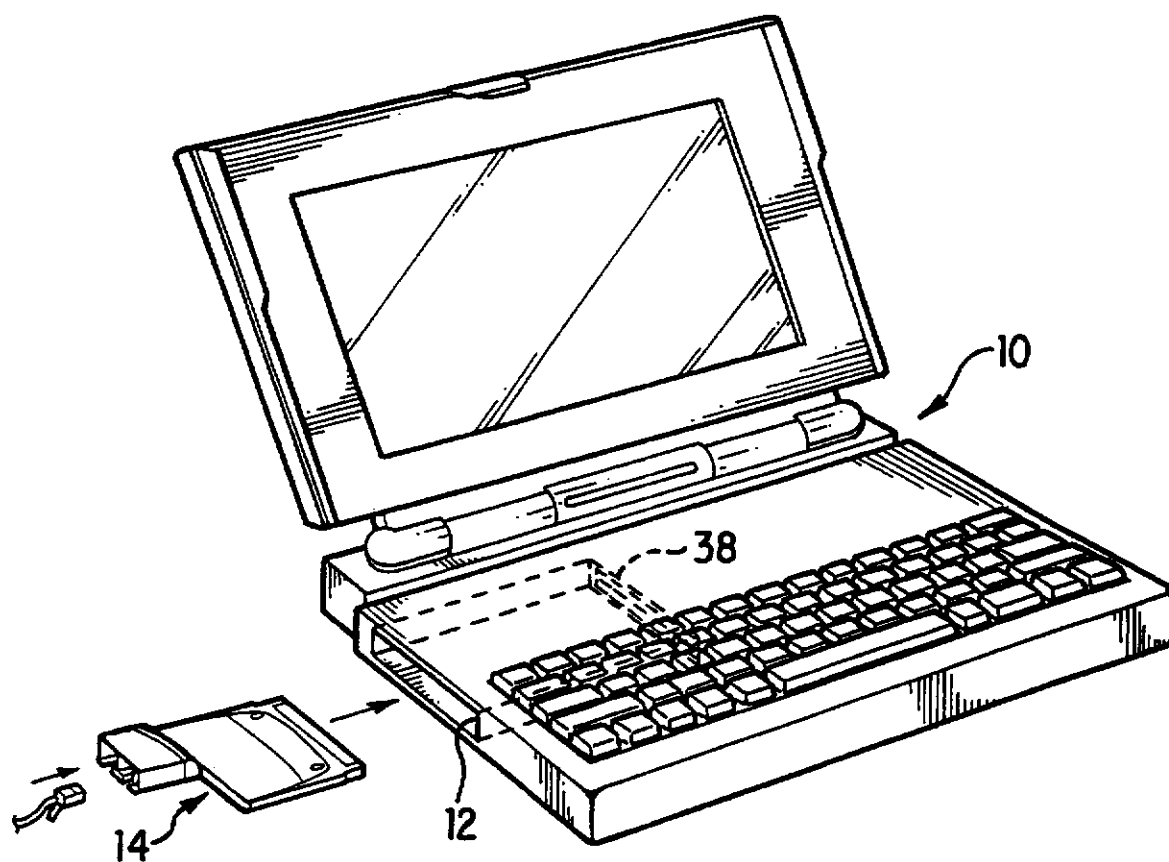
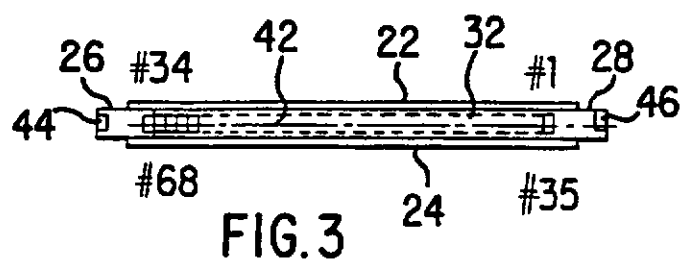
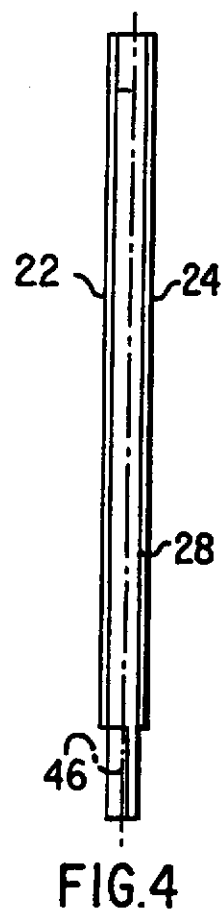
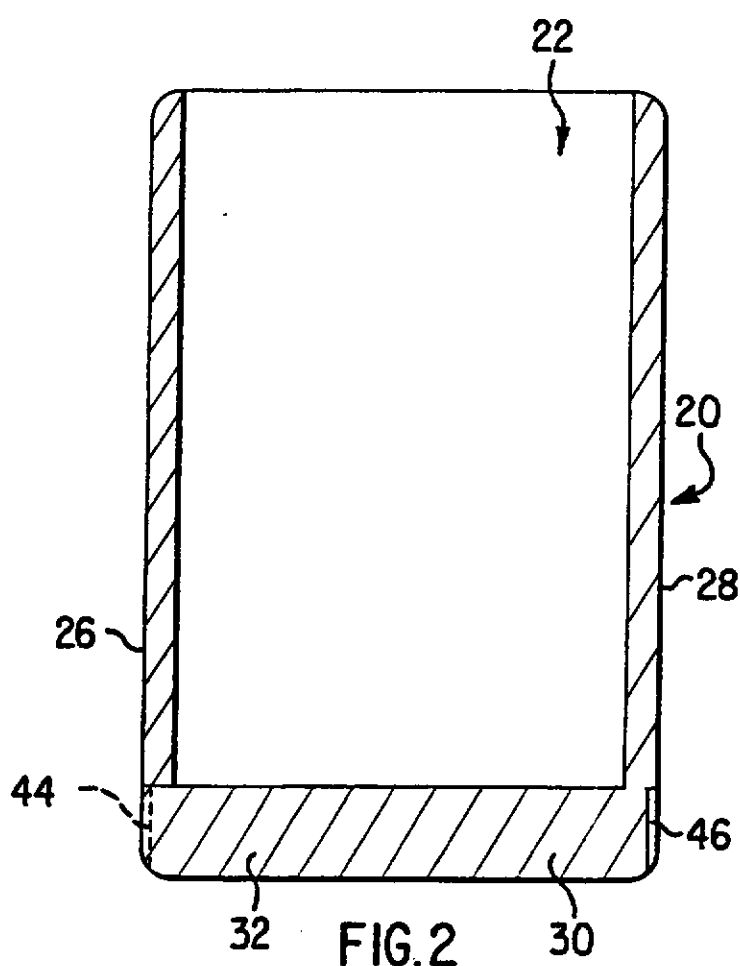
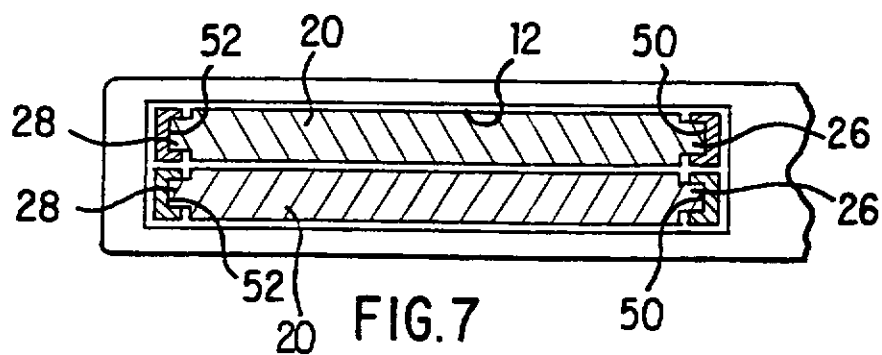
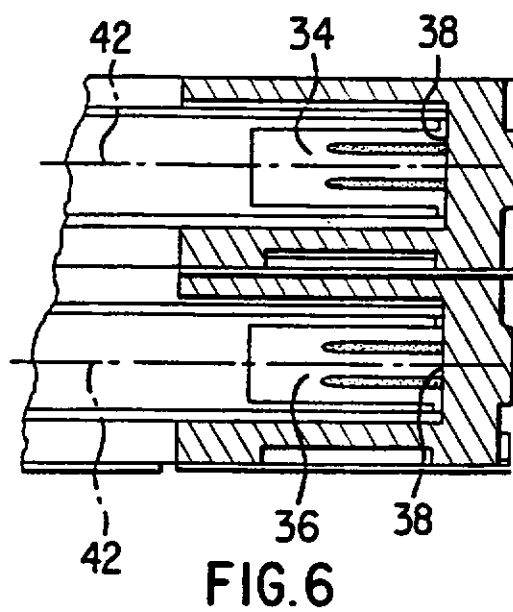
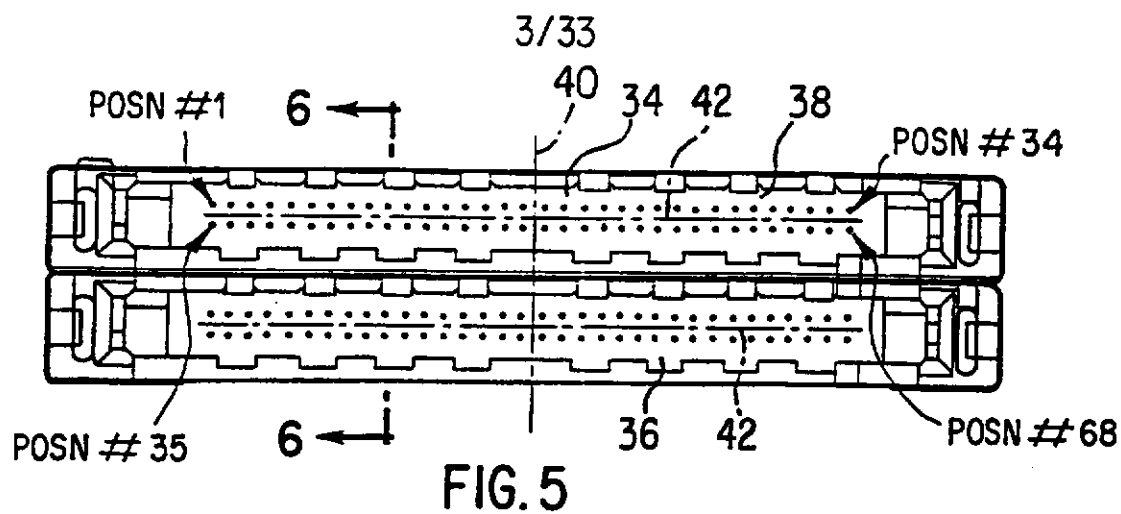


FIG. 1

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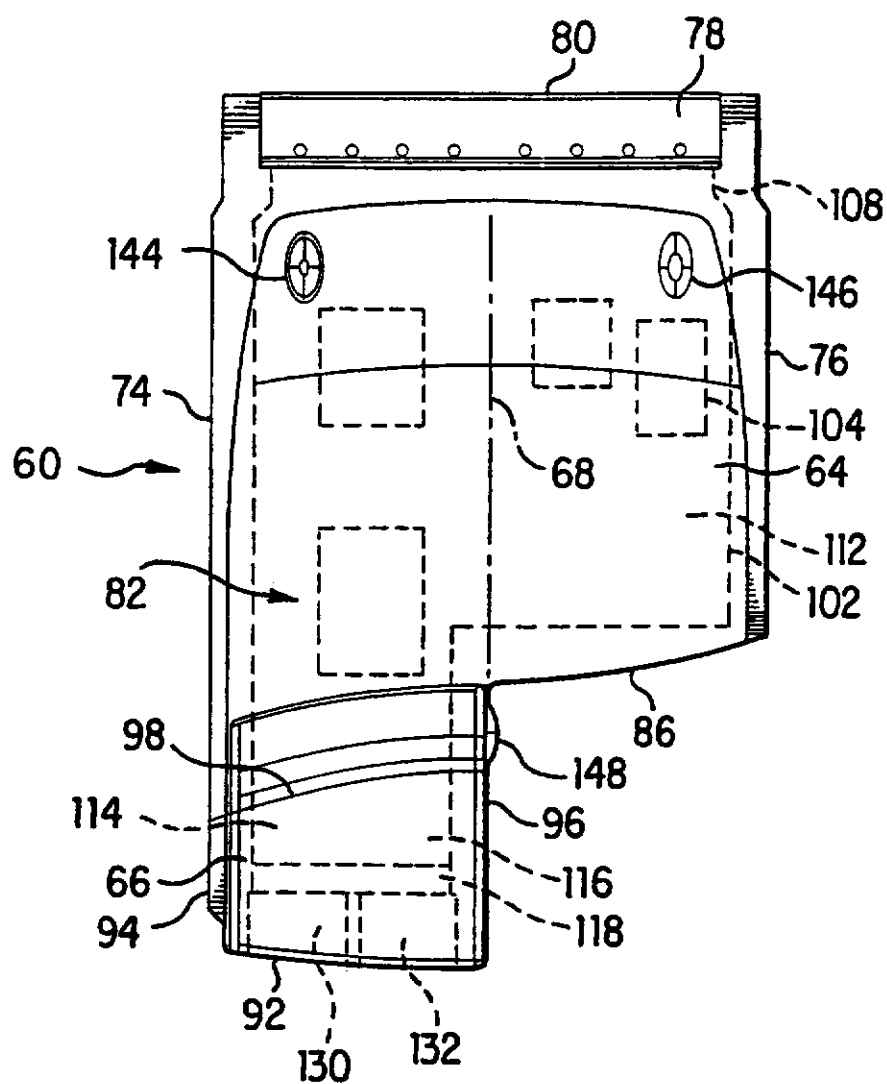
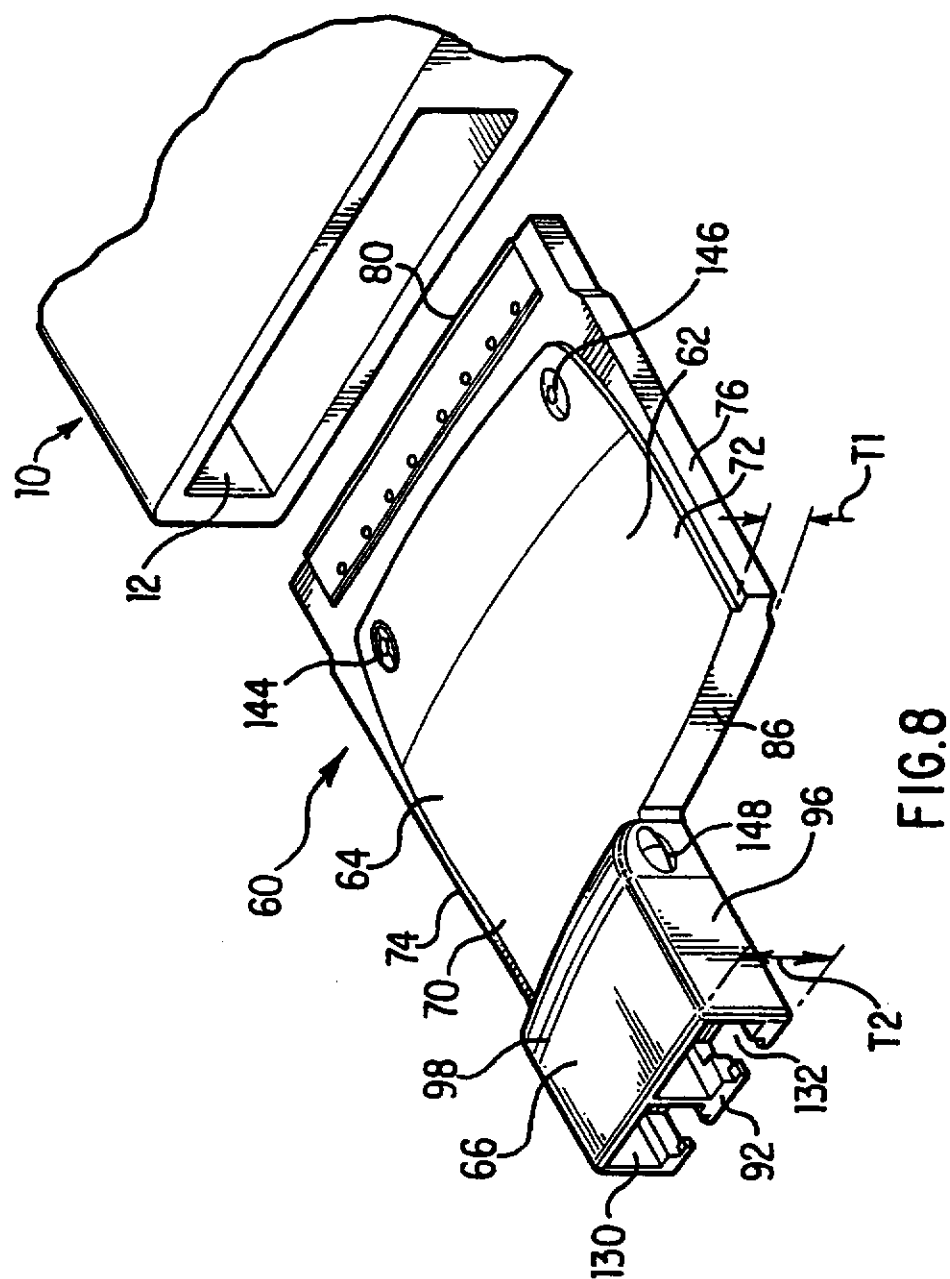
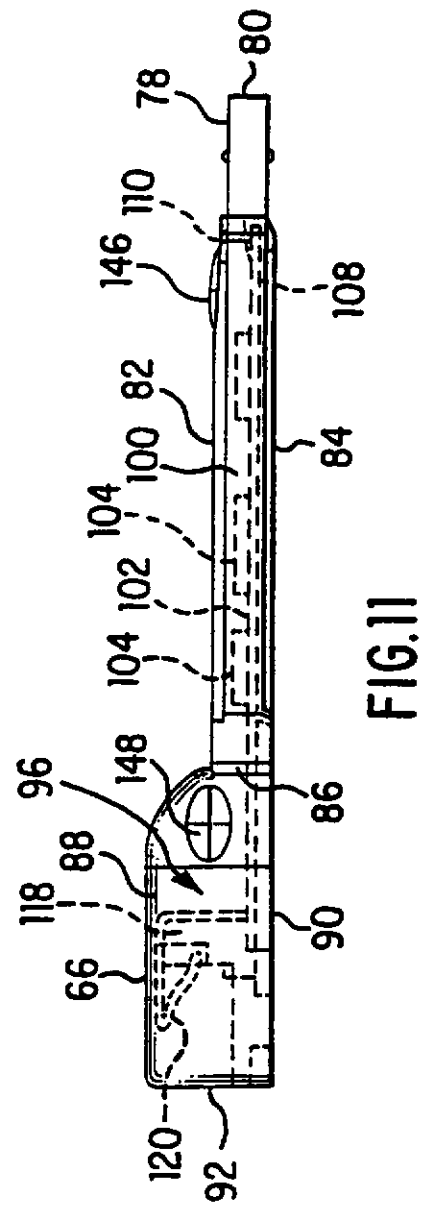
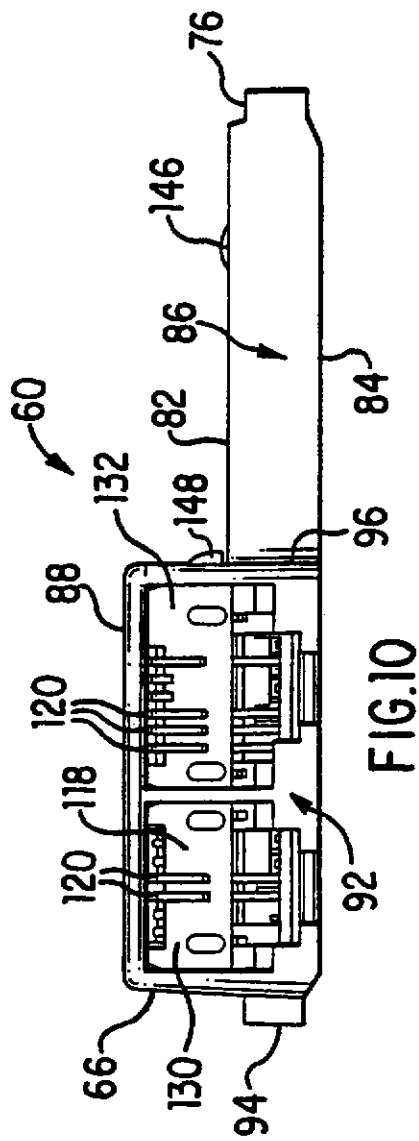


FIG. 9



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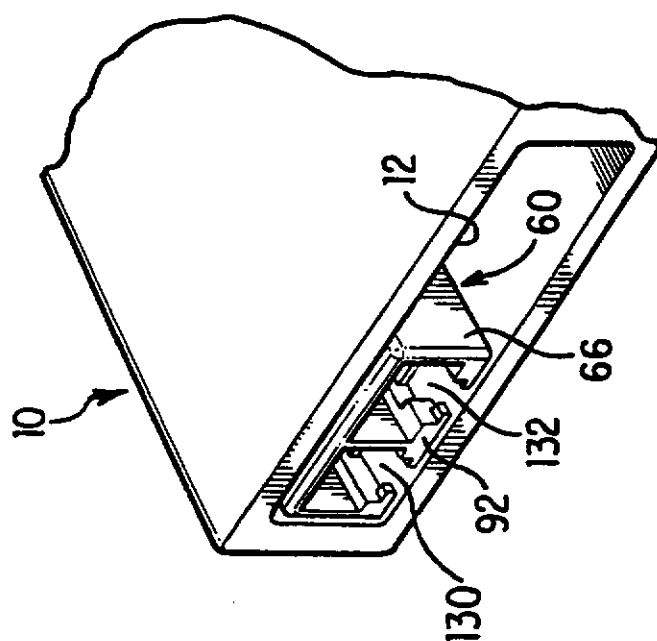


FIG. 12

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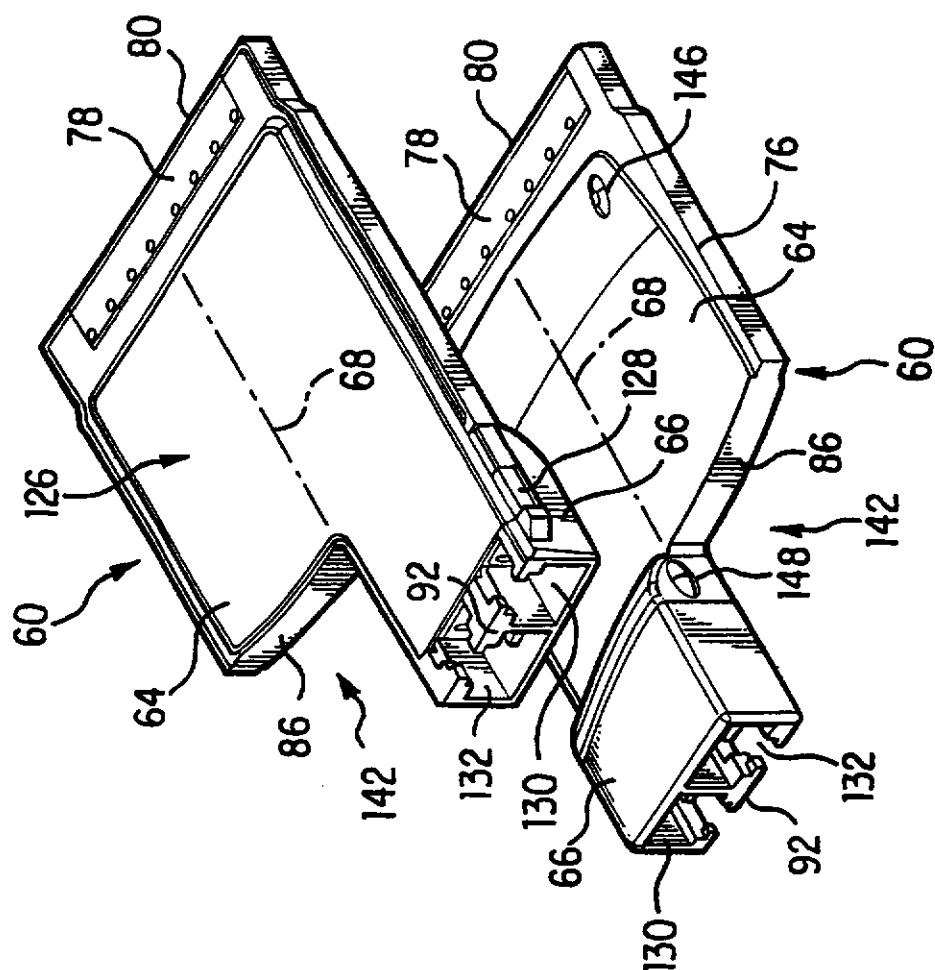
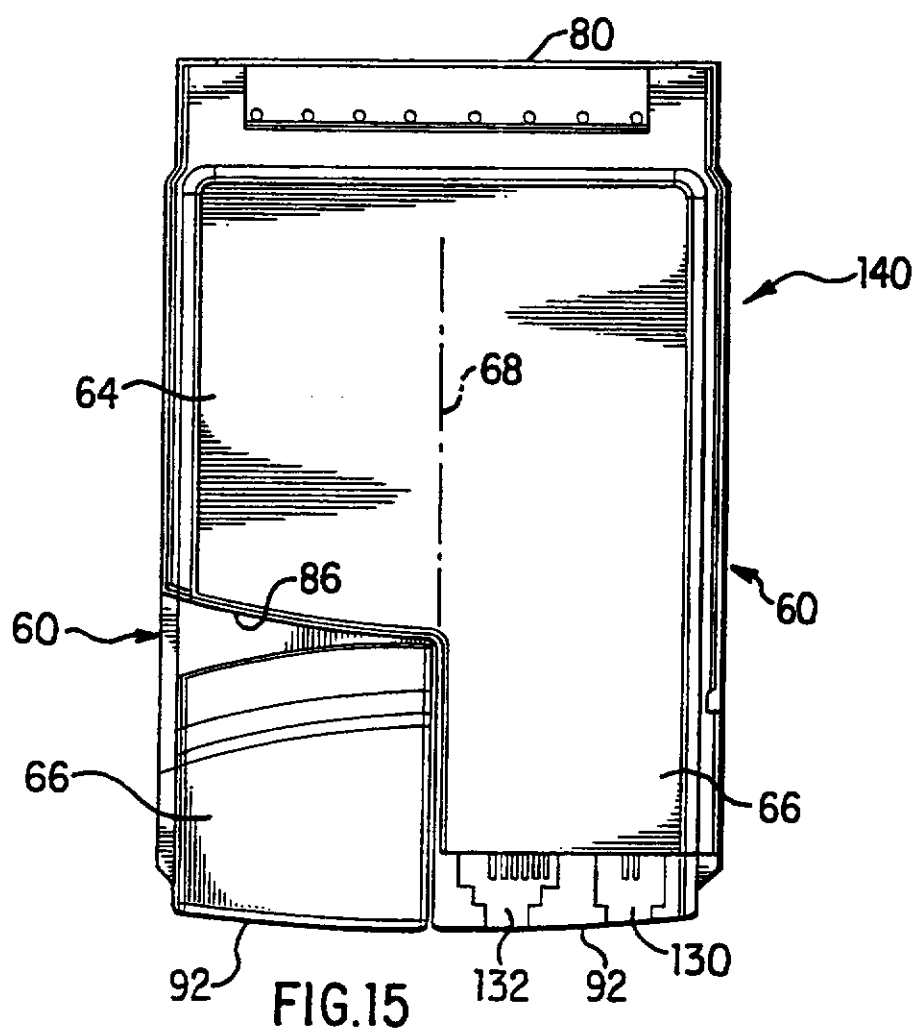
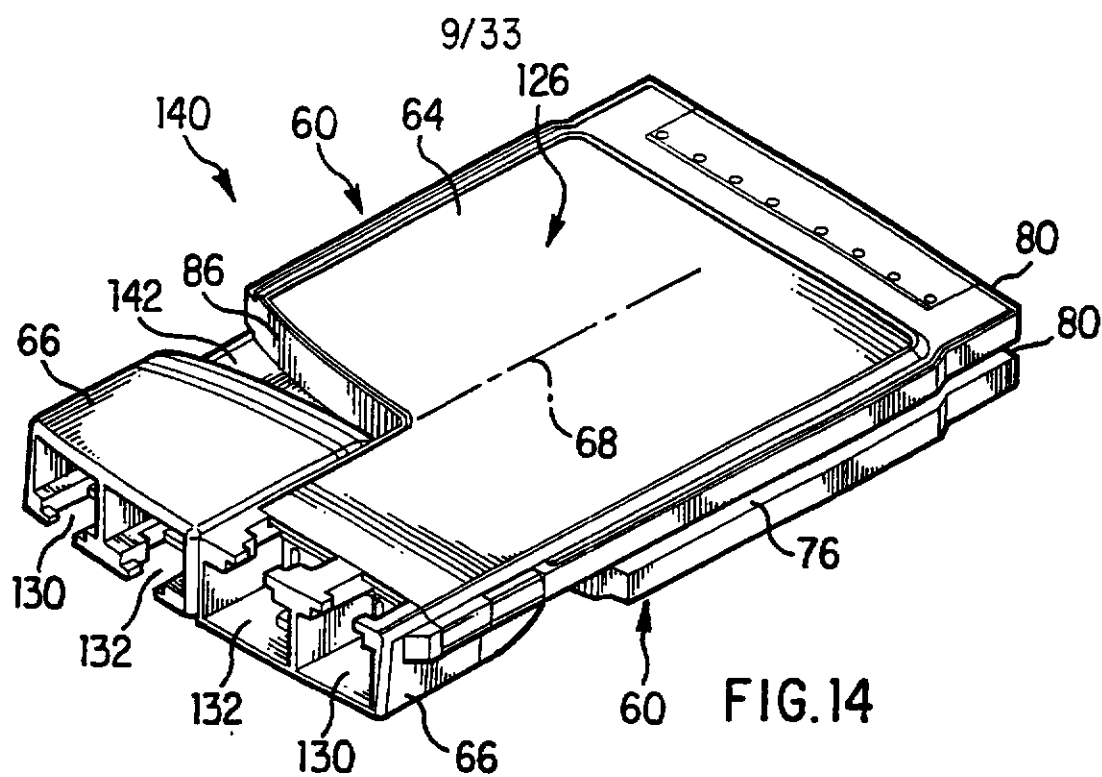
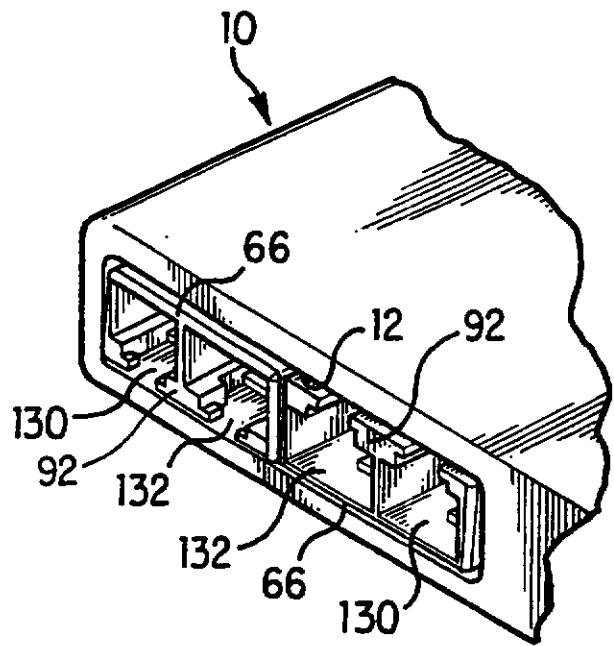
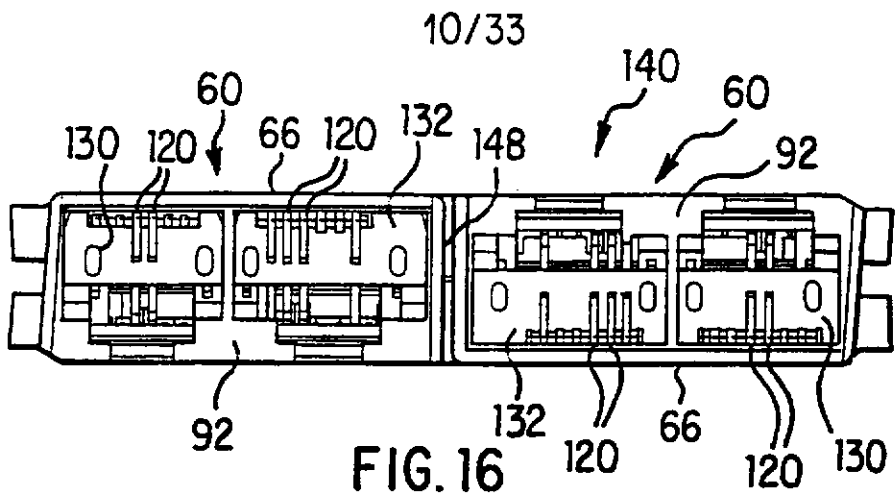
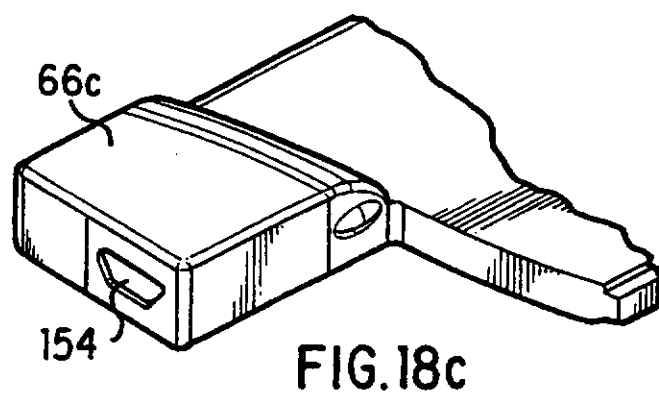
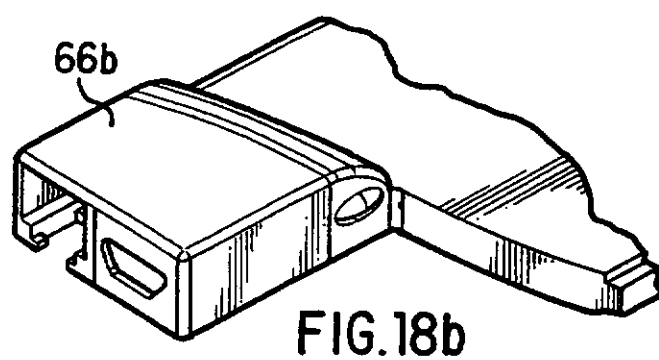
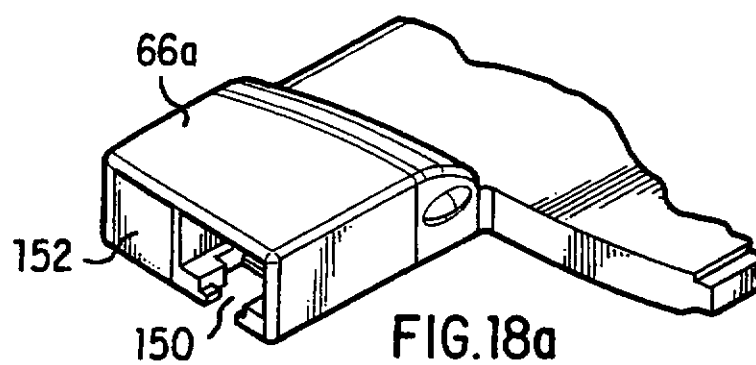


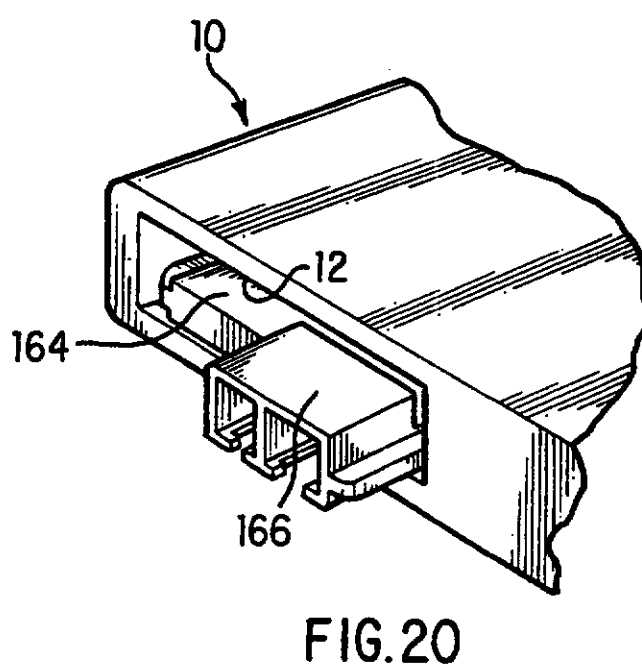
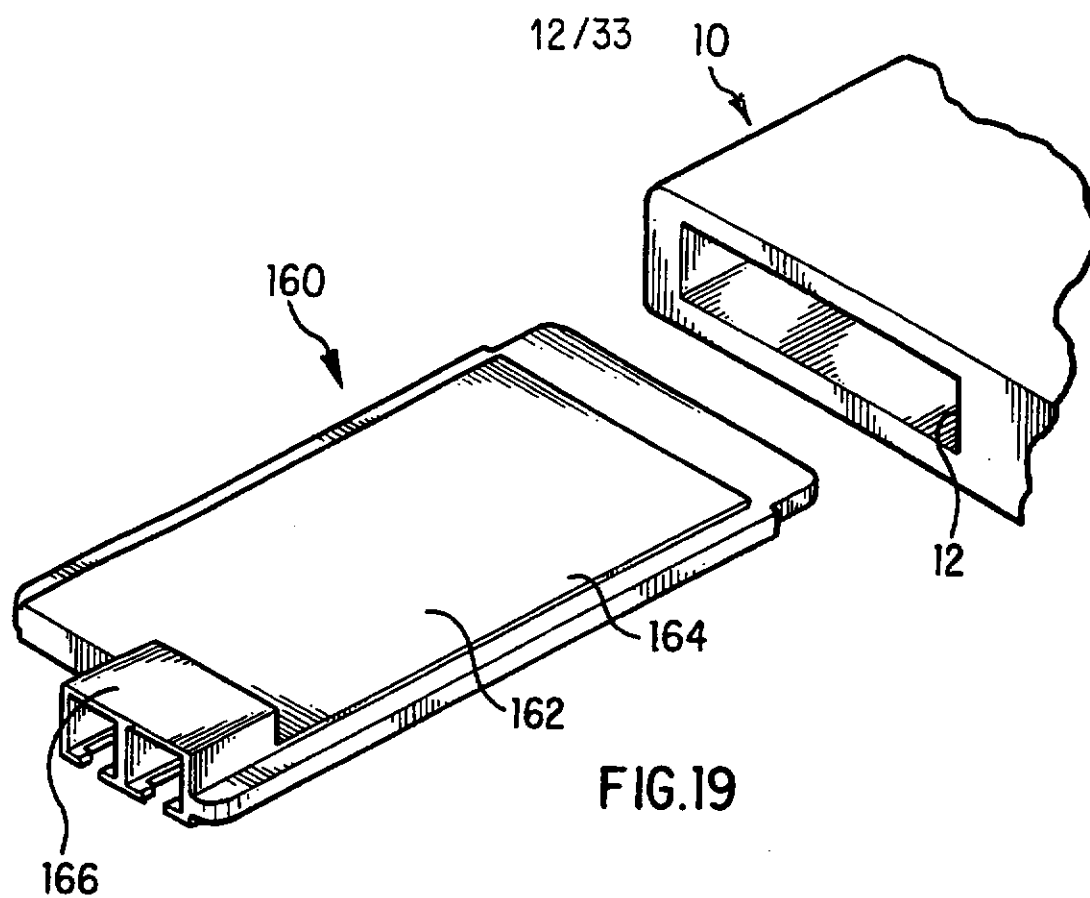
FIG. 13





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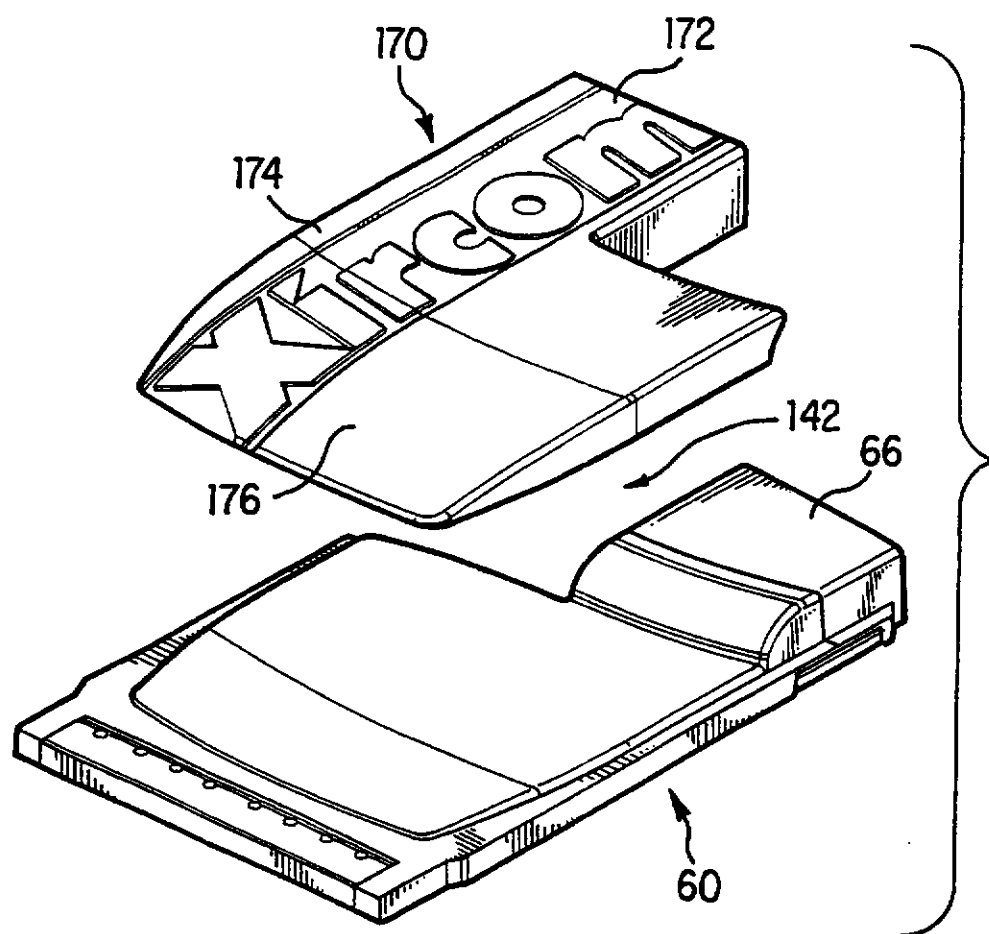


FIG. 21

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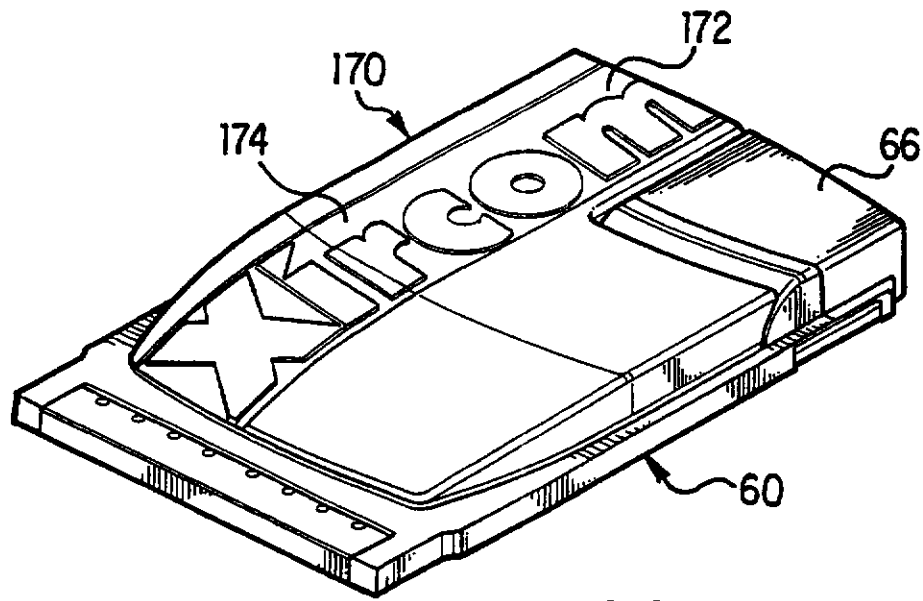


FIG. 22

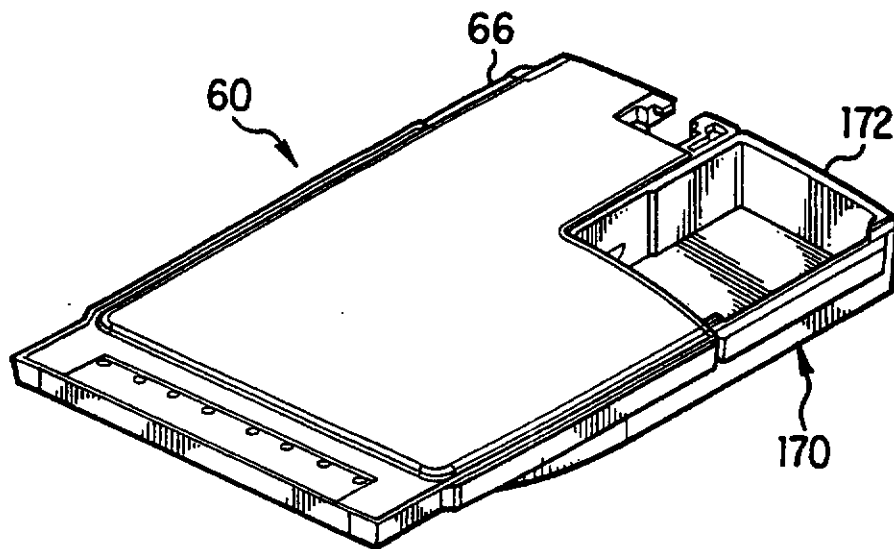
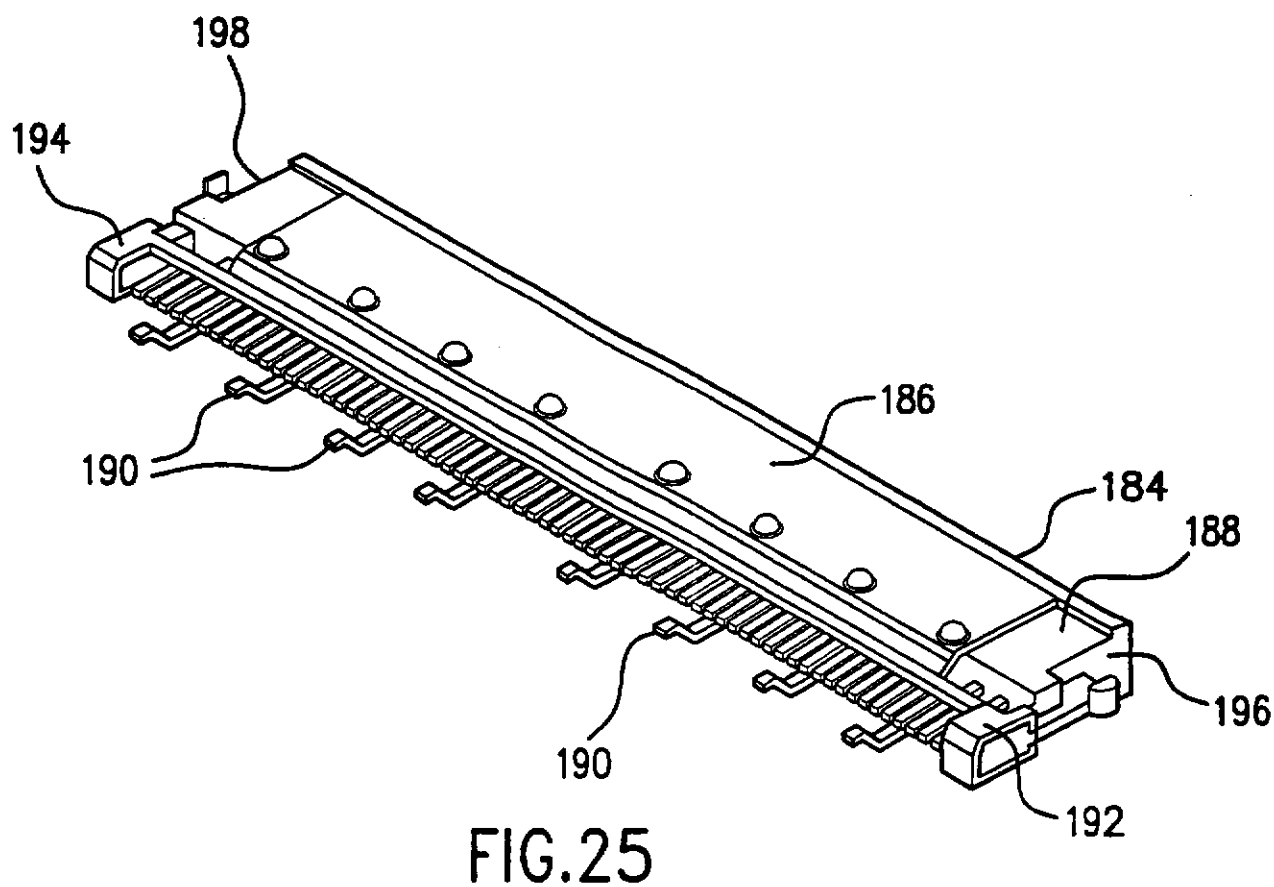
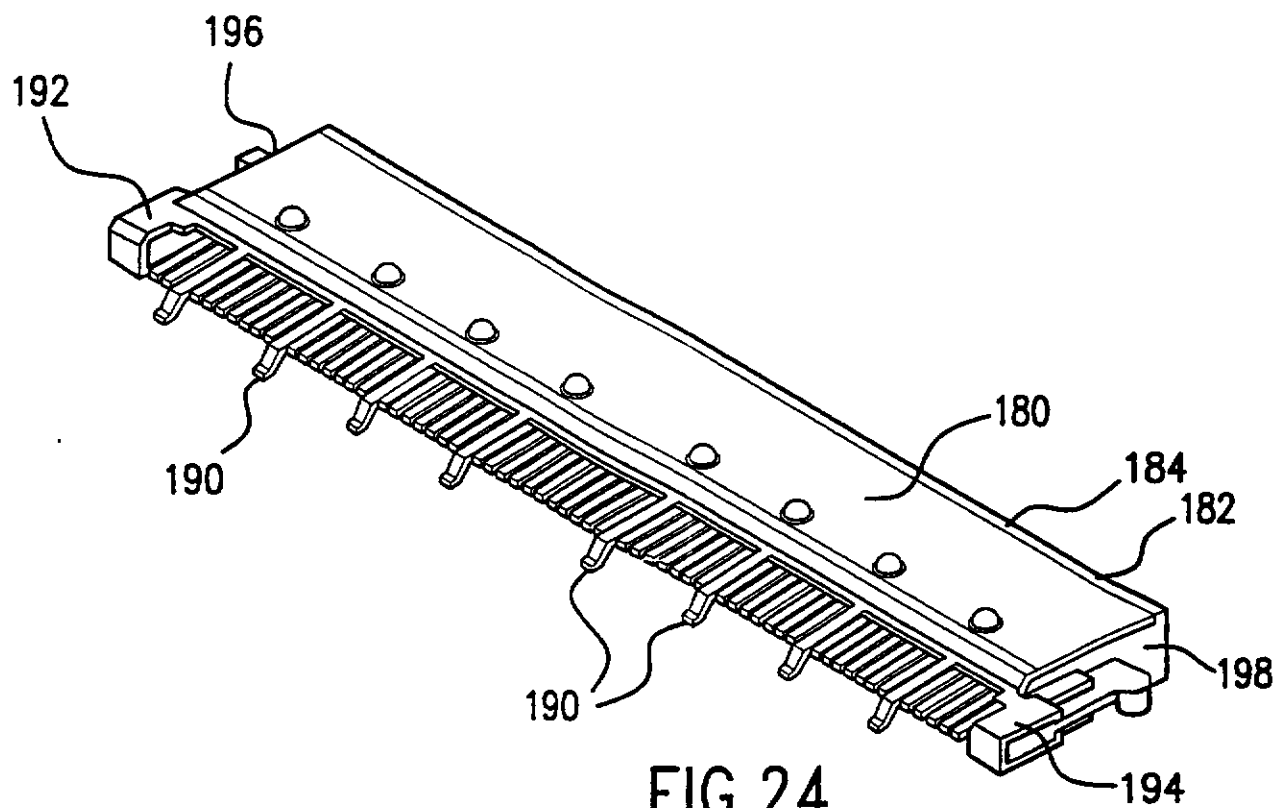


FIG. 23

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16-BIT PC CARD PIN ASSIGNMENTS

PIN NUMBER	SIGNAL NAME	DIRECTION	PIN NUMBER	SIGNAL NAME	DIRECTION
01	GND		68	GND	
02	DATA03	B	67	CD2#	
03	DATA04	B	66	DATA10	B
04	DATA05	B	65	DATA09	B
05	DATA06	B	64	DATA08	B
06	DATA07	B	63	StsChg	B
07	CE1#	I	62	SPkrN	O
08	ADDR10	I	61	REG#	I
09	OE#	I	60	INPACKN	T
10	ADDR11	I	59	WAIT#	O
11	ADR09	I	58	RESET	I
12	ADDR08	I	57	VS2#	
13	ADDR13	I	56	ADDR25	I
14	ADDR14	I	55	ADDR24	I
15	WE#	I	54	ADDR23	I
16	READY	O	53	ADDR22	I
17	VCC		52	VPP2	
18	VPP1		51	VCC	
19	ADDR16	I	50	ADDR21	I
20	ADDR15	I	49	ADDR20	I
21	ADDR12	I	48	ADDR19	I
22	ADDR07	I	47	ADDR18	I
23	ADDR06	I	46	ADDR17	I
24	ADDR05	I	45	IOWRN	I
25	ADDR04	I	44	IORDN	I
26	ADDR03	I	43	VS1#	
27	ADDR02	I	42	CE2#	I
28	ADDR01	I	41	DATA15	B
29	ADDR00	I	40	DATA14	B
30	DATA00	B	39	DATA13	B
31	DATA01	B	38	DATA12	B
32	DATA02	B	37	DATA11	B
33	IOIS16N	O	36	CD1#	
34	GND		35	GND	

FIG. 26

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32-BIT CARDBUS PC CARD PIN ASSIGNMENTS

PIN NUMBER	SIGNAL NAME	DIRECTION	PIN NUMBER	SIGNAL NAME	DIRECTION
01	GND		68	GND	
02	CAD0	B	67	CD2	O
03	CAD1	B	66	CAD31	B
04	CAD3	B	65	CAD30	B
05	CAD5	B	64	CAD28	B
06	CAD7	B	63	CSTSCHG	T
07	CC/BE0#	B	62	CAUDIO#	O
08	CAD9	B	61	CC/BE3#	B
09	CAD11	B	60	CREQ#	T
10	CAD12	B	59	CSERR#	B
11	CAD14	B	58	CRSTN	I
12	CC/BE1#	B	57	VS2	I
13	CPAR	B	56	CAD19	B
14	CPERR#	B	55	CAD17	B
15	CGNT#	I	54	CFRAME#	B
16	CINT#	T	53	CTRDY#	B
17	VCC		52	VPP2	
18	VPP1		51	VCC	
19	CCLK	I	50	CDEVSEL#	B
20	CIRDY#	B	49	CSTOP#	B
21	CC/BE2#	B	48	CBLOCK#	I
22	CAD18	B	47	RFU	
23	CAD20	B	46	CAD16	B
24	CAD21	B	45	CAD15	B
25	CAD22	B	44	CAD13	B
26	CAD23	B	43	VS1	I
27	CAD24	B	42	CAD10	B
28	CAD25	B	41	CAD8	B
29	CAD26	B	40	RFU	
30	CAD27	B	39	CAD6	B
31	CAD29	B	38	CAD4	B
32	RFU		37	CAD2	B
33	CCLKRUN#	B	36	CD1	
34	GND		35	GND	

FIG. 27

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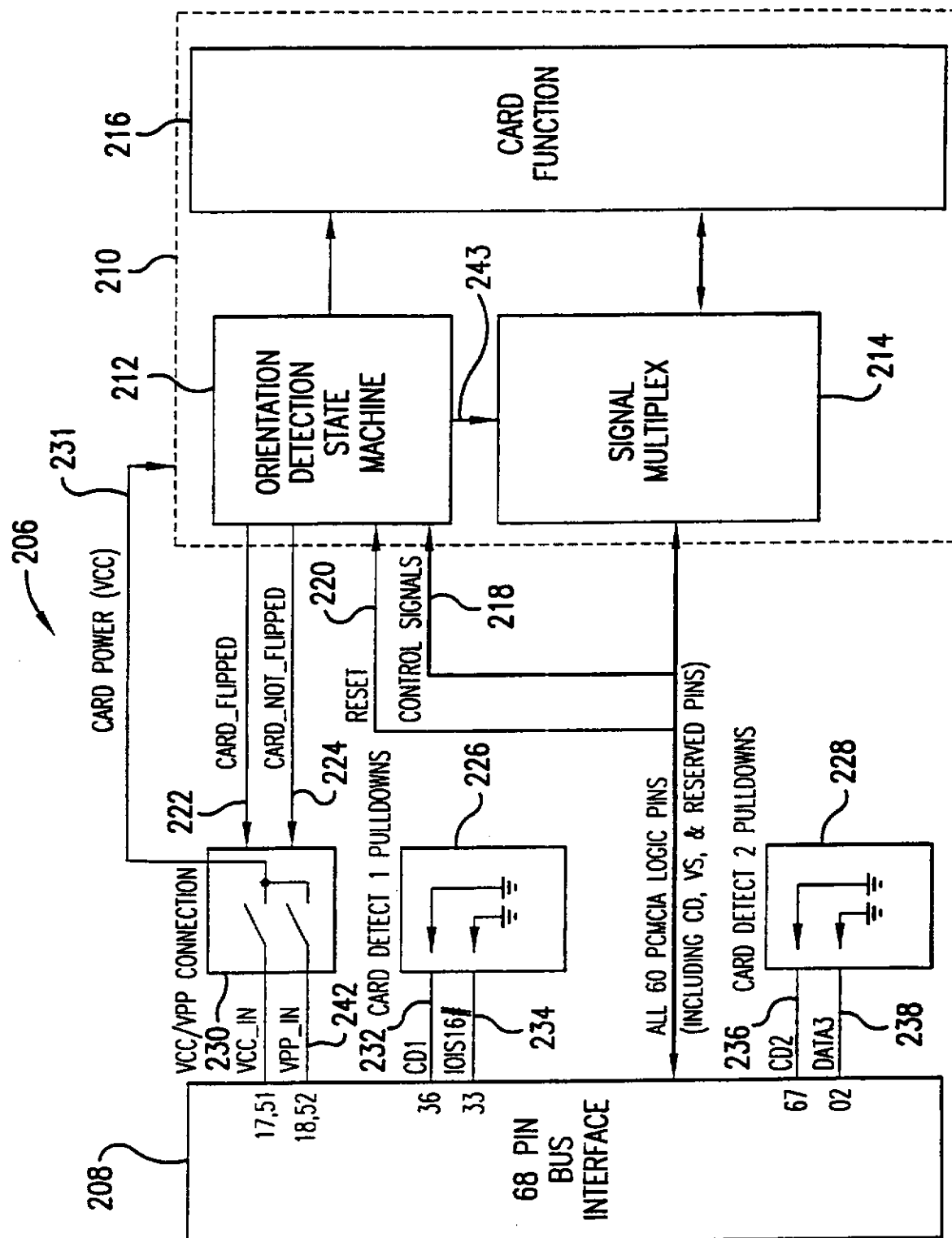


FIG. 28

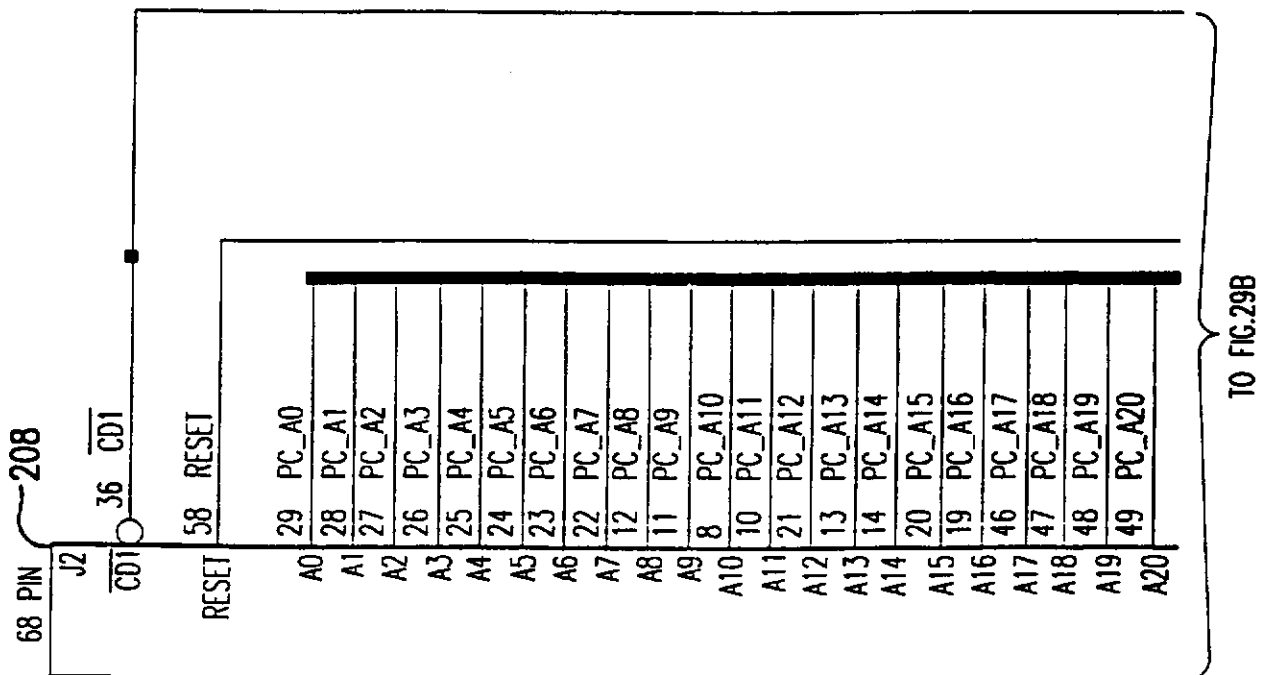
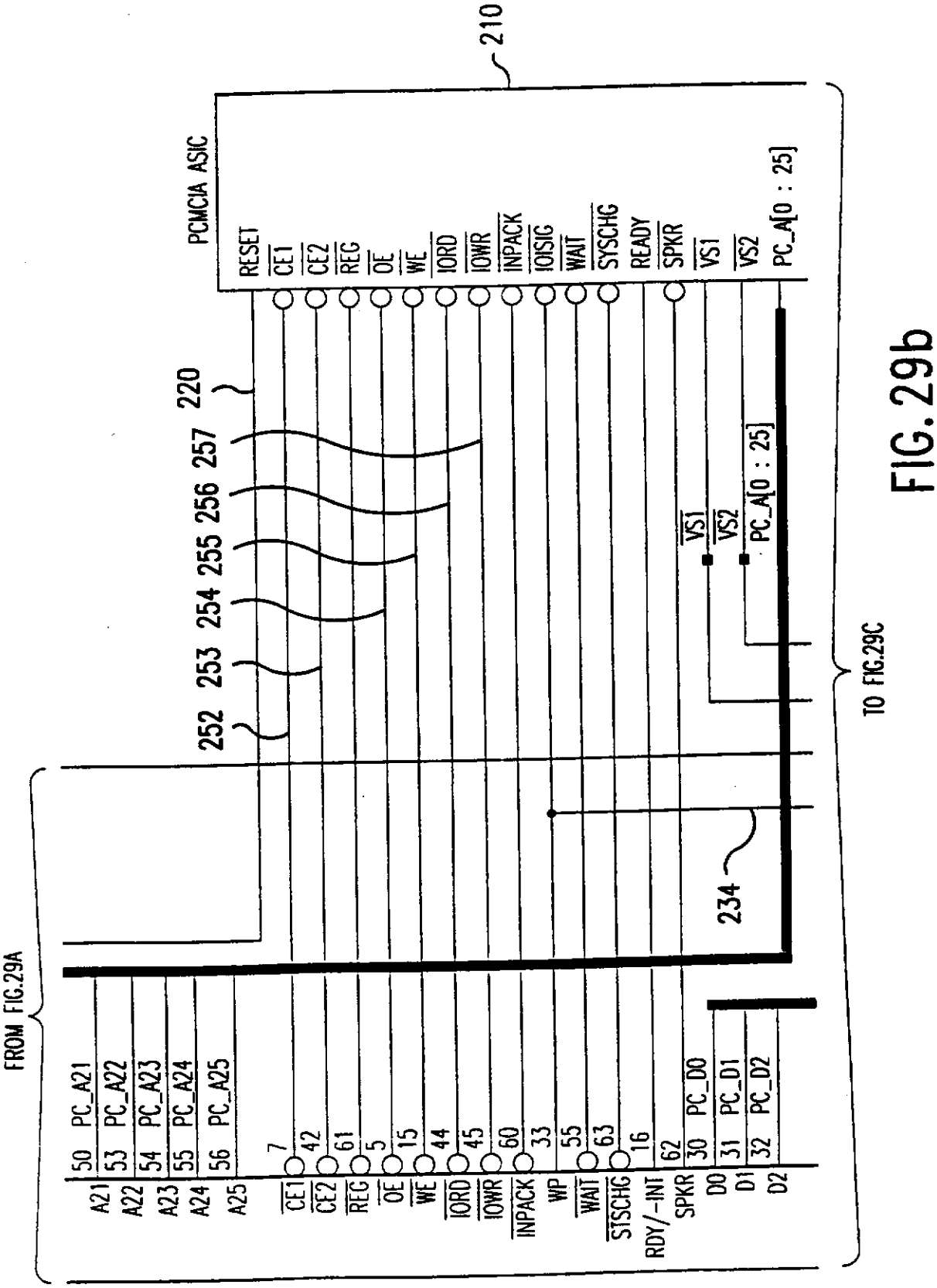


FIG. 29a



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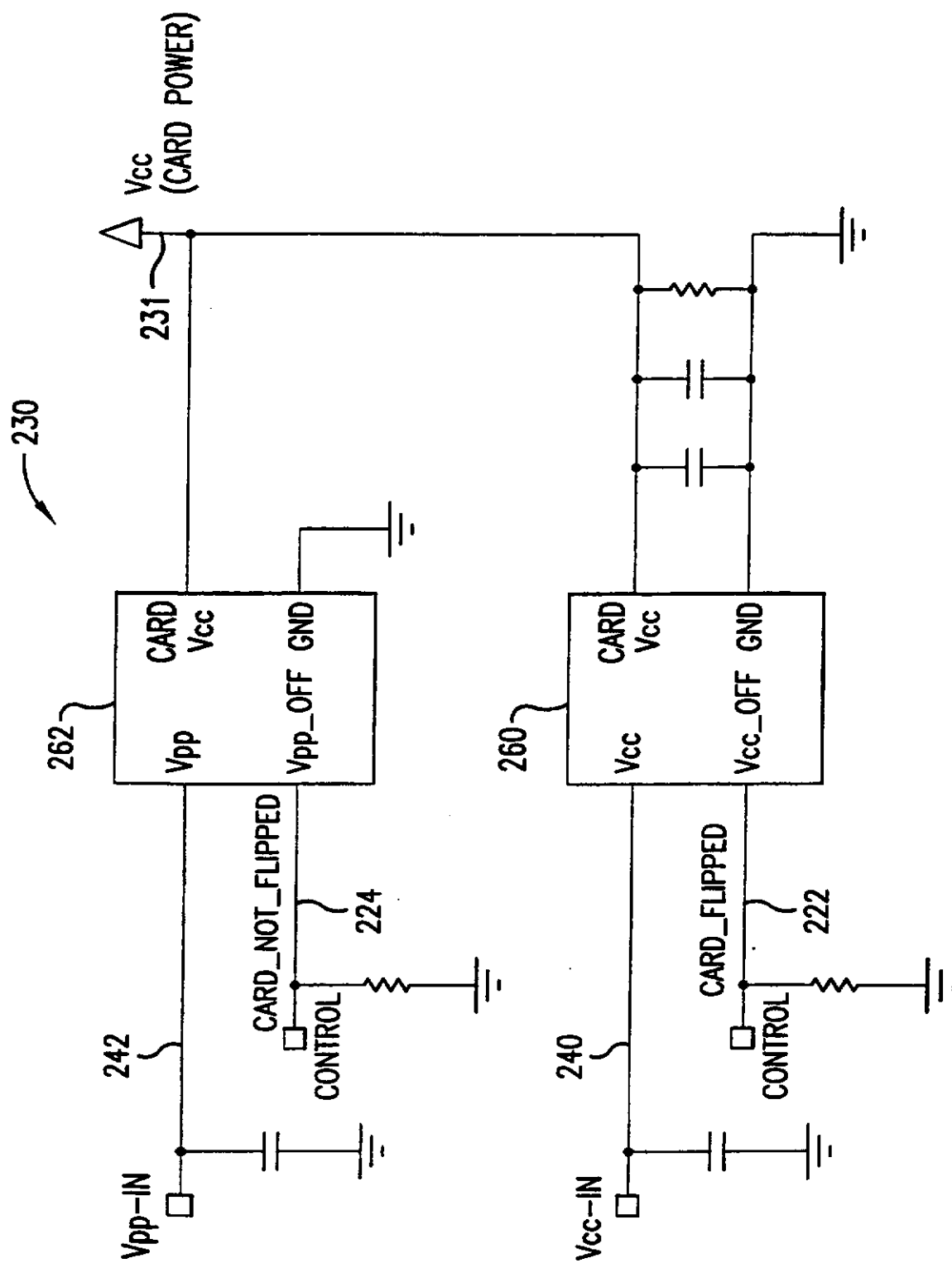


FIG. 30

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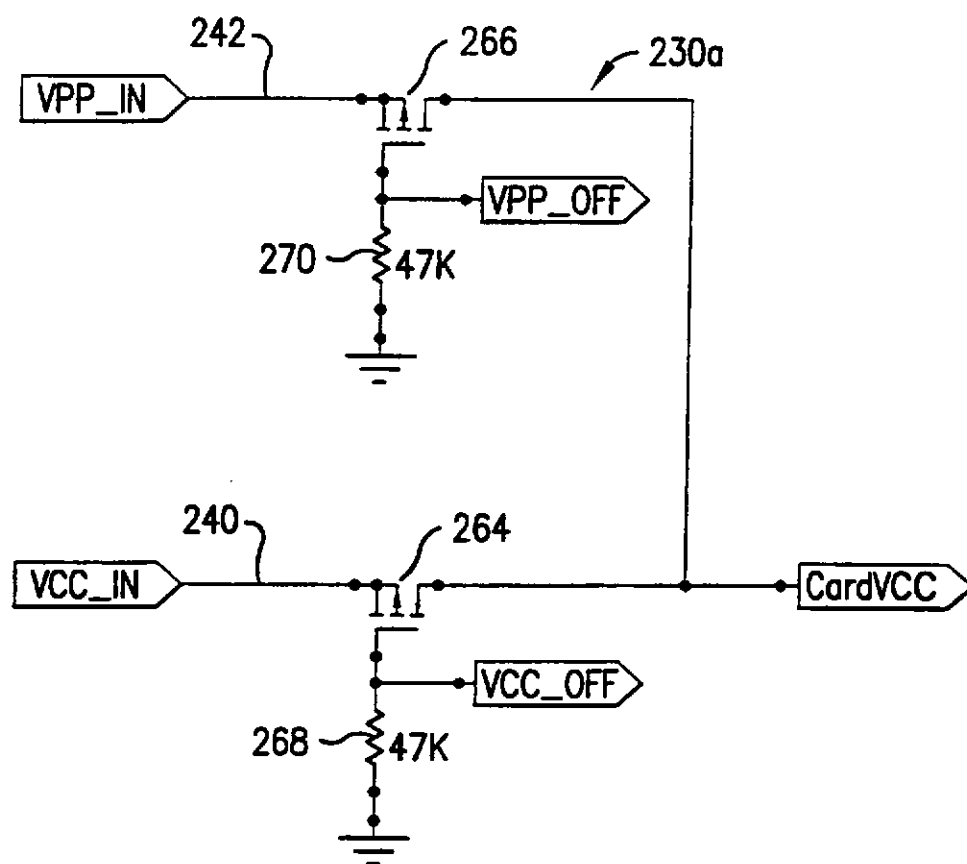


FIG. 31

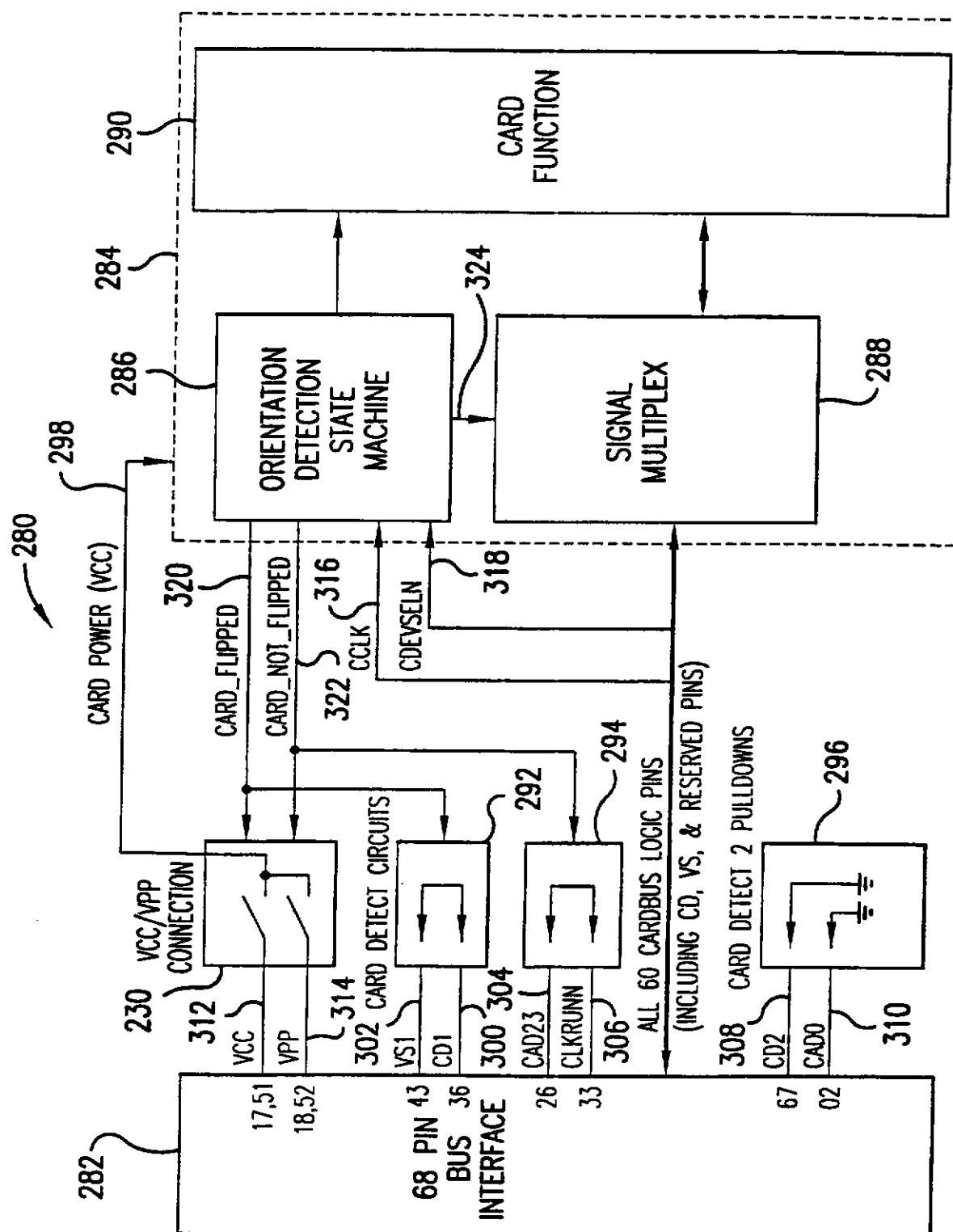
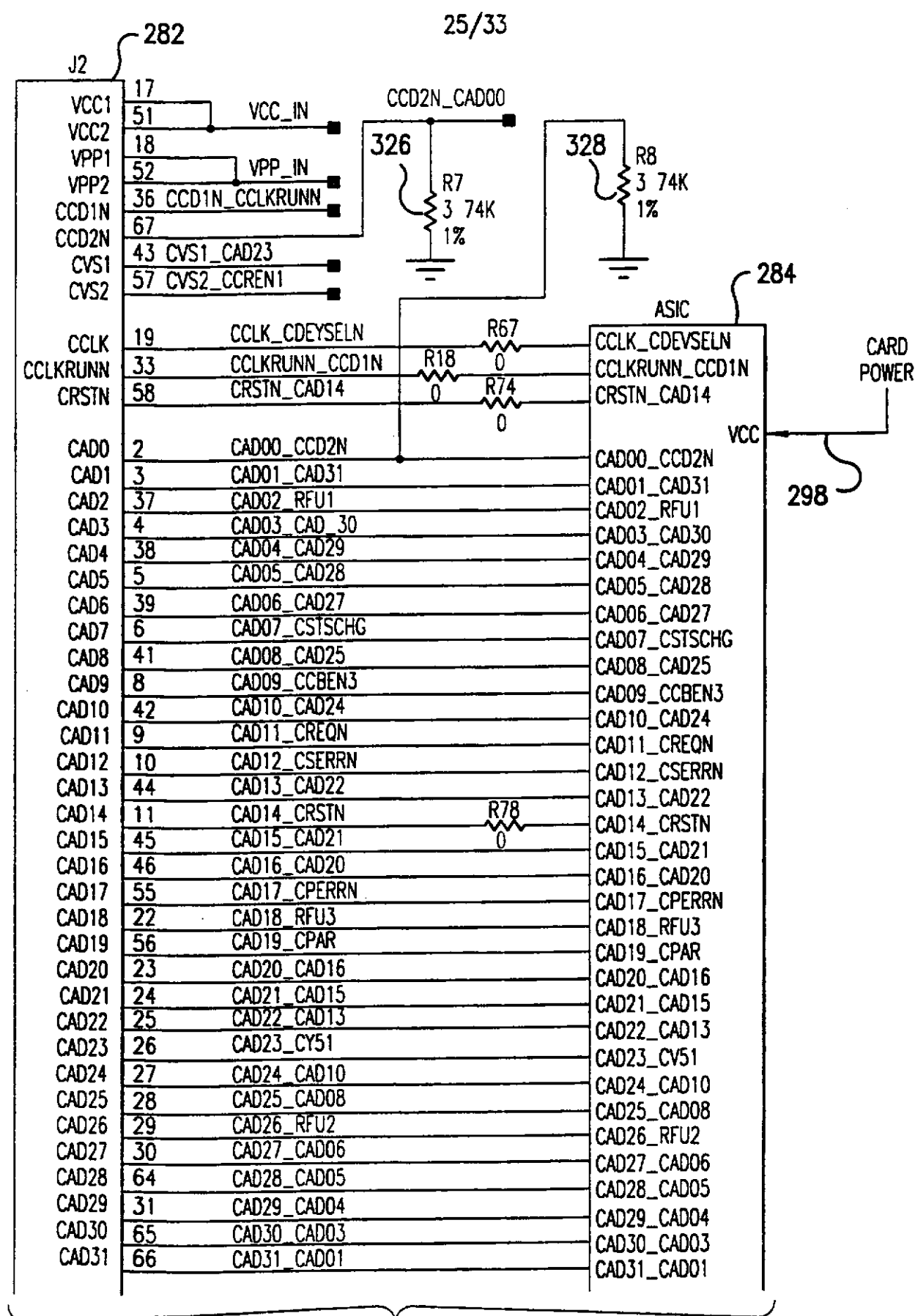


FIG. 32



TO FIG.33B

FIG. 33a

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FROM FIG.33A

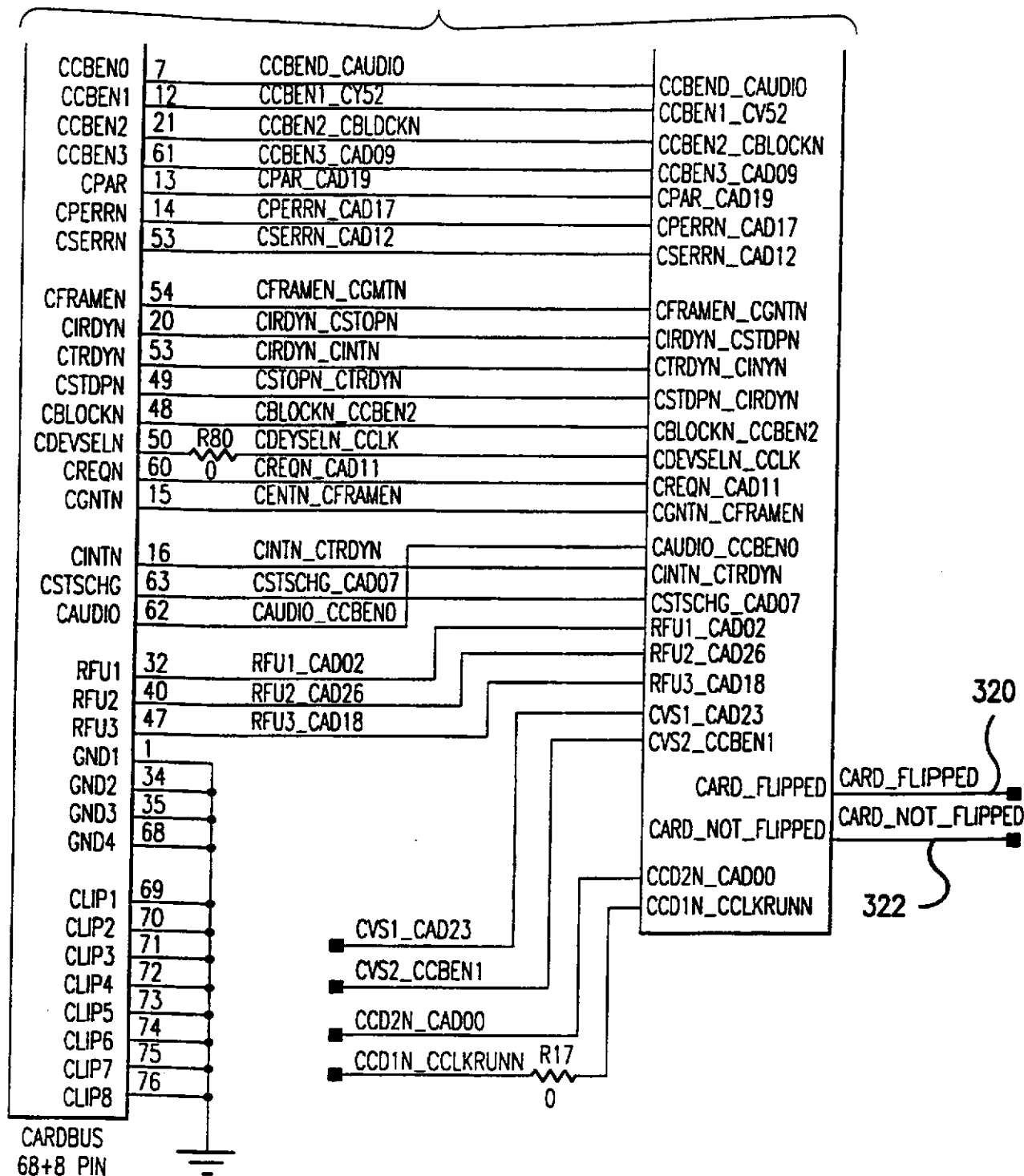
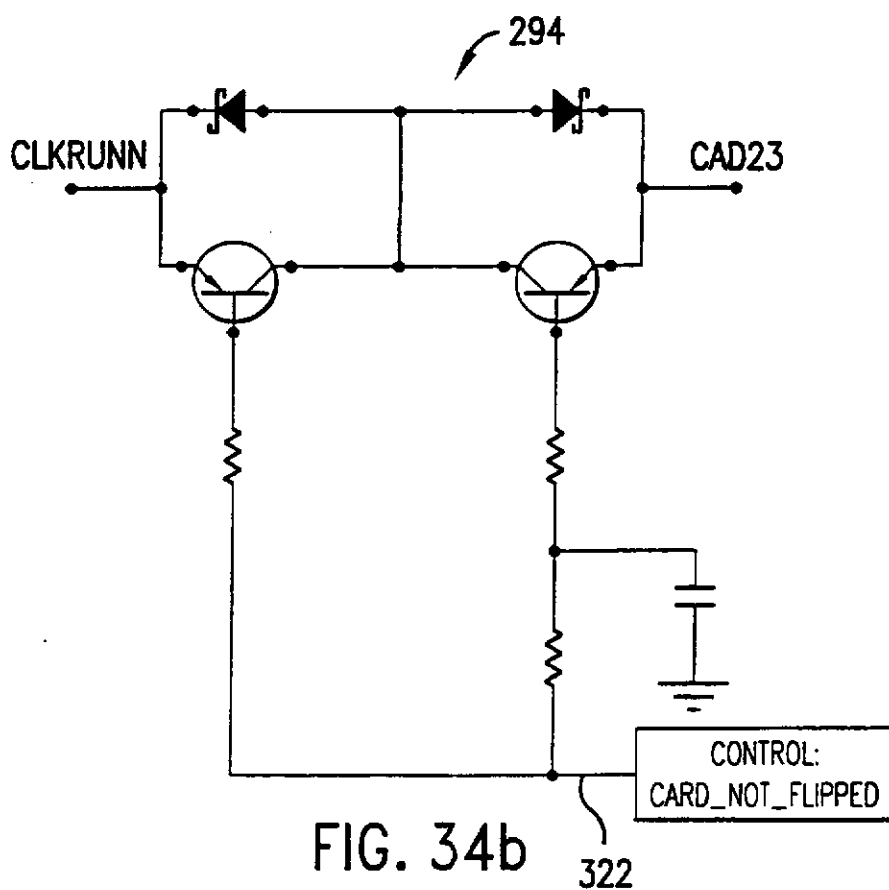
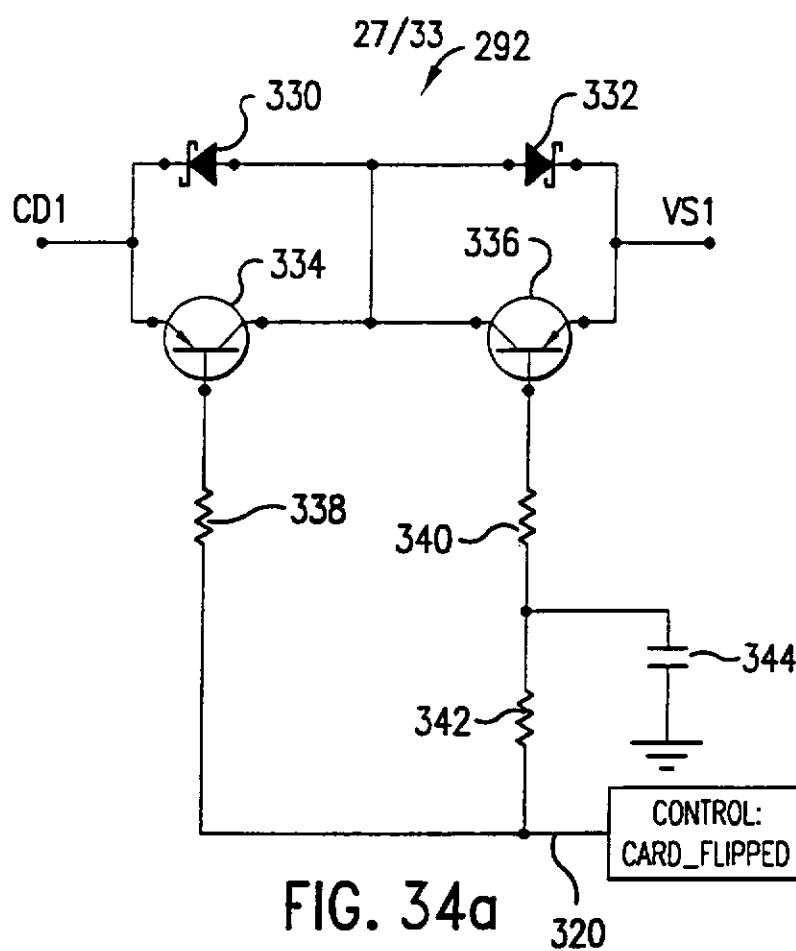


FIG. 33b



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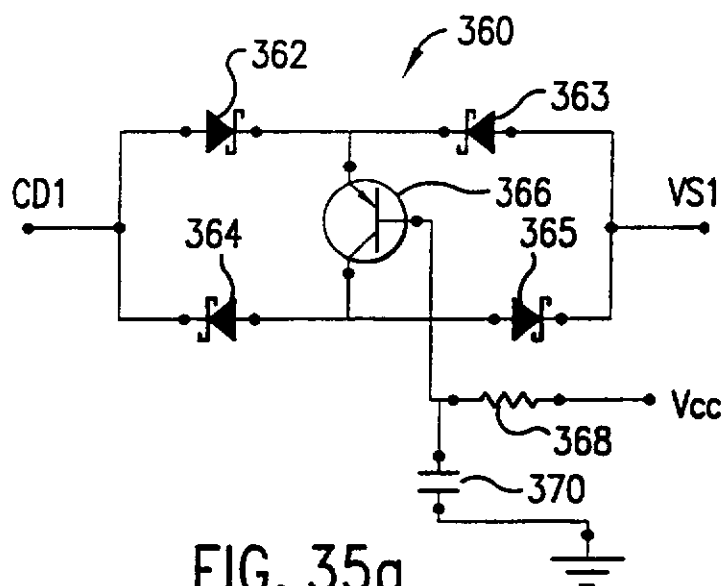


FIG. 35a

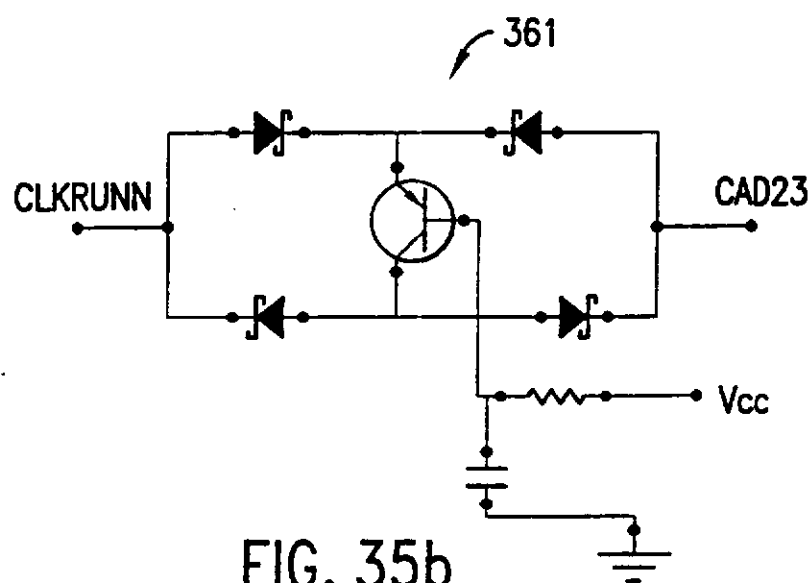


FIG. 35b

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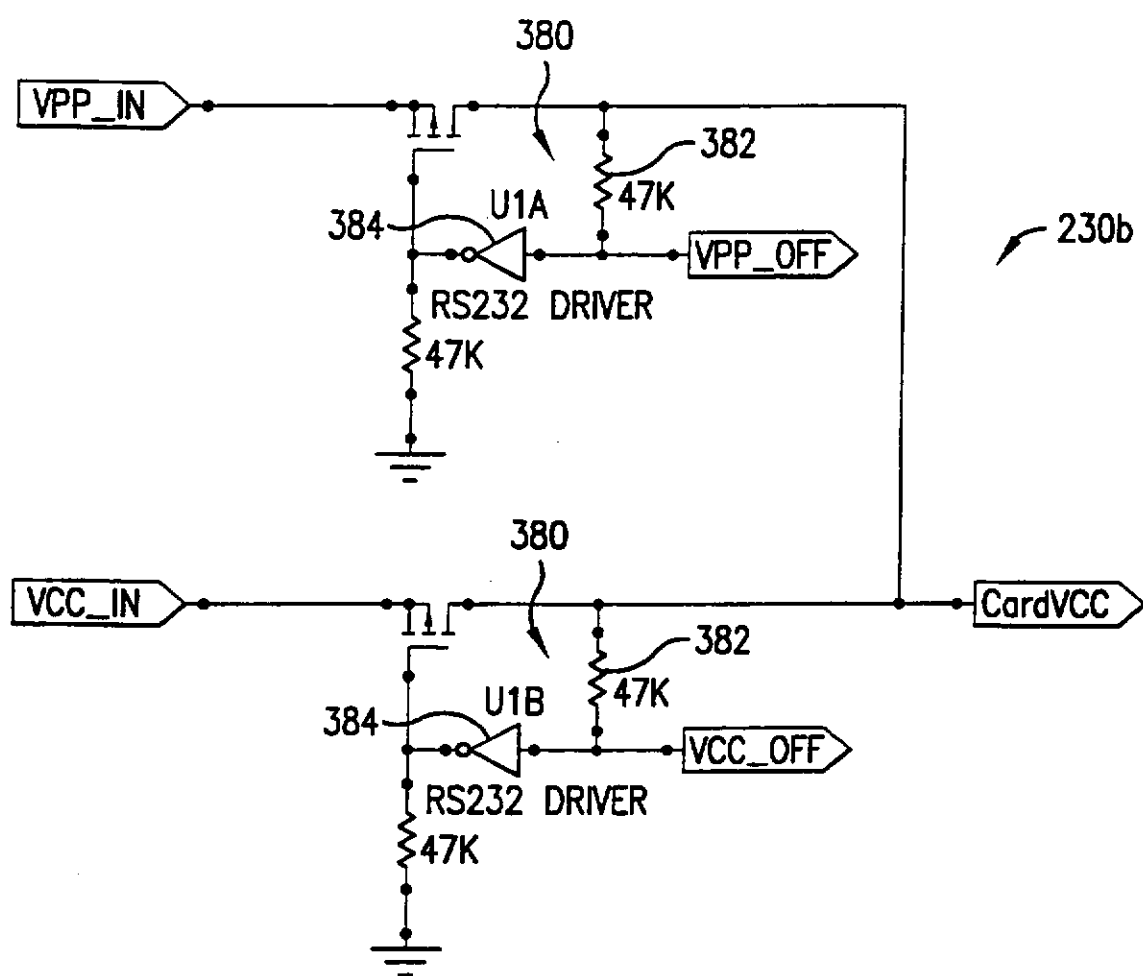


FIG. 36

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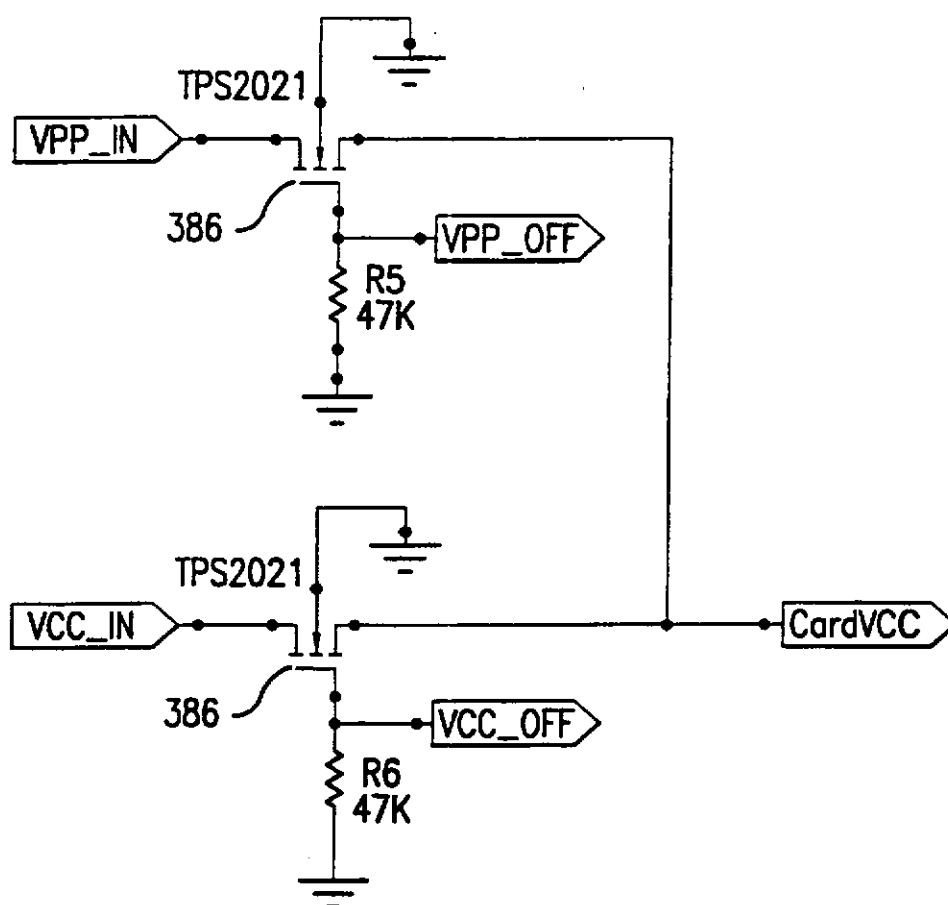


FIG. 37

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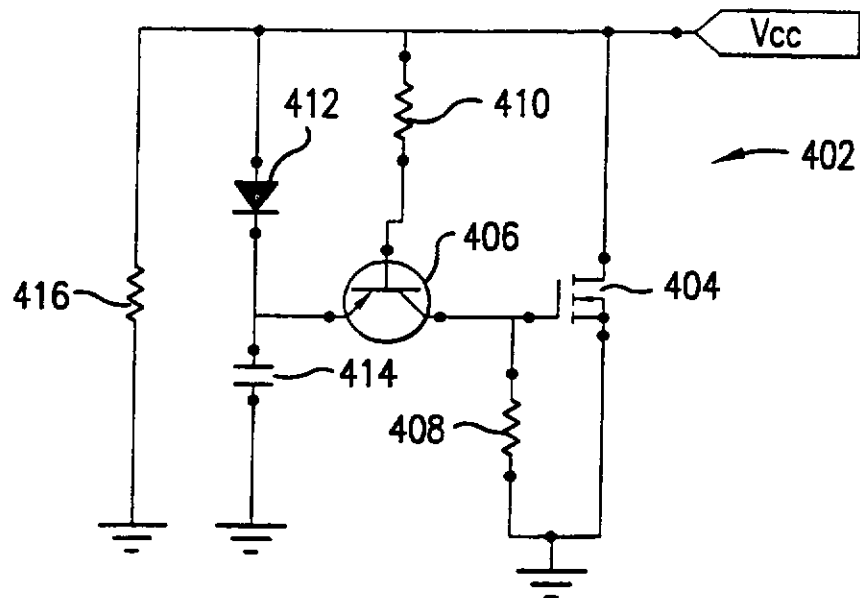


FIG. 38a

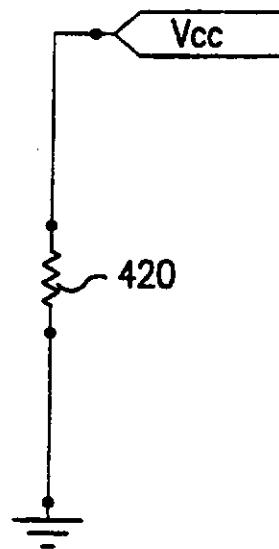


FIG. 38b

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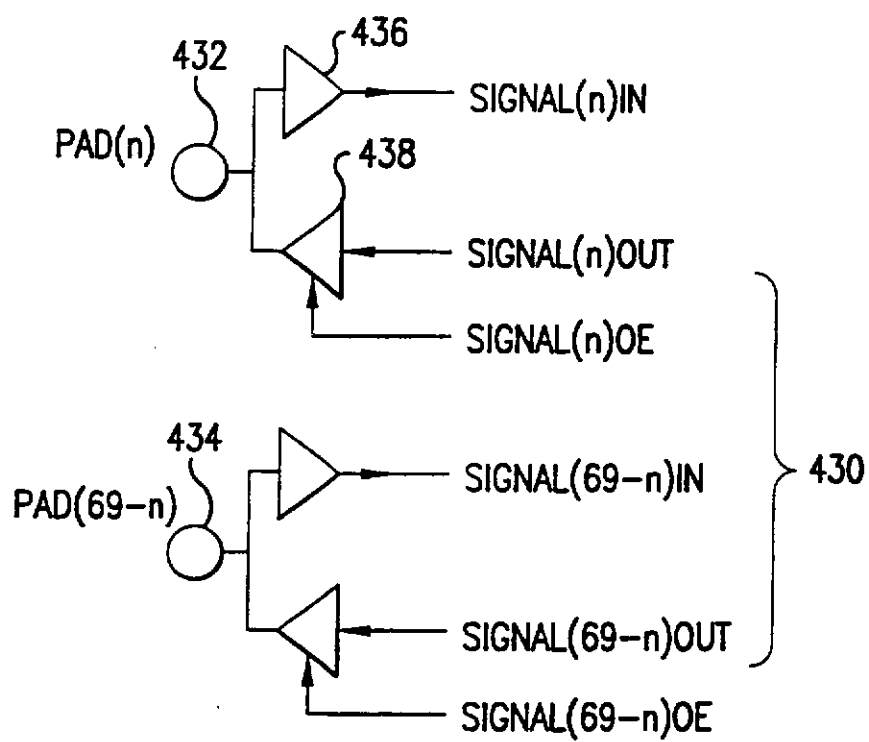


FIG. 39

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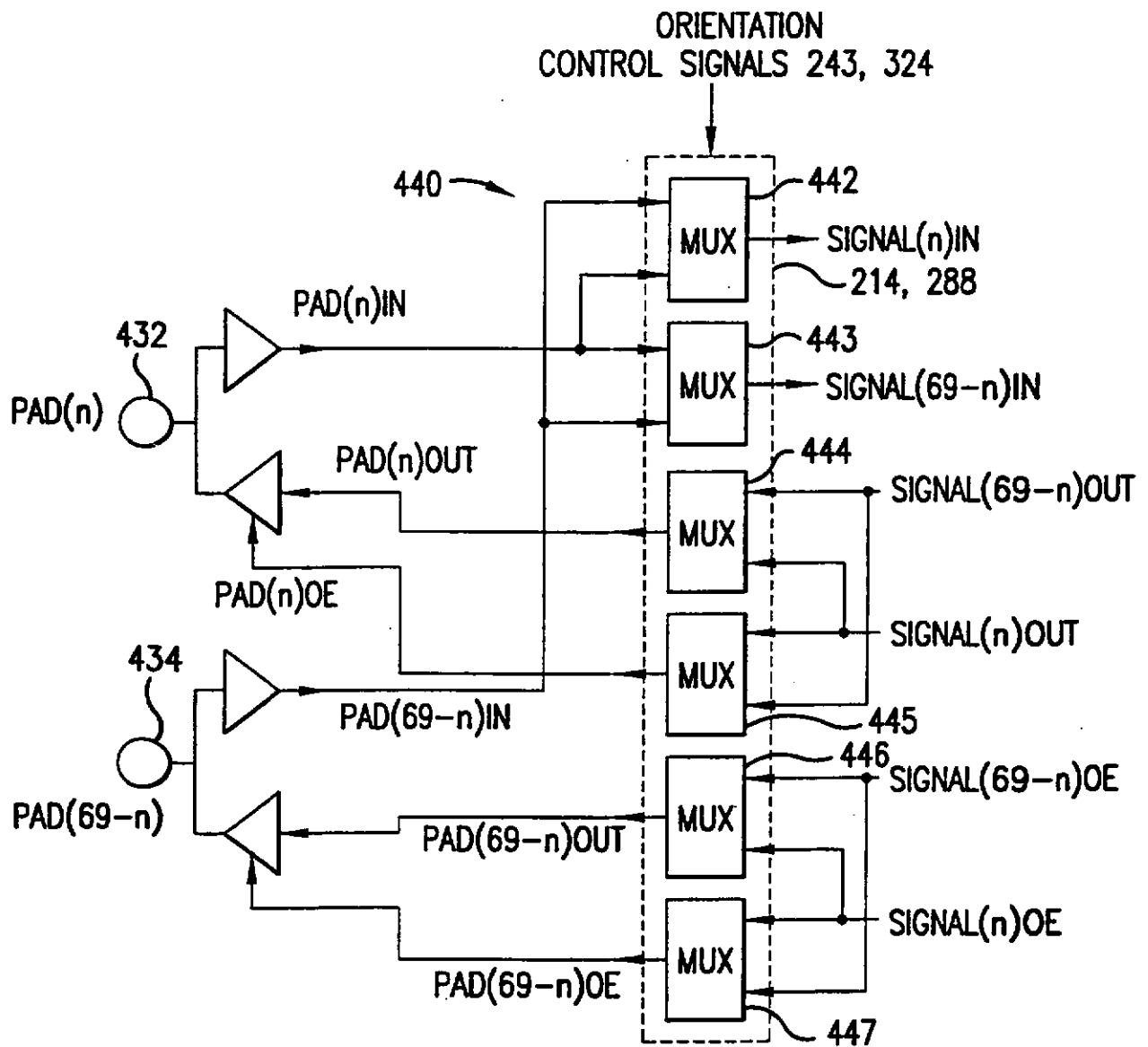


FIG. 40