

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau

(43) International Publication Date
01 October 2020 (01.10.2020)



(10) International Publication Number
WO 2020/197957 A1

(51) International Patent Classification:
H04N 19/119 (2014.01) *H04N 19/96* (2014.01)
H04N 19/176 (2014.01)

(21) International Application Number:
PCT/US2020/023752

(22) International Filing Date:
20 March 2020 (20.03.2020)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
62/822,787 22 March 2019 (22.03.2019) US
16/823,831 19 March 2020 (19.03.2020) US

(71) Applicant: **TENCENT AMERICA LLC** [US/US]; 2747
Park Boulevard, Palo Alto, CA 94306 (US).

(72) Inventors: **ZHAO, Xin**; c/o Tencent America LLC, 2747
Park Boulevard, Palo Alto, CA 94306 (US). **LI, Xiang**; c/o
Tencent America LLC, 2747 Park Boulevard, Palo Alto, CA
94306 (US). **LIU, Shan**; c/o Tencent America LLC, 2747
Park Boulevard, Palo Alto, CA 94306 (US).

(74) Agent: **MA, Johnny** et al.; Oblon, McClelland, Maier &
Neustadt, L.L.P., 1940 Duke Street, Alexandria, VA 22314
(US).

(81) Designated States (*unless otherwise indicated, for every
kind of national protection available*): AE, AG, AL, AM,
AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ,
CA, CH, CL, CN, CO, CR, CU, CZ, DE, DJ, DK, DM, DO,
DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN,
HR, HU, ID, IL, IN, IR, IS, JO, JP, KE, KG, KH, KN, KP,
KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, ME,
MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ,
OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA,

(54) Title: METHOD AND APPARATUS FOR VIDEO CODING

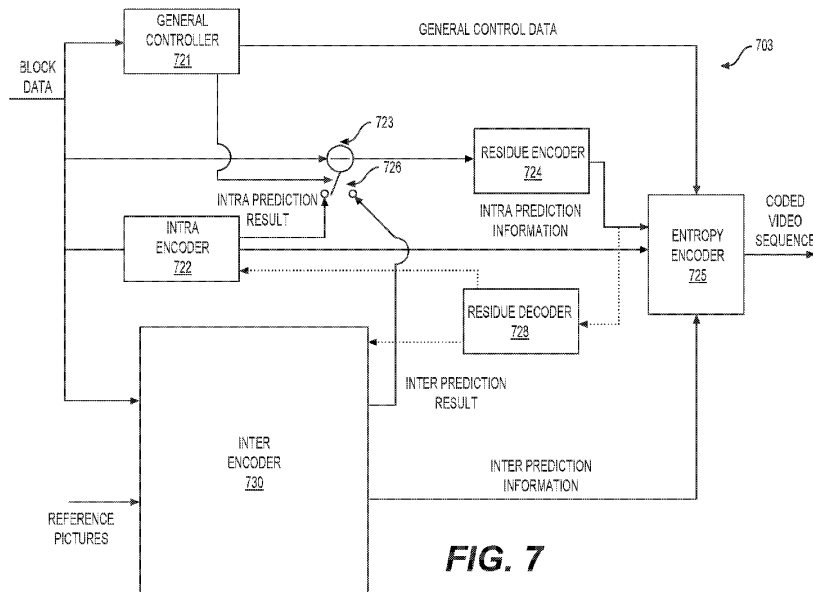


FIG. 7

(57) Abstract: Aspects of the disclosure provide methods and an apparatus including processing circuitry that decodes coded information of a coding block (CB) in a picture from a coded video bitstream. The coded information indicates a width W and a height H of the CB. The processing circuitry partitions the CB into sub-processing units (SPUs) having a width being a minimum one of W and K and a height being a minimum one of H and K. At least one of the width W and the height H is larger than a processing data unit size K. The processing circuitry determines a partitioning structure to partition the SPUs based on the width, the height, and a maximum transform unit (TU) size M. At least one of the width and the height is larger than M. The processing circuitry partitions each of the SPUs into TUs of MxM based on the partitioning structure.

[Continued on next page]

WO 2020/197957 A1

SC, SD, SE, SG, SK, SL, ST, SV, SY, TH, TJ, TM, TN, TR,
TT, TZ, UA, UG, US, UZ, VC, VN, WS, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, KM, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))