

ABSTRACT**DELTA-SIGMA PULSE WIDTH MODULATOR CONTROL CIRCUIT**

5 A delta sigma pulse width modulator control circuit (103) uses a delta sigma
modulator (201) as a first stage to create a sequence of pulses representing an
input control signal. A pulse width modulator (205) weighs each pulse,
accumulates the sequence of weighted pulses and defines a pulse width
modulated output signal (603) from the accumulated pulses. The pulse width
10 modulated signal (603) is given a randomly generated offset (605) to the time of
pulse value transition and an adjacent pulse value matching technique is used to
reduce harmonically related noise generation.

This Sheet Has Been Replaced

