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CONFIGURABLE LOGIC ARRAY

ABSTRACT

A configurable semi-conductor integrated circuit comprising a matrix array of core cells (CC), each having a first simple function in common and at least one subsidiary function, there being at least two different subsidiary functions, the core cells (CC) being grouped in tiles (T) comprising a matrix array of the core cells (CC) smaller than the whole array (fn) and wherein each tile (T) has at least one of each different subsidiary functions (fn) and wherein the tiles (T) of core cells (CC) are arranged so as to uniformly cover the array. Preferably there are four cells to a tile (T).

The application also describes a configurable semi-conductor integrated circuit comprising an area thereof formed with a plurality of logic circuits at discrete sites or cells (CC) respectively defining a matrix array of cells. The matrix array of cells is subdivided at least into zones (11) each comprising a matrix array of cells.

Fig. 1

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