

Read Head Protection Circuit and Method

ABSTRACT

A circuit (80) and method (84) for protecting read heads (18) of a hard-disk drive system (100). Capacitor C_1 is controllably coupled to a dummy head R_{dummy} during a V_{bias} mode, so that capacitor C_1 has a low, predictable voltage upon returning to I_{bias} mode, protecting the read heads (18) from damage. The circuit (80) includes logic (82) and algorithm (84) determining when to couple the capacitor C_1 to the dummy head R_{dummy} during a servo bank write (SBW) sequence.

Figure 4