

DATA RECEIVING CIRCUIT, DATA RECEIVING SYSTEM AND MEMORY DEVICE

ABSTRACT

Embodiments of the disclosure provide a data receiving circuit, a data receiving system and a memory device. The data receiving circuit includes: a first amplification module, configured to receive a data signal, a first reference signal and a second reference signal, perform first comparison on the data signal and the first reference signal in response to a sampling clock signal and output a first signal pair, and perform second comparison on the data signal and the second reference signal and output a second signal pair; a second amplification module, configured to receive an enable signal and a feedback signal, selectively receive the first signal pair or the second signal pair as an input signal pair based on the feedback signal during a period in which the enable signal is at a first level, receive the first signal pair during a period in which the enable signal is at a second level, amplify a voltage difference of the first signal pair, and output a first output signal and a second output signal. According to the embodiments of the disclosure, the power consumption of the data receiving circuit can be at least reduced while the receiving performance of the data receiving circuit is improved.

[Fig. 4]