

FIG. 1

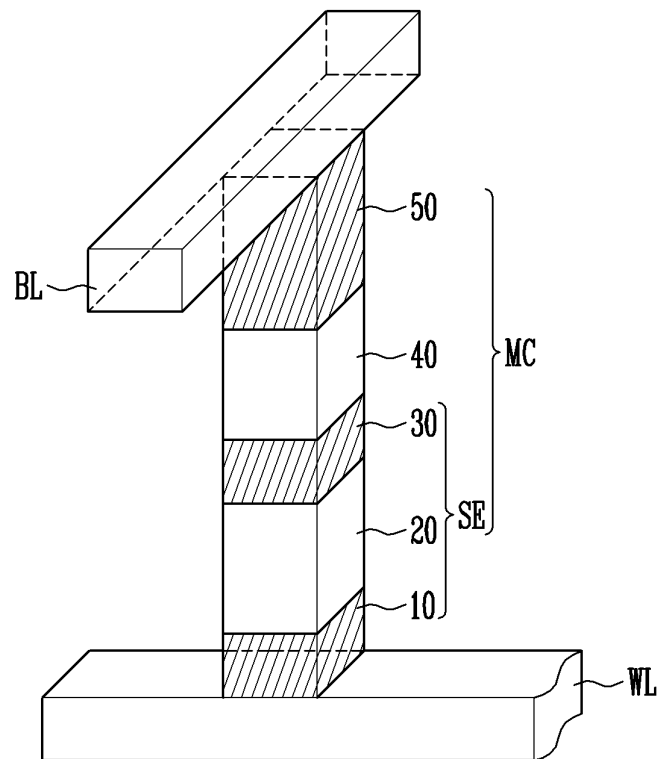


FIG. 2

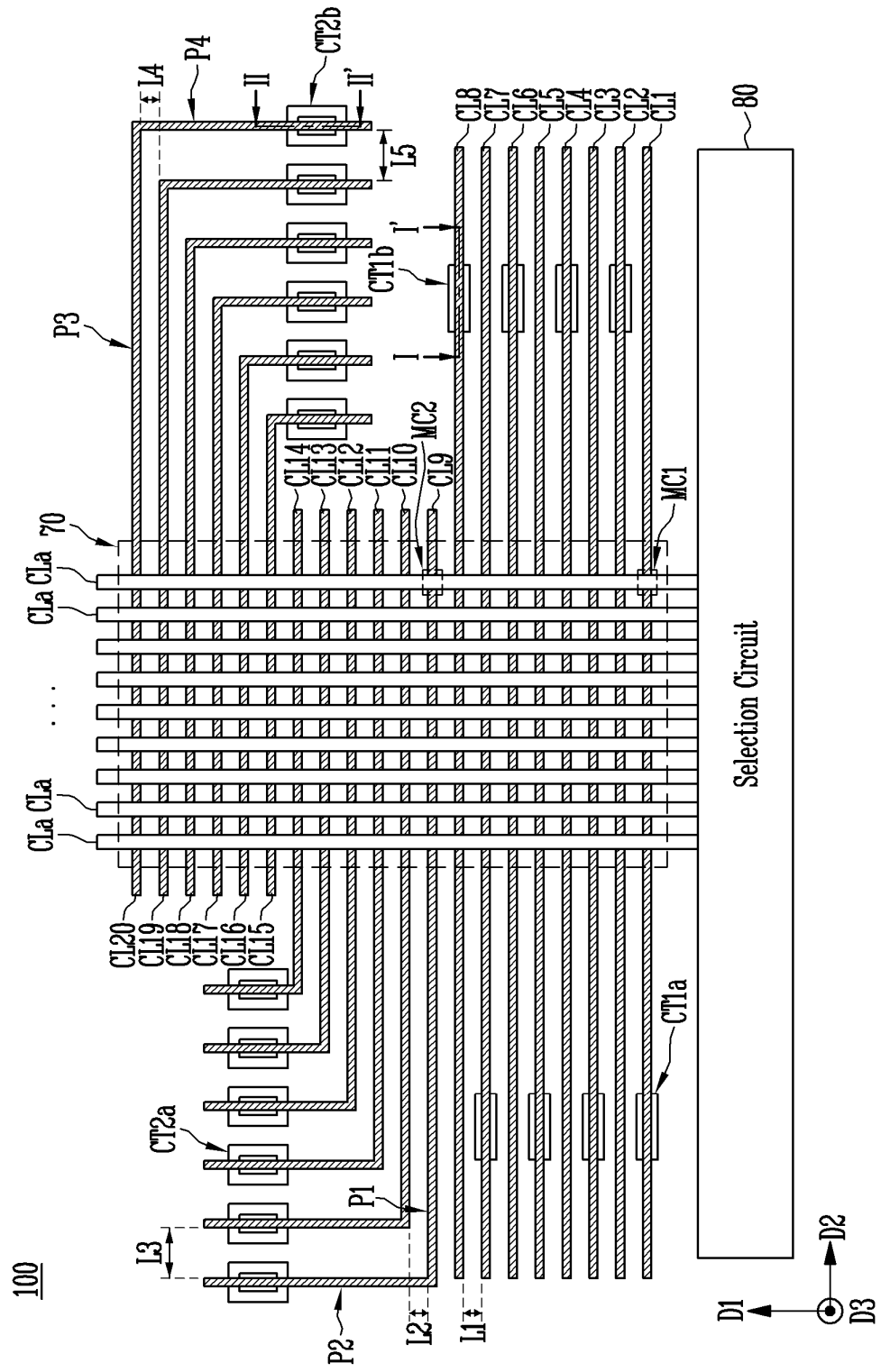


FIG. 3

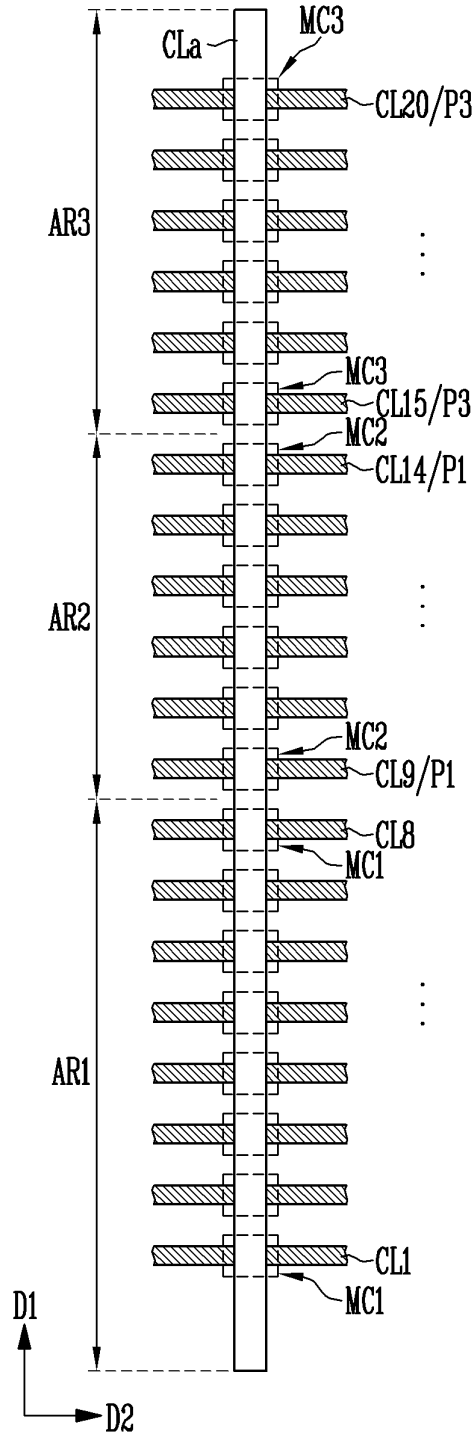


FIG. 4A

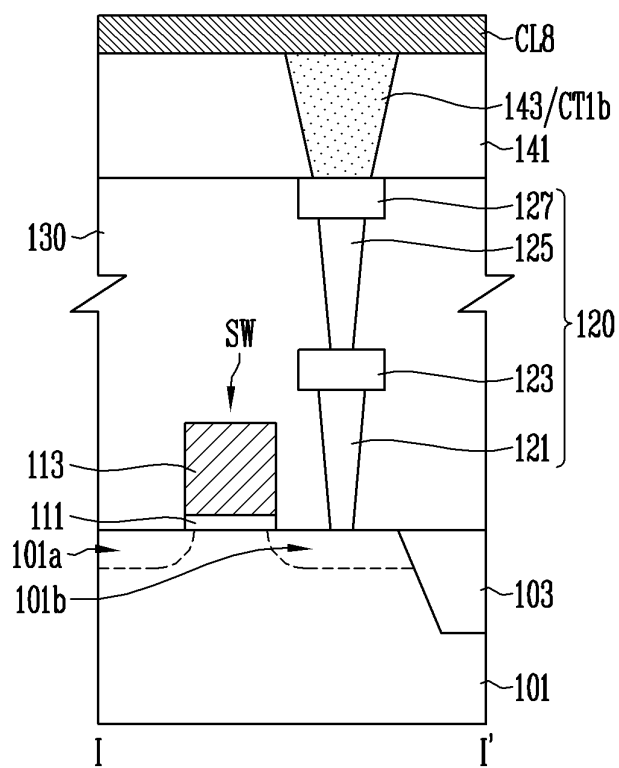
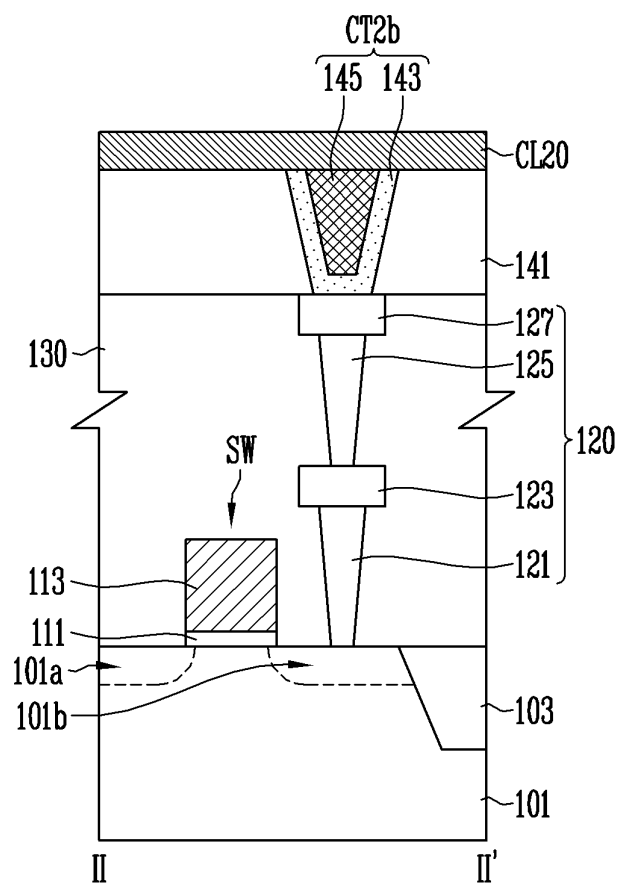


FIG. 4B



LXSW 1			BL1	BL2	BL3	...	BLm-2	BLm-1	BLm		WLn	LXSW 2
											WLn-1	
						...						
		WLk+1										
		WLk				Tile1						
						...					WL4	
		WL3									WL2	
		WL1										
		LYSW										
LXSW 1											WLn	LXSW 2
											WLn-1	
						...						
		WLk+1										
		WLk				Tile2						
						...					WL4	
		WL3									WL2	
		WL1										
			BL1	BL2	BL3	...	BLm-2	BLm-1	BLm			

FIG. 6

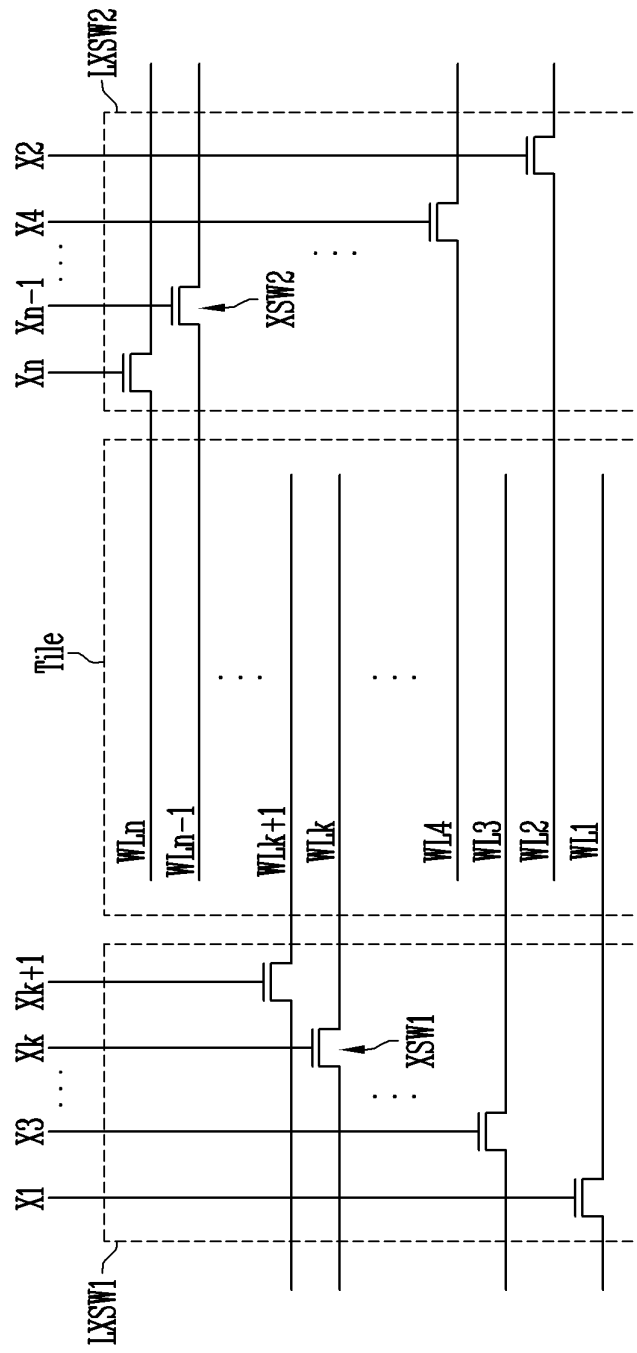


FIG. 7

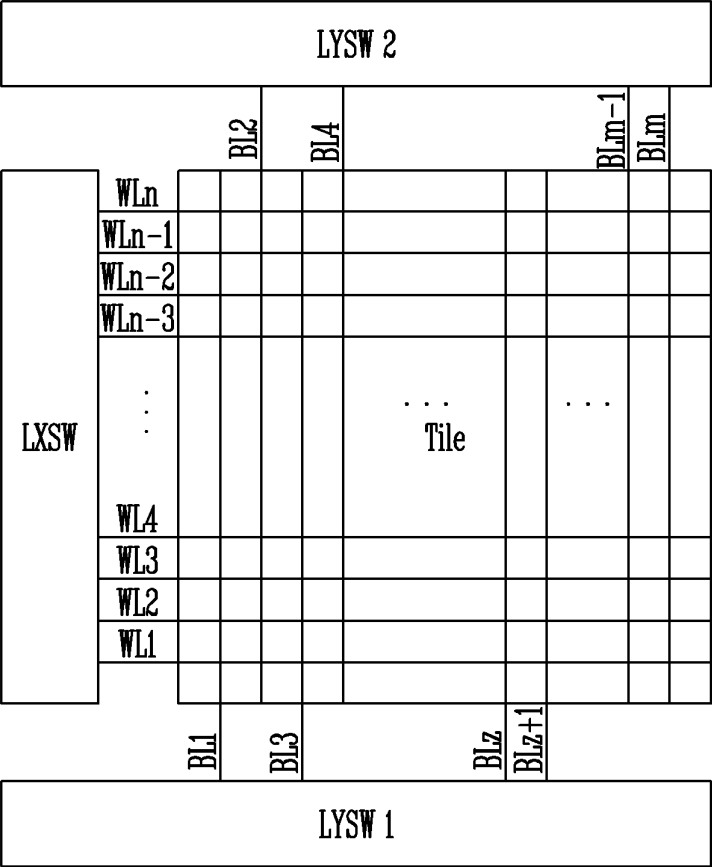


FIG. 8

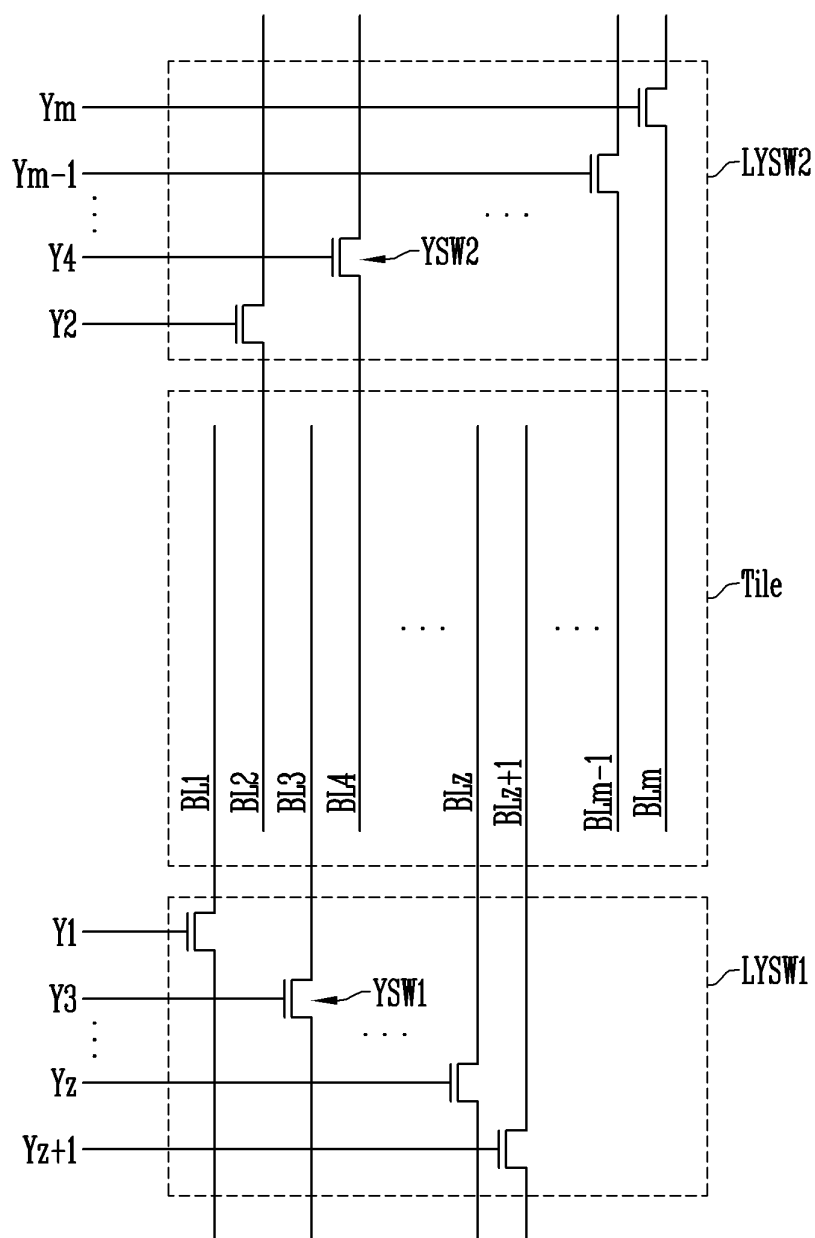


FIG. 9A

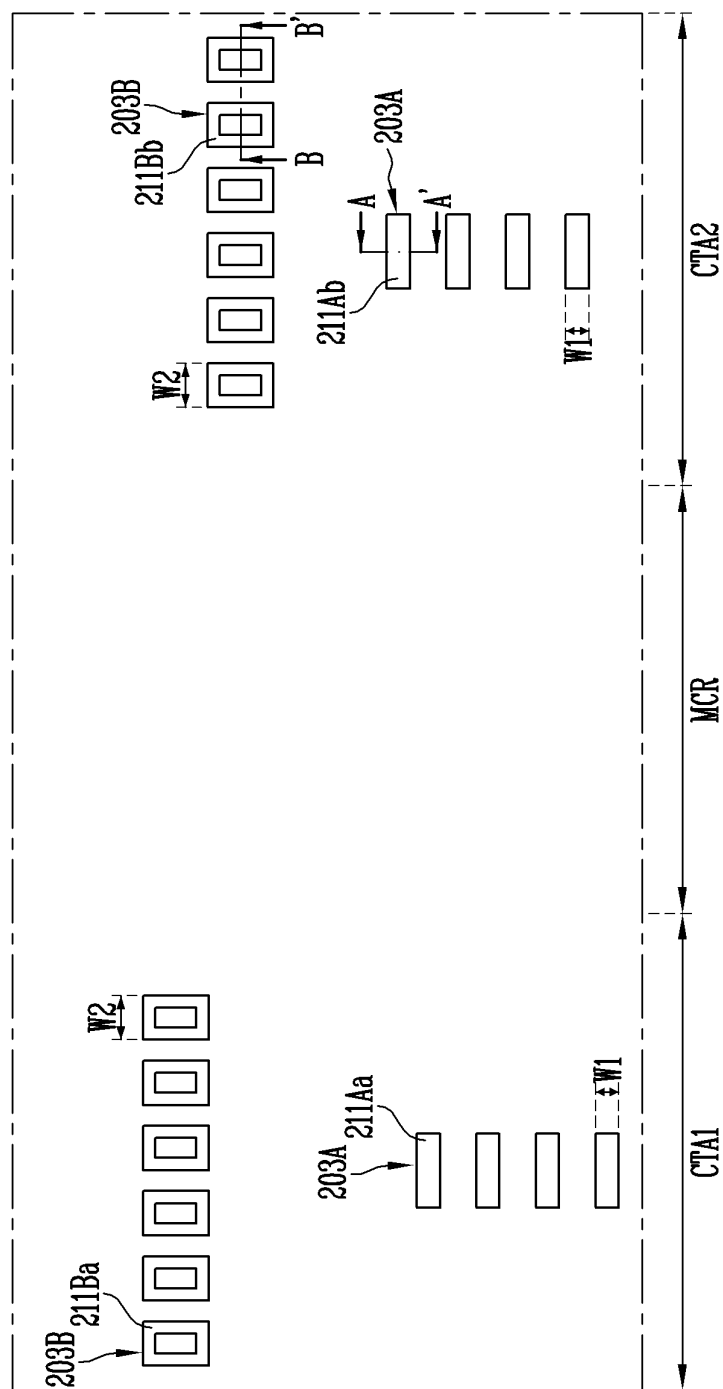


FIG. 9B

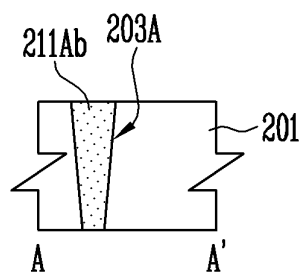


FIG. 9C

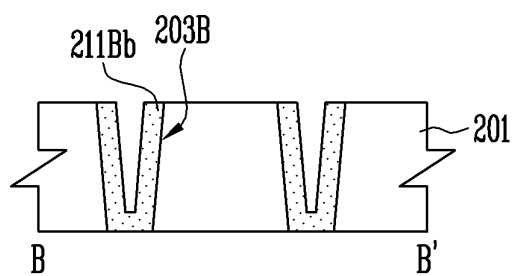


FIG. 10A

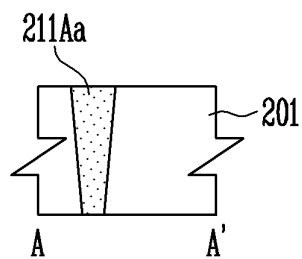


FIG. 10B

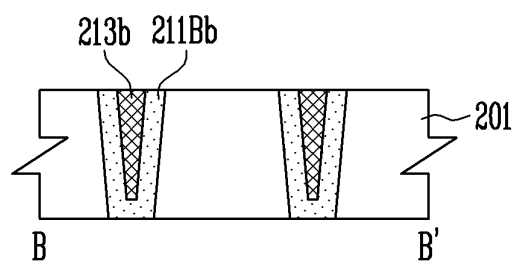


FIG. 11A

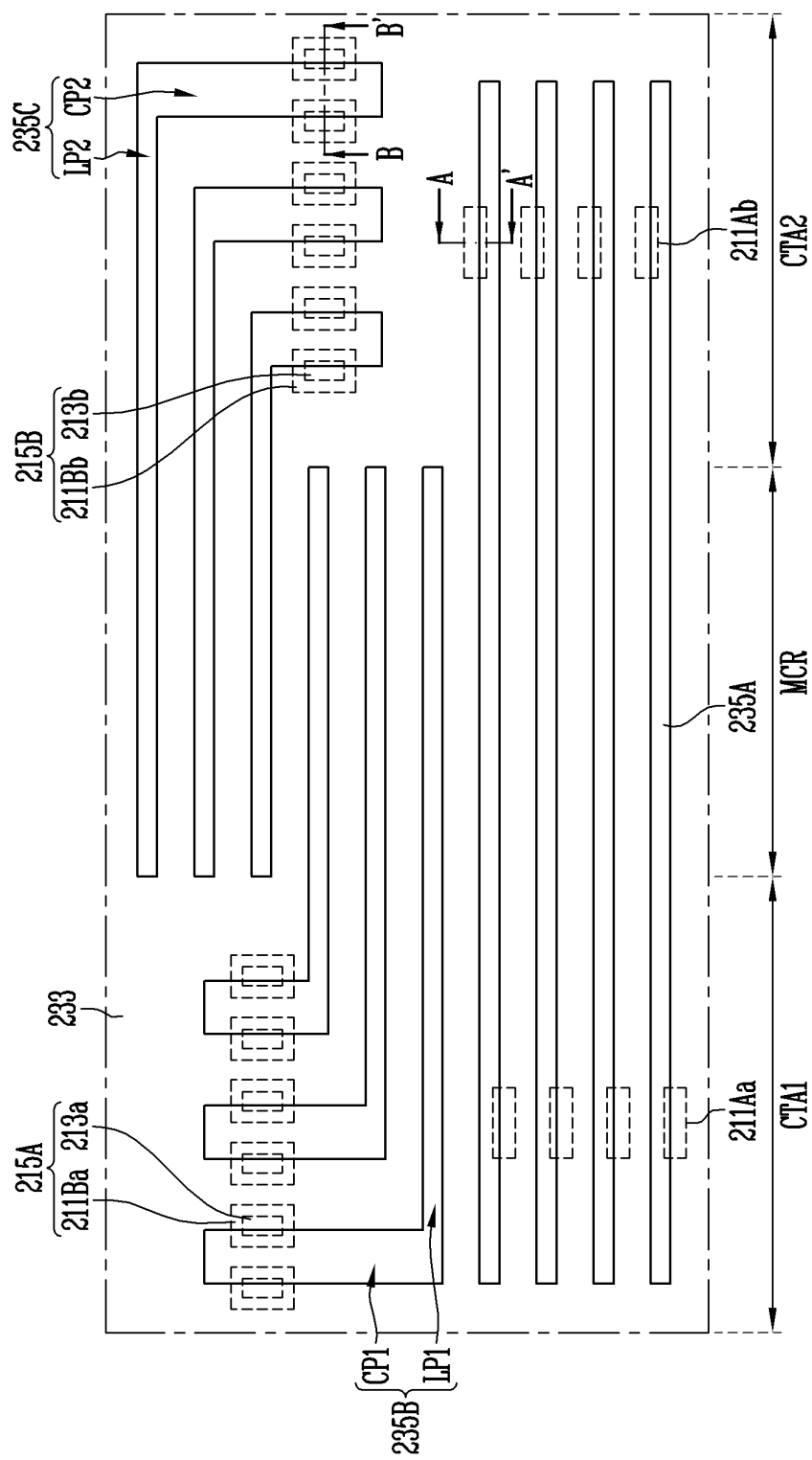


FIG. 11B

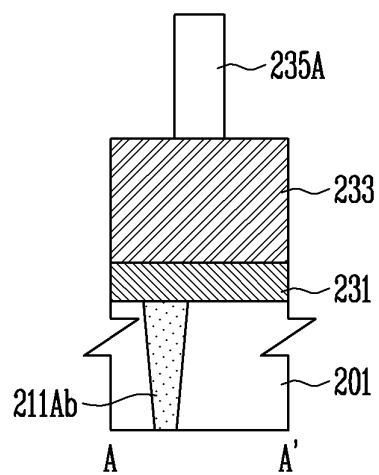


FIG. 11C

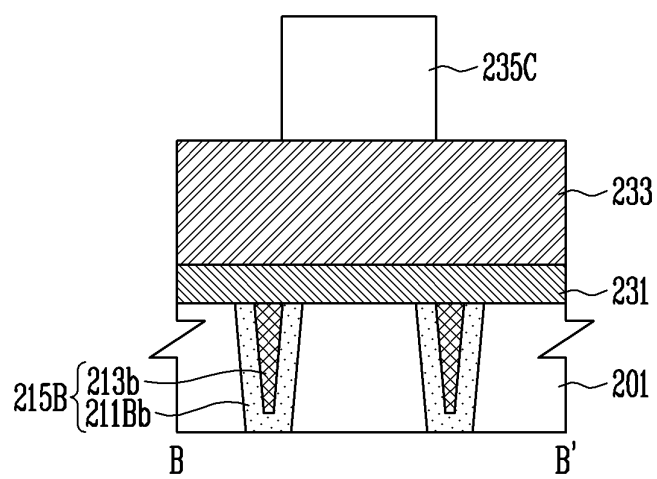


FIG. 12A

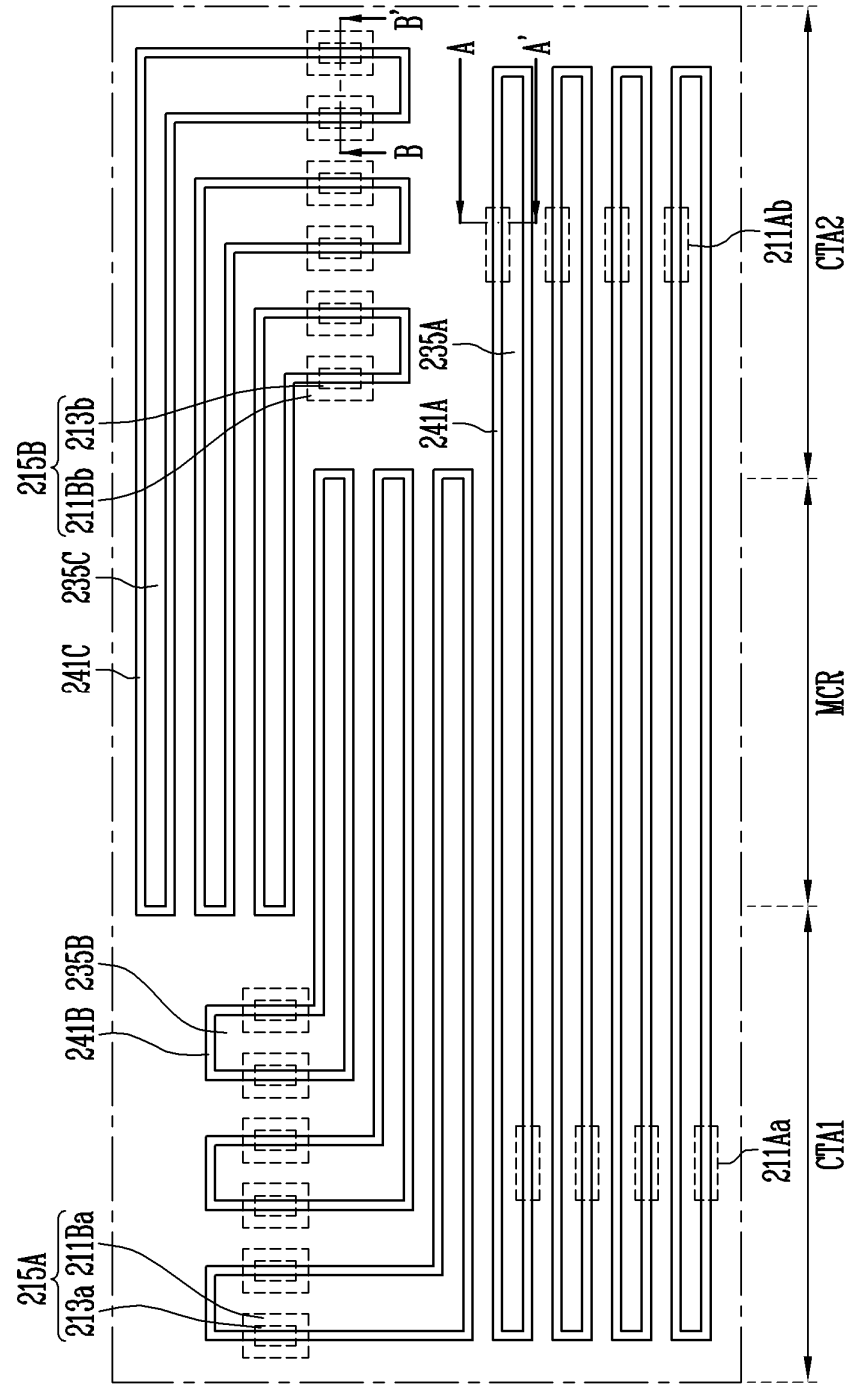


FIG. 12B

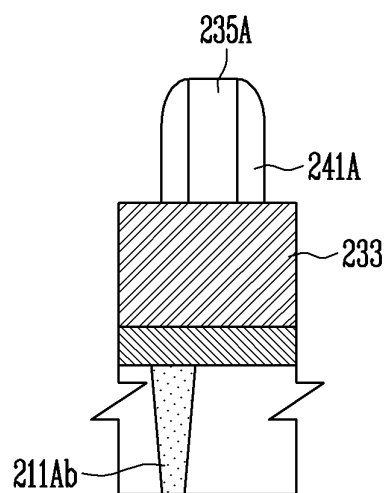


FIG. 12C

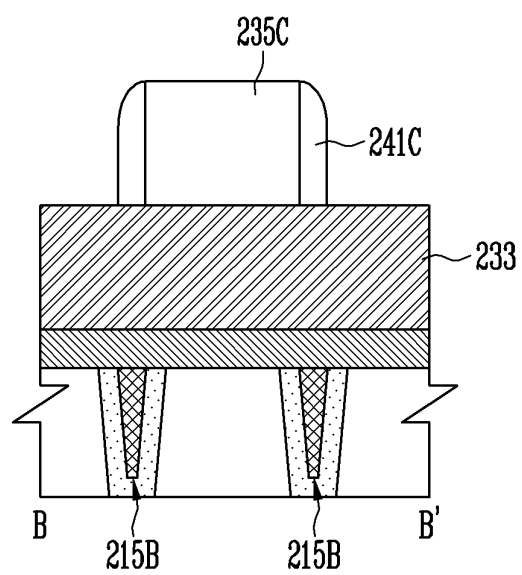


FIG. 13

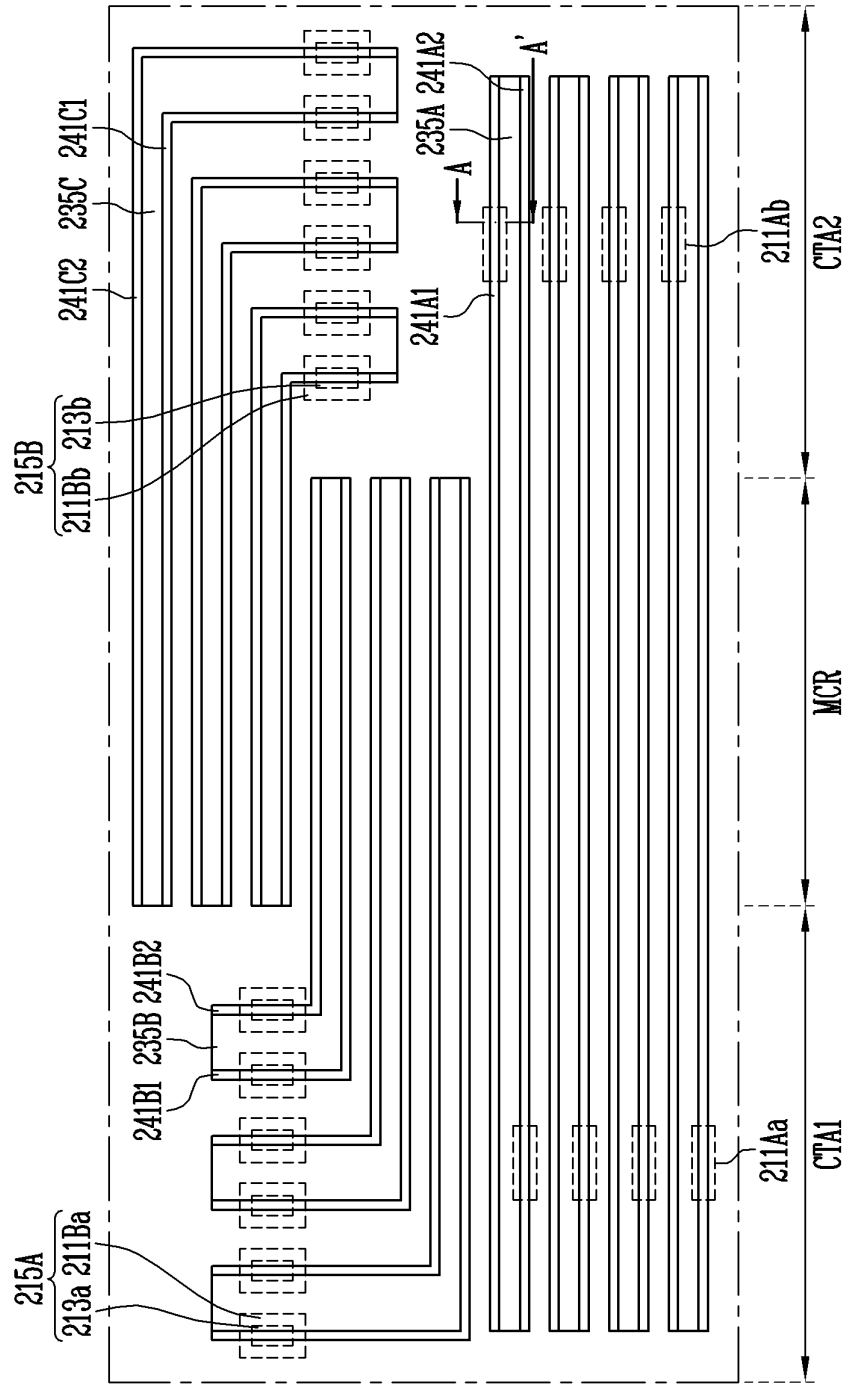


FIG. 14A

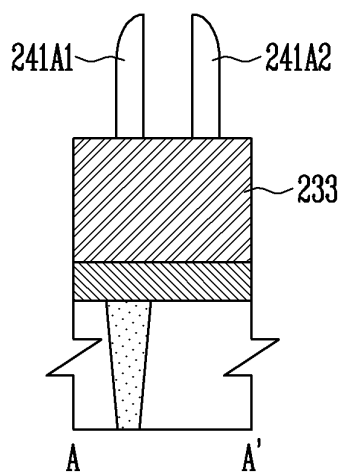


FIG. 14B

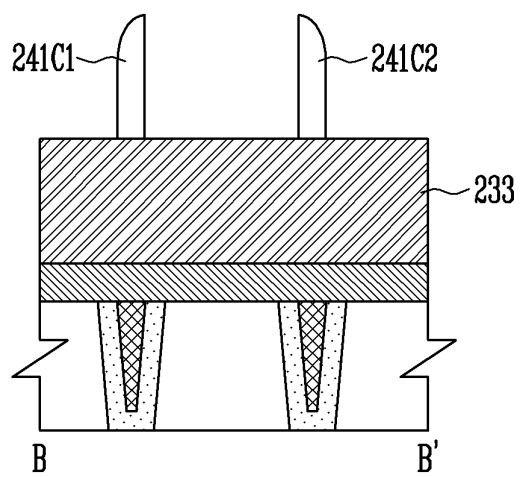


FIG. 15A

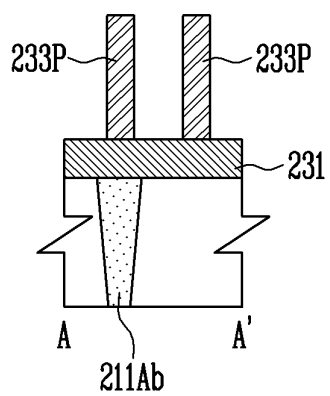


FIG. 15B

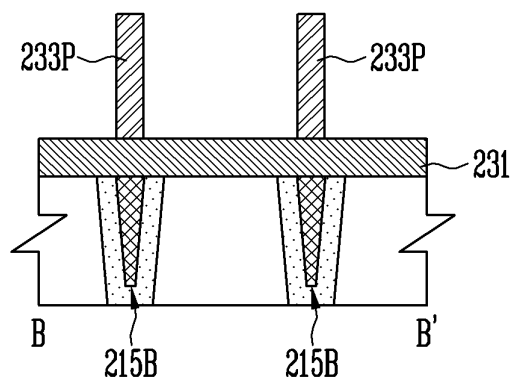


FIG. 16B

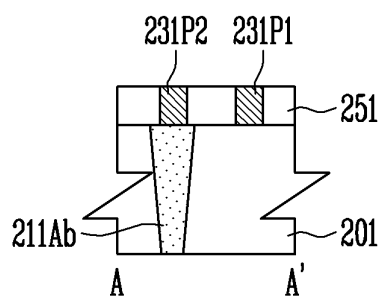


FIG. 16C

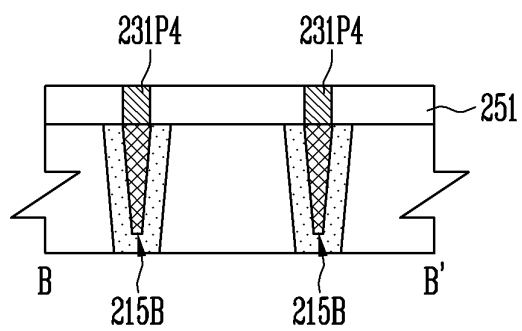


FIG. 17

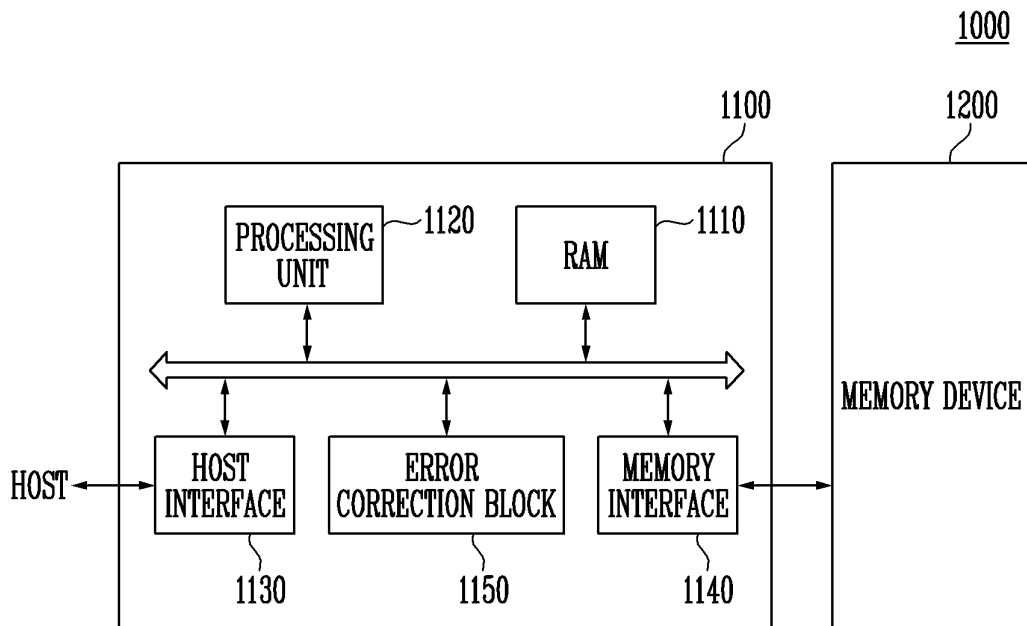


FIG. 18

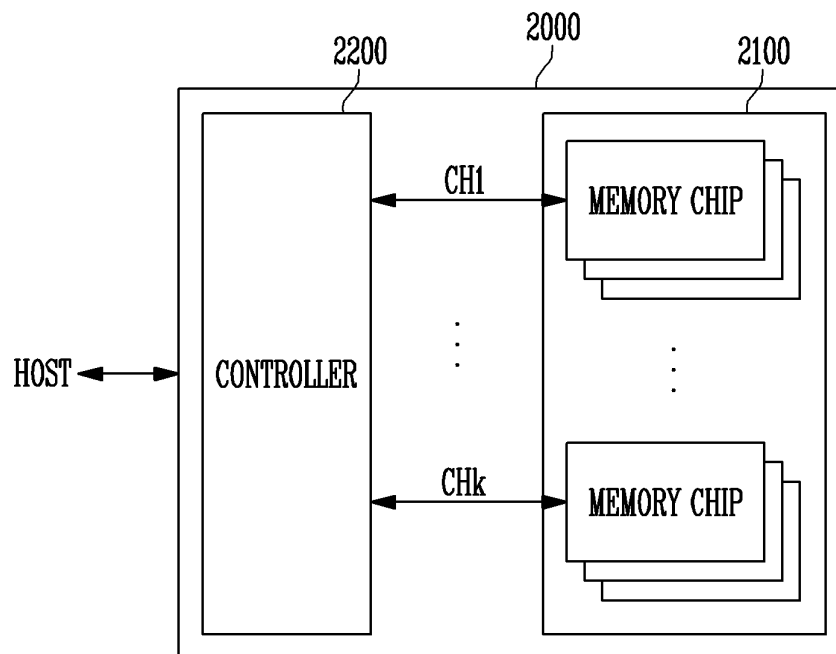


FIG. 19

