Fig. 1

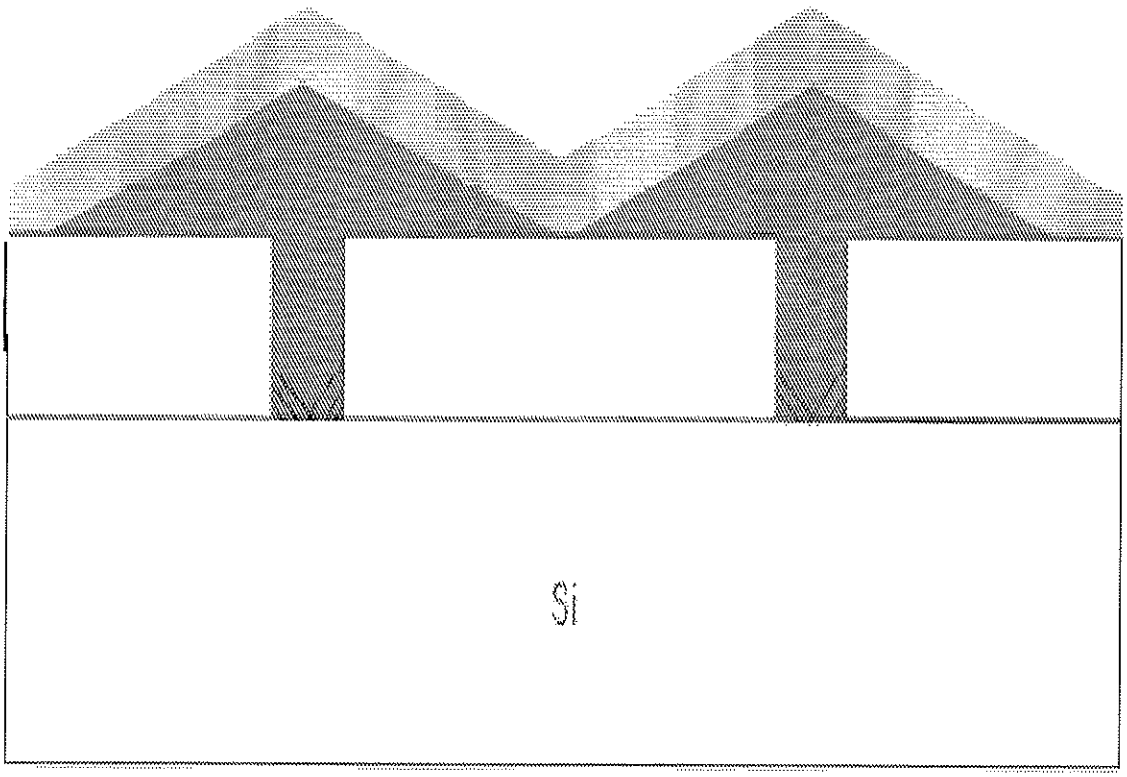


Fig. 2

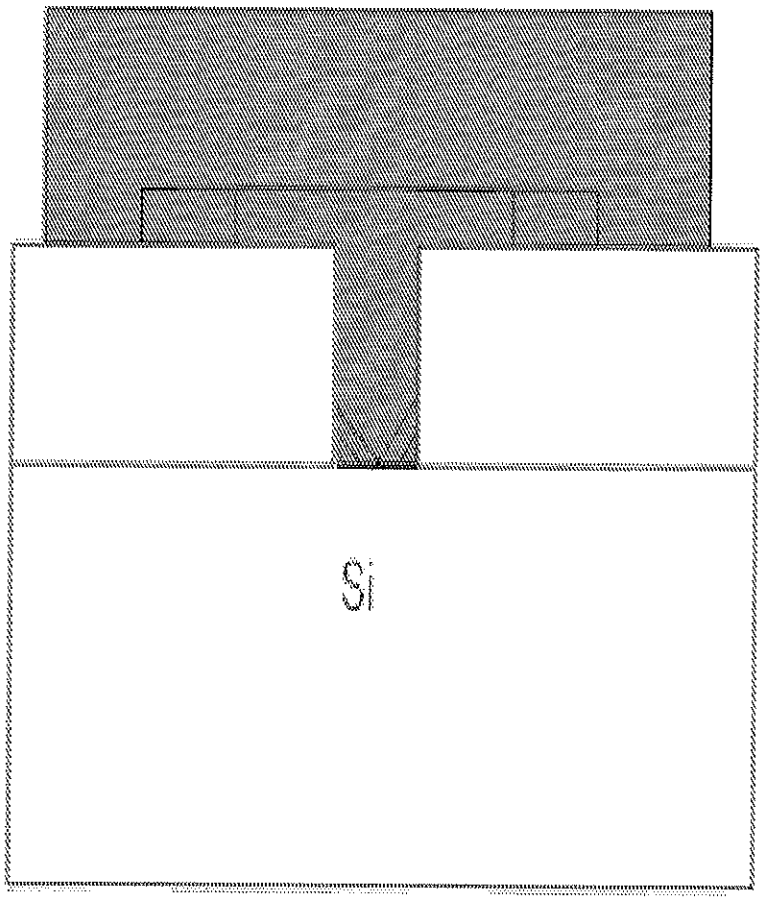


Fig. 3

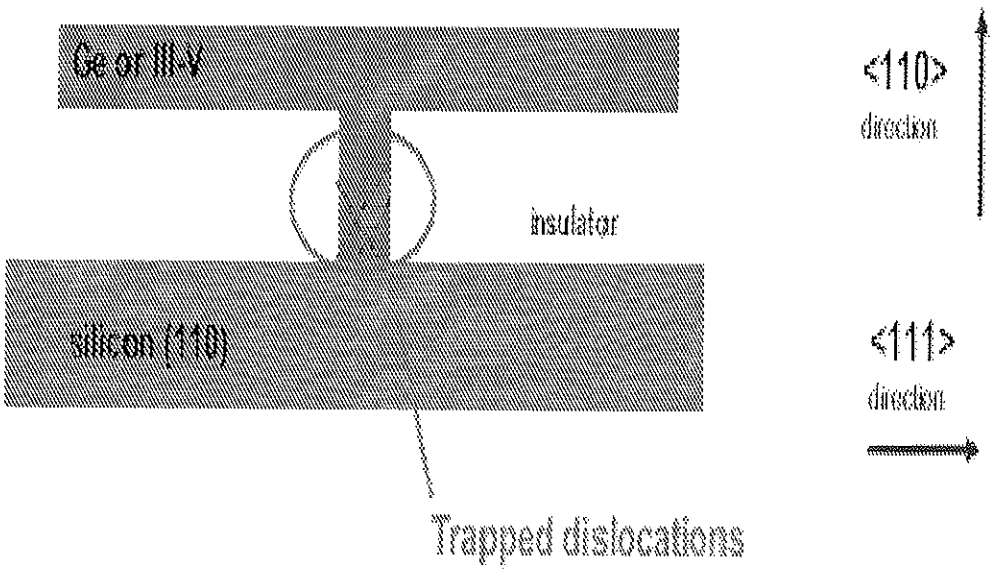

Ge or III-V
insulator
silicon (110)
Trapped dislocations
<110>
direction
<111>
direction

Fig. 4

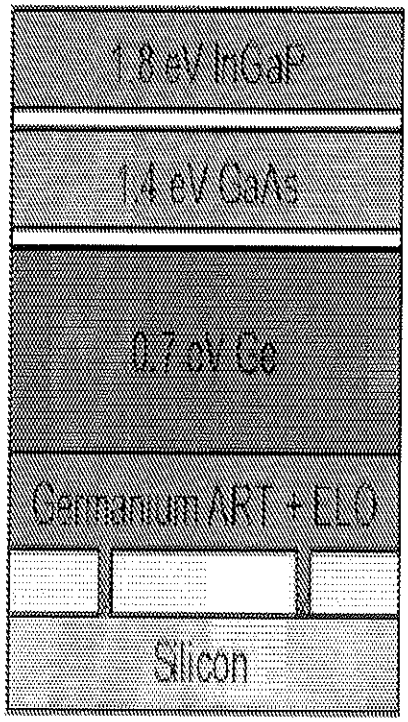
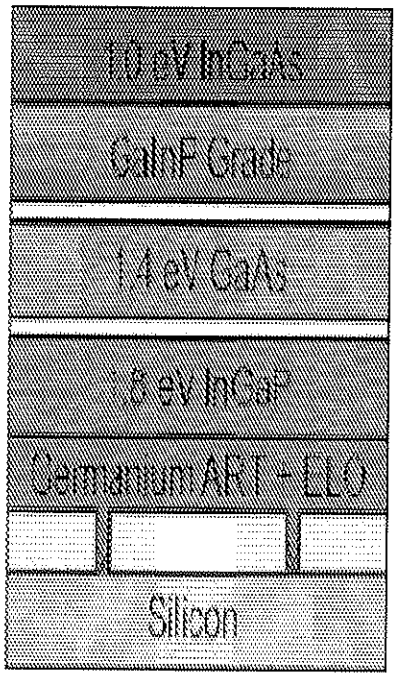
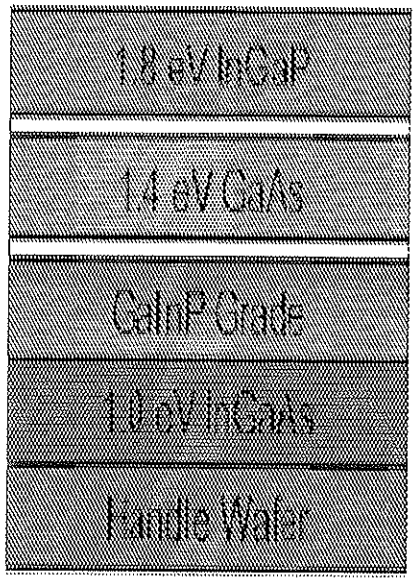


Fig. 5a



1) Inverted Growth on Ge ART + ELO

Fig. 5b



2) Flip, Bond, Selectively Remove Substrate