A STACKED ELECTRICAL DEVICE HAVING REGIONS OF ELECTRICAL ISOLATION AND ELECTRICAL CONNECTION ON A GIVEN STACK LEVEL, AND RELATED METHODS FOR ITS MANUFACTURE

ABSTRACT

An improved method for isolating electrical conductors which are positioned over each other is disclosed. These conductors would normally contact each other because of the somewhat imprecise patterning and etching steps used to fabricate a multitude of conductive elements, e.g., in a very dense semiconductor structure. The method involves forming a recess (94) in the upper surface of the lower conductor, and then at least partially filling the recess with an oxide-type material. This method is particularly valuable in the construction of stacked capacitor cells (74). Cells prepared using this technique also form part of this invention.

FIG. 5