VIDEO PREDICTIVE CODING METHOD AND APPARATUS

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] The present disclosure is based on and claims priority to Chinese Patent Application No. 202011459744.1, filed on December 11, 2020, the disclosure of which is herein incorporated by reference in its entirety.

TECHNICAL FIELD

[0002] Embodiments of the present disclosure relate to video coding technologies, for example, a method and an apparatus for video predictive coding.

BACKGROUND OF THE INVENTION

[0003] In a coding framework, predictive coding is essential in video coding, and predictive coding involves intra-frame prediction and inter-frame prediction. The intra-frame prediction is to predict a current pixel, according to a spatial correlation of a video image, using neighboring coded pixels in an image. The inter-frame coding is to predict a to-be-coded image, according to temporal correlation of a video image, using a coded image. By the intra-frame and inter-frame prediction, a coder eliminates the spatial and temporal correlations of a video, and transforms, quantizes, and entropy codes a predicted residual instead of an original pixel value, such that a coding efficiency is greatly improved.

SUMMARY OF THE INVENTION

[0004] Embodiments of the present disclosure provide a method and an apparatus for video predictive coding.

[0005] The embodiments of the present disclosure provide a method for video predictive coding. The method includes:

[0006] acquiring decision information related to a current coding block in inter-frame predictive coding, wherein the decision information includes one of: pre-analysis information determined by a coder in a lightweight video coding pre-analysis, coding information of a plurality of sub-blocks

acquired by recursively coding the current coding block, and information of an executed mode determined based on the executed mode; and

[0007] determining, based on the decision information, whether to skip motion estimation (ME) coding of the current coding block, wherein the skipping the ME coding includes skipping all ME and skipping partially ME.

[0008] In a second aspect, the embodiments of the present disclosure further provide an apparatus for video predictive coding. The apparatus includes:

[0009] a decision information acquiring module, configured to acquire decision information related to a current coding block in inter-frame predictive coding, wherein the decision information includes one of: pre-analysis information determined by a coder in a lightweight video coding pre-analysis, coding information of a plurality of sub-blocks acquired by recursively coding the current coding block, and information of an executed mode determined based on the executed mode; and

[0010] a skipping ME determining module, configured to determine, based on the decision information, whether to skip ME coding of the current coding block, wherein the skipping the ME coding includes skipping all ME and skipping partially ME.

[0011] The embodiments of the present disclosure further provide an electronic device for video predictive coding. The electronic device includes a memory, a processor, and a computer program stored on the memory and runnable on the processor, wherein the processor, when loading and running the program, is caused to perform the method described above.

[0012] The embodiments of the present disclosure further provide a computer-readable storage medium storing a computer program stored therein, wherein the computer program, when loaded and run by a processor, causes the processor to perform the method described above.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] FIG. 1 is a schematic diagram of a dividing mode of a PU according to some embodiments of the present disclosure;

[0014] FIG. 2 is a schematic diagram of a decision-making process for a best mode of an inter-frame predictive coding process of a single PU according to some embodiments of the present disclosure;

[0015] FIG. 3 is a schematic diagram of a coding process of a single inter-frame mode according to some embodiments of the present disclosure;

[0016] FIG. 4 is a flow chart of a method for video predictive coding according to some embodiments of the present disclosure;

[0017] FIG. 5 is a flow chart of a ME fast skipping decision based on pre-analysis information according to some embodiments of the present disclosure;

[0018] FIG. 6 is a schematic diagram of a relationship between recursive dividing of a coding block and a downsampling block according to some embodiments of the present disclosure;

[0019] FIG. 7 is a flow chart of a ME fast skipping decision based on coding information of a sub-block according to some embodiments of the present disclosure;

[0020] FIG. 8 is a schematic diagram of recursive dividing of a coding block according to some embodiments of the present disclosure;

[0021] FIG. 9 is a flow chart of a ME fast skipping decision for a non-square PU based on information of an executed mode according to some embodiments of the present disclosure;

[0022] FIG. 10 is a structural block diagram of an apparatus for video predictive coding according to some embodiments of the present disclosure; and

[0023] FIG. 11 is a schematic structural diagram of an electronic device according to some embodiments of the present disclosure.

DETAILED DESCRIPTION OF THE INVENTION

[0024] The present disclosure will be further described in detail hereinafter with reference to the accompanying drawings and the embodiments. It can be understood that the specific embodiments described herein are merely used for explaining the present disclosure and are not intended to limit the present disclosure. In addition, it shall be noted that for convenience of description, only the portions associated with the present disclosure, rather than the entire structure, are shown in the drawings.

[0025] Currently, in mainstream video coding standards, inter-frame prediction adopts block-based motion compensation (MC). A main principle is that motion estimation (ME) is executed in a reference frame by a prediction unit (PU) to search for a matched block, a prediction block is generated using the motion compensation technology to acquire a residual block, and then the residual block is used as an input of a subsequent coding module for the next coding processing. In addition, as not all object motion is full pixel motion, and only half a pixel or 1/4 pixel may be moved, sub-pixel ME is also required. That is, sub-pixel data is generated by an interpolation

method based on a full pixel, and then search and compensation of the matched block are performed in the sub-pixel. In high efficiency video coding (HEVC), a highest precision of a luminance component sub-pixel is 1/4 pixel and a highest precision of a chrominance component sub-pixel is 1/8 pixel.

[0026] Inter-frame coding PUs in the HEVC include at least seven cases: 2N x 2N/BIDIR, 2N x N/N x 2N/2N x nD/2N x nU/nR x 2N/nL x 2N (RECT/AMP), the above inter-frame coding process is performed on each of the PUs, and thus calculation overhead is very large.

[0027] The embodiments of the present disclosure are applicable to a predictive coding stage in video coding. Predictive coding indicates predicting a current sample value based on one or several coded sample values by a model or method, coding a difference value between a true value and a predicted value of a sample, and converting, quantifying, and entropy coding a predicted residual rather than an original pixel value, such that a coding efficiency is greatly improved. The predictive coding includes an intra-frame prediction (Intra) mode and an inter-frame prediction (Inter) mode.

[0028] A new video compression standard (high efficiency video coding, HEVC) defines a new set of syntax units including a coding unit (CU), a prediction unit (PU), and a transform unit (TU) for image dividing. The CU is a basic unit for processing, such as prediction, transformation, quantization, and entropy coding. The PU is a basic unit for predictive coding, including intra-frame prediction and inter-frame prediction. The TU is a basic unit for transformation and quantization. The PU specifies all prediction modes of the CU, and all information related to prediction is defined in the PU. For example, a direction of intra-frame prediction, a partitioning mode of inter-frame prediction, motion vector prediction, and an inter-frame predictive reference image index belong to a category of the PU.

[0029] FIG. 1 is a schematic diagram of a PU dividing mode. A PU dividing mode in a 2N x 2N CU includes: two alternative modes of an intra-frame prediction PU: 2N x 2N and N x N for a CU mode of 2N x 2N; eight alternative modes of an inter-frame predictive PU: four symmetric modes (2N x 2N, 2N x N, N x 2N, N x N) and four asymmetric modes (2N x nD, 2N x nU, nR x 2N, nL x 2N). Based on a shape of the PU, modes of 2N x 2N and N x N are referred to as square PUs, modes of 2N x N, N x 2N, 2N x nD, 2N x nU, nR x 2N, nL x 2N are referred to as non-square PUs. A skip mode is a kind of inter-frame prediction, and is a 2N x 2N skip mode in the case that motion information required to be coded is only a motion parameter set index, and the coding residual does not require to be coded.

[0030] FIG. 2 is a schematic diagram of a decision-making process for a best mode of an inter-frame predictive coding process of a single PU. As shown in FIG. 2, the decision-making process includes the following processes (N x N mode is not used in FIG. 2).

[0031] Firstly, a 2N x 2N merge/skip (MERGE/SKIP) mode is executed. In the case that a recursive condition is satisfied, a CU recursive mode is executed, a CU of a size of the current block is returned upon completion of the recursion, and the intra-frame prediction (Intra) mode is continuously executed. Upon completion of the Intra mode, the 2N x 2N inter-frame mode is executed (a best matched block is acquired by forward motion estimation (ME) and backward ME). Then 2N x 2N bidirectional (BIDIR) is executed (only bidirectional search is performed to acquire a best matched block), and 2N x N and N x 2N mode types are continuously executed. Mode decision is performed each completion of one mode to acquire the temporary best mode (tempBestMode).

[0032] Upon execution of the N x 2N mode, in the case that tempBestMode = 2N x N, modes of 2N x nU and 2N x nD are continuously executed, and comparison is made to acquire the final best mode (BestMode); in the case that tempBestMode = N x 2N, modes of nL x 2N and nR x 2N are continuously executed, and comparison is made to acquire the BestMode; and in the case that tempBestMode ! = 2N x N && tempBestMode ! = N x 2N, 2N x nU, modes of 2N x nD, nL x 2N, and nR x 2N are continuously executed, and comparison is made to acquire the BestMode.

[0033] It should be noted that in the case that CU recursion is performed on the current block, the above decision-making process is then executed with a unit of a sub-block, the cost of the BestMode is compared with a recursive cost to determine whether to perform the recursive dividing on the CU.

[0034] In addition, non-square modes, such as 2N x N, N x 2N, 2N x nU, 2N x nD, nL x 2N, nR x 2N, include two PUs, a MERGE/SKIP mode, a forward ME, a backward ME, and a bidirectional ME are required to be executed on each PU, and a best mode is acquired by comparison.

[0035] In addition, as not all object motion is full pixel motion, and only half a pixel or 1/4 pixel may be moved, the sub-pixel ME is also required. That is, sub-pixel data is generated by an interpolation method based on a full pixel, and then search and compensation of the matched block are performed in a sub-pixel, such that a highest precision of a luminance component sub-pixel is 1/4 pixel and a highest precision of a chrominance component is 1/8 pixel in HEVC. FIG. 3 shows a coding process of a single inter-frame mode in the plural modes of FIG. 2. For one PU, whether

to perform sub-pixel ME is first determined, motion estimation and motion compensation of a sub-pixel are performed based on a result of the determination of performing the sub-pixel ME, motion estimation and motion compensation of a full pixel are performed based on a result of the determination of not performing the sub-pixel ME, and then a rate-distortion optimization (RDO) coding process is performed on the acquired residual block.

[0036] It can be seen that the inter-frame coding process in FIG. 3 is performed on each inter-frame PU, such that an overhead is great. In the case that unnecessary PU ME is reduced, the resource overhead of the coder is reduced to a great extent, such that coding speed is increased, and a coding cost is reduced.

[0037] It should be noted that the following embodiments of the present disclosure are applicable to any block-based hybrid coding architecture, all coders in accordance with the coding standard of HEVC, and other coders in accordance with standards upon adjustment, such as AVS1, H.264, VP8, VP9, AVS2, AVS3, AV1, and VVC. In addition, the coder mentioned in the embodiments is applicable to both a transcoding service and a real-time coding service of a mobile terminal.

[0038] FIG. 4 is a flow chart of a method for video predictive coding according to some embodiments of the present disclosure. The embodiments include the following processes.

[0039] In S410, decision information related to a current coding block is acquired in inter-frame predictive coding, wherein the decision information includes: pre-analysis information determined by a coder in a lightweight video coding pre-analysis, coding information of a plurality of sub-blocks acquired by recursively coding the current coding block, and information of an executed mode determined based on the executed mode.

[0040] In S420, whether to skip ME coding of the current coding block is determined based on the decision information.

[0041] For example, for a decision-making process shown in FIG. 2, in a best mode decision of an inter-frame prediction mode for a current PU, whether to skip the ME process of the plurality of modes in the above decision-making process is determined based on the acquired decision information. Skipping the ME process includes two cases. One case is skipping all ME in the above decision-making process, and the other case is skipping partially ME in the above decision-making process. For example, ME of 2N x 2N mode is executed, and ME of other modes after 2N x 2N modes is skipped.

[0042] For a scenario of skipping all the ME in the above decision-making process, whether to skip the ME process is determined in different manners based on different acquired decision information in the embodiments.

[0043] In some embodiments, a decision to fast skip the ME coding is made based on the pre-analysis information, and the decision information includes the pre-analysis information determined by the coder in the lightweight video coding pre-analysis. As shown in FIG. 5, S420 includes the following sub-steps.

[0044] In S11, a number of corresponding downsampling blocks is determined based on a size of the current coding block, and the pre-analysis information of each of the downsampling blocks is indexed.

[0045] In some embodiments, a coder firstly performs a pre-analysis operation on a video by a pre-analysis module prior to actually coding an input video. For example, the pre-analysis operation indicates that the pre-analysis module performs a lightweight video coding on the basis of downsampling the input video to acquire a frame structure, a frame type, and a coding quantization parameter (QP) for actual coding.

[0046] A coding unit performed with the pre-analysis operation is an 8 x 8 downsampling block. For example, assuming that a size of an original block is w x h, and a size of a downsampled block is w/2 x h/2, the 8 x 8 downsampling block corresponds to a 16 x 16 coding block. That is, in the case that S11 is performed, a size of a coding block is at least 16 x 16, and includes 16 x 16, 32 x 32, and 64 x 64 in the embodiments.

[0047] In some embodiments, taking a 32 x 32 coding block as an example, as shown in FIG. 6, the 32 x 32 coding block is divided into four 16 x 16 coding sub-blocks, and each 16 x 16 coding sub-block corresponds to one downsampling block (the Lowres shown in FIG. 6), such that a number of the downsampling blocks acquired by the 32 x 32 coding block is four. Similarly, a 64 x 64 coding block is divided into 16 16 x 16 coding sub-blocks corresponding to 16 downsampling blocks.

[0048] As ME coding is also performed in a pre-analysis stage, in the case that the pre-analysis information of the downsampling block corresponding to the current coding block in the pre-analysis stage is found, a ME fast skipping decision for the current block is performed based on the pre-analysis information. In some embodiments, the corresponding downsampling block is indexed according to coordinate information of the current coding block. For example, in FIG. 6,

the 32 x 32 coding block is divided into four 16 x 16 coding sub-blocks, and the coordinate information of the coding block is known, such that the coordinate information of each 16 x 16 coding sub-block is acquired. Then, a downsampling block within a coordinate range of each coding sub-block is found and determined as a downsampling block corresponding to the coding sub-block, and the pre-analysis information of the downsampling block is acquired upon the downsampling block corresponding to each coding block being found.

[0049] In some embodiments, the pre-analysis information of the downsampling block includes, but is not limited to: an inter-frame coding cost, an intra-frame coding cost, and a predicted best mode of the downsampling block.

[0050] In S12, the pre-analysis information of the downsampling blocks is summarized, and whether to skip the ME coding of the current coding block is determined based on a result of the summarization.

[0051] Upon acquisition of all the downsampling blocks corresponding to the current coding block, the pre-analysis information of all the downsampling blocks corresponding to the current coding block is summarized, and whether an ME process of the inter-frame prediction mode is skipped is determined based on a result of the summarization.

[0052] In some embodiments, S12 includes the following sub-steps.

[0053] In S121, an estimated inter-frame coding cost of the current coding block is acquired by calculating a sum of inter-frame coding costs of all the downsampling blocks corresponding to the current coding block.

[0054] For example, the 32 x 32 coding block in FIG. 6 indexes four downsampling blocks, and upon acquisition of the inter-frame coding cost of each downsampling block, a sum of inter-frame coding costs of the four downsampling blocks is calculated and determined as an estimated inter-frame coding cost (InterCost) of the 32 x 32 coding block.

[0055] In S122, an estimated intra-frame coding cost of the current coding block is acquired by calculating a sum of intra-frame coding costs of all the downsampling blocks corresponding to the current coding block.

[0056] For example, for the 32 x 32 coding block in FIG. 6, upon index of the intra-frame coding costs of the four downsampling blocks, a sum of the intra-frame coding costs of the four downsampling blocks is calculated and determined as an estimated intra-frame coding cost (IntraCost) of the 32 x 32 coding block.

[0057] In S123, a number of downsampling blocks of which the best mode is an intra-frame prediction mode in all the downsampling blocks corresponding to the current coding block is calculated, and a first ratio at which the best mode of the current coding block is the intra-frame prediction mode is determined based on the number.

[0058] For example, for the 32 x 32 coding block in FIG. 6, upon index of the best modes of the four downsampling blocks, a number of downsampling blocks of which the best modes are the intra-frame prediction (Intra) modes in the four downsampling blocks and the first ratio (IntraRatio) are calculated. For example, in the case that the best modes of two downsampling blocks are the Intra mode, the IntraRatio is 2/4. For example, in the case that the best modes of three downsampling blocks are the Intra mode, the IntraRatio is 3/4.

[0059] In S124, in the case that the first ratio is greater than a first predetermined ratio threshold, and a product of the estimated intra-frame coding cost and a first adjustment factor is less than a product of the estimated inter-frame coding cost and a second adjustment factor, the ME coding of the current coding block is skipped, wherein the first adjustment factor is less than the second adjustment factor.

[0060] In the step, upon acquisition of the InterCost, IntraCost, and IntraRatio of the current coding block, the ME coding is skipped in the case that the following conditional expression (1) is satisfied:

$$\text{if (IntraRatio} > a \;\&\&\; \text{IntraCost} * b < c * \text{InterCost)} \quad (1),$$

$$\text{skipME} = \text{true};$$

[0061] a represents a first predetermined ratio threshold, b represents a first adjustment factor, c represents a second adjustment factor, and skipME is skipping of ME. It should be noted that a, b, and c are adjusted according to requirements, and a coding speed is increased or decreased linearly on a premise of maintaining a consistent acceleration cost-effectiveness to meet different service requirements. For example, b is set to less than c according to service requirements. For example, default values of a, b, and c are set to 0.5, 1, and 4.

[0062] According to the above conditional expression (1), in the case that IntraCost * b < c * InterCost, and IntraRatio is great, a spatial correlation is stronger than a temporal correlation of the current block, such that the ME process is directly skipped.

[0063] In some embodiments, in the case that the current coding block is recursively coded, a decision to fast skip the ME is performed based on the coded information of the sub-block, and

the decision information includes coding information of a plurality of sub-blocks acquired by recursively coding the coding block. Illustratively, the coding information of the sub-block at least includes the best mode of the sub-block and whether to further recursively divide the sub-block. As shown in FIG. 7, S420 includes the following sub-steps.

[0064] In S21, a second ratio at which the best mode of the current coding block is an intra-frame prediction mode and a third ratio at which the best mode of the current coding block is a SKIP mode are determined based on the best mode of each of the sub-blocks of the current coding block.

[0065] In some embodiments, the second ratio at which the best mode of the current coding block is the intra-frame prediction model is a ratio of a number of the sub-blocks of which the best mode is the intra-frame prediction model in the current coding block to a total number of all the sub-blocks in the current coding block. The third ratio at which the best mode of the current coding block is the SKIP mode is a ratio of a number of the sub-blocks of which the best mode is the intra-frame prediction SKIP mode in the current coding block to a total number of all the sub-blocks in the current coding block.

[0066] For example, as shown in FIG. 8, assuming that the current coding block is divided into four 16 x 16 coding sub-blocks, a number of the sub-blocks of which the best mode is the Intra mode is one, and a number of the sub-blocks of which the best mode is the SKIP mode is two in the four coding sub-blocks, the second ratio is 1/4, and the third ratio is 2/4.

[0067] In S22, a recursion ratio of the plurality of sub-blocks of the current coding block is determined based on whether to recursively divide each of the plurality of sub-blocks of the current coding block.

[0068] In the step, the recursion ratio of the sub-block is indicative of a ratio of the dividable sub-block in the current coding block. In some embodiments, the recursion ratio of the sub-block of the current coding block is a ratio of a number of the sub-blocks that are further recursively coded in the current coding block to a total number of all the sub-blocks of the current coding block. For example, as shown in FIG. 8, assuming that the current coding block is divided into four 16 x 16 coding sub-blocks, and the number of the sub-blocks capable of being further recursively coded is one in the four coding sub-blocks (a Split sub-block shown in FIG. 8), the recursion ratio of the sub-block is 1/4.

[0069] In S23, whether to skip the ME coding of the current coding block is determined based on the second ratio, the third ratio, and the recursion ratio of the plurality of sub-blocks.

[0070] In some embodiments, S23 includes the following sub-steps.

[0071] In the case that the recursion ratio of the plurality of sub-blocks is greater than a predetermined recursion ratio threshold, and the second ratio is greater than a second predetermined ratio threshold, the ME coding of the current coding block is skipped.

[0072] In the step, upon acquisition of the recursion ratio of the sub-block and the second ratio, the ME coding is skipped in the case that the following conditional expression (2) is satisfied:

if (SubSplitRatio > threshold_A && SubIntraRatio > threshold_B) (2),

skipME = true;

[0073] SubSplitRatio represents the recursion ratio of the sub-block, SubIntraRatio represents the second ratio, threshold_A represents the predetermined recursion ratio threshold, threshold_B represents the second predetermined ratio threshold, and threshold_A and threshold_B are set according to actual service requirements.

[0074] In the case that SubSplitRatio > threshold_A and SubIntraRatio > threshold_B, there are a large number of sub-blocks Split blocks in the current coding block at a Intra ratio. In this case, the current coding block is more easily divided into four sub-blocks for coding, such that the ME process of the current coding block is skipped.

[0075] In some embodiments, S23 includes the following sub-steps.

[0076] In the case that the third ratio is greater than a third predetermined ratio threshold, and the recursion ratio of the plurality of sub-blocks is greater than a fourth predetermined ratio threshold, the ME coding of the current coding block is skipped.

[0077] In the step, upon acquisition of the recursion ratio of the sub-block and the third ratio, the ME coding is skipped in the case that the following conditional expression (3) is satisfied:

if (SubSkipRatio > threshold_C && SubSplitRatio > threshold_D) (3),

skipME = true;

[0078] SubSplitRatio represents the recursion ratio of the sub-block, SubSkipRatio represents the third ratio, threshold_C represents the third predetermined ratio threshold, threshold_D represents the fourth predetermined ratio threshold, and the threshold_C and the threshold_D are set according to actual service requirements.

[0079] In the case that SubSkipRatio > threshold_C and SubSplitRatio > threshold_D, there are a large number of sub-blocks Split blocks in the current coding block at a high SKIP mode ratio. In

this case, the current coding block has weak motion, such that the remaining ME process is skipped.

[0080] In some embodiments, in the case that the current coding block CU is not recursively coded, whether to skip the ME coding is determined based on mode information of an executed mode. In the case, the decision information includes the information of the executed mode determined based on the executed mode. Illustratively, the information of the executed mode at least includes a SATD cost of a temporary best mode and an intra-frame prediction SATD cost. S420 includes the following sub-steps.

[0081] In the case that the current coding block is not recursively coded, and a product of the intra-frame prediction SATD cost and a third adjustment factor is less than a product of the SATD cost of the temporary best mode and a fourth adjustment factor, the ME coding of the current coding block is skipped, wherein the third adjustment factor is greater than the fourth adjustment factor.

[0082] In the embodiments, the SATD cost of the temporary best mode is a SATD cost of the temporary best mode acquired by executing a mode decision upon execution of the mode. The intra-frame prediction SATD cost is the SATD cost of the best intra-frame mode determined by a SATD rough decision in the intra-frame prediction.

[0083] Upon acquisition of the SATD cost of the temporary best mode and the intra-frame prediction SATD cost, the ME coding is skipped in the case that the following conditional expression (4) is satisfied:

$$\text{if (IntraSATDCost} * \alpha < \beta * \text{InterSATDCost)} \quad (4)$$

$$\text{skipME} = \text{true};$$

[0084] IntraSATDCost represents the intra-frame prediction SATD cost, InterSATDCost represents the SATD cost of the temporary best mode SATD cost, α represents the third adjustment factor, β represents the fourth adjustment factor, and both α and β are adjusted according to actual service requirements. For example, a default value of α is 5, and a default value of β is 3.

[0085] In the case that IntraSATDCost * α is less than β* InterSATDCost, the spatial correlation is stronger than the temporal correlation of the current block, such that a ME coding process of the current coding block is skipped.

[0086] In some embodiments, in a scenario of skipping partially ME in the above decision-making process, for example, for non-square PUs, such as 2N x N/N x 2N/2N x nD/2N x nU/nR x 2N/nL x 2N (RECT/AMP), a MERGE mode, a forward ME, a backward ME, and a bidirectional ME are

performed on each PU, and a best mode is acquired by comparison. As a complexity of an overall ME process is high, a decision to quickly skip the above ME of the non-square PUs is made based on an executed ME mode. The decision information includes the information of the executed mode determined based on the executed mode. The executed mode at least includes a 2N x 2N mode and a BIDIR mode in FIG. 2, and the information of the executed mode includes a SATD cost of a MERGE mode executed in the 2N x 2N mode, and the SATD cost of the temporary best mode.

[0087] As shown in FIG. 9, step 420 includes the following sub-steps.

[0088] In S31, in the case that the current prediction unit is a non-square prediction unit, whether the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode is determined in response to determining that a current prediction unit is a non-square prediction unit.

[0089] In S32, the MERGE mode is executed on the non-square prediction unit in response to determining that the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode, and the forward ME coding, the backward ME coding, and the bidirectional ME coding of the non-square prediction unit are skipped.

[0090] In the embodiments, in the case that the SATD cost of the MERGE mode executed in the 2N x 2N mode is less than the SATD cost of the temporary best mode, only the MERGE mode is executed on the current non-square PU, and the forward ME coding, the backward ME coding, and the bidirectional ME coding processes are not performed.

[0091] In some embodiments, in the case that the current coding block is recursively coded, in determining whether to skip the forward ME, the backward ME, and the bidirectional ME of a non-square PU mode, a ratio at which the best mode of the current coding block is the SKIP mode is also considered. That is, a calculation method of a ratio of the sub-block whose best mode is the SKIP mode in the coding sub-blocks of the current coding block is referred to the above description of the third ratio, and S420 includes the following sub-steps.

[0092] In the case that the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode, and the ratio at which the best mode of the current coding block is the SKIP mode is greater than the predetermined ratio threshold, the MERGE mode is executed on the non-square prediction unit, and the forward ME coding, the backward ME coding, and the bidirectional ME coding of the non-square prediction unit are skipped.

[0093] In implementation, the coding processes of the forward ME, the backward ME, and the bidirectional ME are skipped, and only the MERGE mode of the mode is executed in the case that the current non-square mode satisfies the following conditional expression (5):

$$\text{if (SubSkipRatio} > \theta \;\&\&\; \text{MrgCost} * a1 < b1 * \text{InterSATDCost)} \quad (5)$$

$$\text{skipMELeft} = \text{true};$$

[0094] SubSkipRatio represents the ratio at which the best mode of the current coding block is the SKIP mode, MrgCost represents the SATD cost of the MERGE mode executed in the 2N x 2N mode, InterSATDCost represents the SATD cost of the temporary best mode, and θ, a1, and b1 represent adjustment factors, and are set according to actual service requirements. For example, a default value of θ is set to 0.8, and default values of a1 and b1 are both 1. SkipMELeft represents that RECT/AMP only executes the MERGE mode, and does not execute the forward ME, the backward ME, and the bidirectional ME.

[0095] In the embodiments, whether to skip the ME coding of the current coding block is determined based on the acquired decision information related to the current coding block in the inter-frame predictive coding. The decision information includes the pre-analysis information determined by the coder in the lightweight video coding pre-analysis, the coding information of the plurality of sub-blocks acquired by recursively coding the current coding block, or the information of the executed mode determined based on the executed mode. Based on different decision information, a ME skipping decision is made by different fast decision solutions, such that the unnecessary PU ME is reduced, the coding complexity is reduced to a great extent, the resource overhead of the coder is reduced, and the coding compression speed of the coder is further improved. The throughput of the transcoding service is improved upon the improvement of the compression speed of the coder, such that the server resources are saved, and the transcoding cost is reduced. By the decision to skip the ME coding in the embodiments, the coding speed is increased by 18% under a condition that the loss of the compression rate of the coder is 0.2%. From a perspective of online transcoding application, 18% of server calculation resources is saved under small image quality loss, and the transcoding cost is further reduced.

[0096] FIG. 10 is a structural block diagram of an apparatus for video predictive coding according to an embodiment of the present disclosure. The apparatus includes the following modules:

[0097] a decision information acquiring module 1001, configured to acquire decision information related to a current coding block in inter-frame predictive coding, wherein the decision information

includes: pre-analysis information determined by a coder in a lightweight video coding pre-analysis, coding information of a plurality of sub-blocks acquired by recursively coding the current coding block, or information of an executed mode determined based on the executed mode; and

[0098] a skipping ME determining module 1002, configured to determine, based on the decision information, whether to skip ME coding of the current coding block.

[0099] In some embodiments, in the case that the decision information is the pre-analysis information determined by the coder in the lightweight video coding pre-analysis, the skipping ME determining module 1002 includes the following sub-modules:

[0100] a pre-analysis information indexing sub-module, configured to determine, based on a size of the current coding block, a number of corresponding downsampling blocks, and index the pre-analysis information of each of the downsampling blocks.

[0101] a pre-analysis information determining sub-module, configured to summarize the pre-analysis information of the downsampling blocks, and determine, based on a result of the summarization, whether to skip the ME coding of the current coding block.

[0102] In some embodiments, the pre-analysis information of the downsampling block includes an inter-frame coding cost, an intra-frame coding cost, and a predicted best mode of the downsampling block.

[0103] The pre-analysis information determining sub-module is configured to:

[0104] acquire an estimated inter-frame coding cost of the current coding block by calculating a sum of inter-frame coding costs of all the downsampling blocks corresponding to the current coding block;

[0105] acquire an estimated intra-frame coding cost of the current coding block by calculating a sum of intra-frame coding costs of all the downsampling blocks corresponding to the current coding block;

[0106] calculate a number of downsampling blocks of which the best mode is an intra-frame prediction mode in all the downsampling blocks corresponding to the current coding block, and determine, based on the number, a first ratio at which the best mode of the current coding block is the intra-frame prediction mode; and

[0107] skip the ME coding of the current coding block in the case that the first ratio is greater than a first predetermined ratio threshold, and a product of the estimated intra-frame coding cost and a

first adjustment factor is less than a product of the estimated inter-frame coding cost and a second adjustment factor, wherein the first adjustment factor is less than the second adjustment factor.

[0108] In some embodiments, in the case that the decision information is the coding information of the plurality of sub-blocks acquired by recursively coding the current coding block, the coding information of the plurality of sub-blocks includes a best mode of each of the plurality of sub-blocks and whether to recursively divide each of the plurality of sub-blocks.

[0109] The skipping ME determining module 1002 includes the following sub-modules:

[0110] a ratio calculating sub-module, configured to determine, based on the best mode of each of the plurality of sub-blocks of the current coding block, a second ratio at which the best mode of the current coding block is the intra-frame prediction mode, and a third ratio at which the best mode of the current coding block is a skipping (SKIP) mode; and determine a recursion ratio of the plurality of sub-blocks of the current coding block based on whether to recursively divide each of the plurality of sub-blocks of the current coding block; and

[0111] a ratio determining sub-module, configured to determine, based on at least one of the second ratio, the third ratio, and the recursion ratio of the plurality of sub-blocks, whether to skip the ME coding of the current coding block.

[0112] In some embodiments, the ratio determining sub-module is configured to:

[0113] skip the ME coding of the current coding block in the case that the recursion ratio of the plurality of sub-blocks is greater than a predetermined recursion ratio threshold, and the second ratio is greater than a second predetermined ratio threshold;

[0114] or,

[0115] skip the ME coding of the current coding block in the case that the third ratio is greater than a third predetermined ratio threshold, and the recursion ratio of the plurality of sub-blocks is greater than a fourth predetermined ratio threshold.

[0116] In some embodiments, in the case that the decision information is the information of the executed mode determined based on the executed mode, the information of the executed mode includes: a SATD cost of a temporary best mode and an intra-frame prediction SATD cost.

[0117] The skipping ME determining module 1002 includes the following sub-modules:

[0118] a SATD cost determining sub-module, configured to skip the ME coding of the current coding block in the case that the current coding block is not recursively coded, and a product of the intra-frame prediction SATD cost and a third adjustment factor is less than a product of the

SATD cost of the temporary best mode and a fourth adjustment factor, wherein the third adjustment factor is greater than the fourth adjustment factor.

[0119] In some embodiments, in the case that the decision information is the information of the executed mode determined based on the executed mode, the executed mode at least includes a 2N x 2N mode and a BIDIR mode, and the information of the executed mode includes: an SATD cost of a MERGE mode executed in the 2N x 2N mode, and the SATD cost of the temporary best mode.

[0120] The skipping ME determining module 1002 includes the following sub-modules:

[0121] a first part ME skipping determining sub-module, configured to determine, in response to determining that a current prediction unit is a non-square prediction unit, whether the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode; and execute the MERGE mode on the non-square prediction unit in response to determining that the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode, and skip the forward ME coding, the backward ME coding, and the bidirectional ME coding of the non-square prediction unit.

[0122] In some embodiments, in the case that the current coding block is recursively coded, the information of the executed mode further includes a ratio at which a best mode of the current coding block is a SKIP mode.

[0123] The skipping ME determining module 1002 includes the following sub-modules:

[0124] a first part ME skipping determining sub-module, configured to execute the MERGE mode on the non-square prediction unit in the case that the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode, and the ratio at which the best mode of the current coding block is the SKIP mode is greater than a predetermined ratio threshold, and skip the forward ME coding, the backward ME coding, and the bidirectional ME coding of the non-square prediction unit.

[0125] It should be noted that the apparatus for video predictive coding in the embodiments of the present disclosure performs the method for video predictive coding in the embodiments of the present disclosure, and has corresponding functional modules and beneficial effects to execute the methods.

[0126] FIG. 11 is a schematic structural diagram of an electronic device according to some embodiments of the present disclosure. As shown in FIG. 11, the electronic device includes a processor 1110, a memory 1120, an input apparatus 1130, and an output apparatus 1140. The

number of the processors 1110 in the electronic device is one or more, and FIG. 11 is illustrated by taking one processor 1110 as an example. The processor 1110, the memory 1120, the input apparatus 1130, and the output apparatus 1140 in the electronic device are connected by a bus or other means, and by way of example, and FIG. 11 is illustrated by taking the bus as an example.

[0127] The memory 1120, as a computer-readable storage medium, is configured to store software programs, computer executable programs, and modules, such as program instructions/modules corresponding to the method in the embodiments of the present disclosure. By executing software programs, instructions, and modules stored in the memory 1120, the processor 1110 performs various functional applications and data processing of the electronic device, that is, achieves the above method.

[0128] The memory 1120 mainly includes a program storage region and a data storage region.

[0129] The program storage region stores an operating system and an application program required by at least one function. The storage data region stores data created based on a use of a terminal, and the like. Furthermore, the memory 1120 includes a high speed random access memory, and further includes a non-volatile memory, such as at least one magnetic disk memory, flash memory, or other non-volatile solid state memory. In some embodiments, the memory 1120 includes memories arranged remotely relative to the processor 1110, the remote memories are connected to the electronic device over a network. Examples of the above network include, but are not limited to, the Internet, intranets, local area networks, mobile communication networks, and combinations thereof.

[0130] The input apparatus 1130 is configured to receive input digital or character information, and generate key signal inputs related to user settings and function control of the electronic device. The output apparatus 1140 includes a display device, such as a display screen.

[0131] Some embodiments of the present disclosure further provide a storage medium including computer executable instructions. The computer executable instructions, when executed by a processor of a server, causes the processor to perform the method in any one of the embodiments of the present disclosure.

[0132] Based on the above description of the embodiments, it is obvious for those skilled in the art that the present disclosure is implemented either through software and necessary general-purpose hardware, or through hardware. Based on such understanding, the technical solutions of the present disclosure, or the parts that contribute to the related art are substantially embodied in

the form of a software product. The computer software product is stored in a computer-readable storage medium, such as a floppy disk, a read-only memory (ROM), a random-access memory (RAM), a flash memory (FLASH), a hard disk, or an optical disk of a computer, and includes several instructions, such that a computer device (a personal computer, a server, or a network device) performs the method according to the embodiments of the present disclosure. The computer-readable storage medium is a non-transitory computer-readable storage medium.

[0133] It should be noted that in the above embodiments of the apparatus, a plurality of units and modules are only divided based on function logic, but are not limited to the above division, as long as corresponding functions are achieved. In addition, specific names of multiple function units are merely for convenience of distinguishing from each other, and are not intended to limit the protection scope of the present disclosure.

CLAIMS

What is claimed is:

1. A method for video predictive coding, comprising:

acquiring decision information related to a current coding block in inter-frame predictive coding, wherein the decision information comprises one of: pre-analysis information determined by a coder in a lightweight video coding pre-analysis, coding information of a plurality of sub-blocks acquired by recursively coding the current coding block, and information of an executed mode determined based on the executed mode; and

determining, based on the decision information, whether to skip motion estimation (ME) coding of the current coding block, wherein the skipping the ME coding comprises skipping all ME and skipping partially ME.

2. The method according to claim 1, wherein in response to determining that the decision information is the pre-analysis information determined by the coder in the lightweight video coding pre-analysis, determining, based on the decision information, whether to skip the ME coding of the current coding block comprises:

determining, based on a size of the current coding block, a number of corresponding downsampling blocks, and indexing pre-analysis information of each of the downsampling blocks, wherein the number of the downsampling blocks is plural; and

summarizing the pre-analysis information of the downsampling blocks, and determining, based on a result of the summarization, whether to skip the ME coding of the current coding block.

3. The method according to claim 2, wherein

the pre-analysis information of the downsampling block comprises an inter-frame coding cost, an intra-frame coding cost, and a predicted best mode of the downsampling block; and

summarizing the pre-analysis information of the downsampling blocks, and determining, based on the result of the summarization, whether to skip the ME coding of the current coding block comprise:

acquiring an estimated inter-frame coding cost of the current coding block by calculating a sum of the inter-frame coding costs of all the downsampling blocks corresponding to the current coding block;

acquiring an estimated intra-frame coding cost of the current coding block by calculating a sum of the intra-frame coding costs of all the downsampling blocks corresponding to the current coding block;

calculating a number of downsampling blocks of which the best mode is an intra-frame prediction mode in all the downsampling blocks corresponding to the current coding block, and determining, based on the number, a first ratio at which the best mode of the current coding block is the intra-frame prediction mode; and

skipping the ME coding of the current coding block in response to determining that the first ratio is greater than a first predetermined ratio threshold, and that a product of the estimated intra-frame coding cost and a first adjustment factor is less than a product of the estimated inter-frame coding cost and a second adjustment factor, wherein the first adjustment factor is less than the second adjustment factor.

4. The method according to claim 1, wherein

in response to determining that the decision information is the coding information of the plurality of sub-blocks acquired by recursively coding the current coding block, the coding information of the plurality of sub-blocks comprises a best mode of each of the plurality of sub-blocks and whether to recursively divide each of the plurality of sub-blocks; and

determining, based on the decision information, whether to skip the ME coding of the current coding block comprises:

determining, based on the best mode of each of the plurality of sub-blocks of the current coding block, a second ratio at which the best mode of the current coding block is an intra-frame prediction mode, and a third ratio at which the best mode of the current coding block is a skipping (SKIP) mode;

determining a recursion ratio of the plurality of sub-blocks of the current coding block based on whether to recursively divide each of the plurality of sub-blocks of the current coding block; and

determining, based on at least one of the second ratio, the third ratio, and the recursion ratio of the plurality of sub-blocks, whether to skip the ME coding of the current coding block.

5. The method according to claim 4, wherein determining, based on at least one of the second

ratio, the third ratio, and the recursion ratio of the plurality of sub-blocks, whether to skip the ME coding of the current coding block comprises:

skipping the ME coding of the current coding block in response to determining that the recursion ratio of the plurality of sub-blocks is greater than a predetermined recursion ratio threshold, and that the second ratio is greater than a second predetermined ratio threshold; or

skipping the ME coding of the current coding block in response to determining that the third ratio is greater than a third predetermined ratio threshold, and that the recursion ratio of the plurality of sub-blocks is greater than a fourth predetermined ratio threshold.

6. The method according to claim 1, wherein

in response to determining that the decision information is the information of the executed mode determined based on the executed mode, the information of the executed mode comprises a sum of absolute transformed difference (SATD) cost of a temporary best mode and an intra-frame prediction SATD cost; and

determining, based on the decision information, whether to skip the ME coding of the current coding block comprises:

skipping the ME coding of the current coding block in response to determining that the current coding block is not recursively coded, and that a product of the intra-frame prediction SATD cost and a third adjustment factor is less than a product of the SATD cost of the temporary best mode and a fourth adjustment factor, wherein the third adjustment factor is greater than the fourth adjustment factor.

7. The method according to claim 1, wherein

in response to determining that the decision information is the information of the executed mode determined based on the executed mode, the executed mode at least comprises a 2N x 2N mode and a BIDIR mode, and the information of the executed mode comprises an SATD cost of a MERGE mode executed in the 2N x 2N mode, and the SATD cost of a temporary best mode, wherein N is a positive integer; and

determining, based on the decision information, whether to skip the ME coding of the current coding block comprises:

determining, in response to determining that a current prediction unit is a non-square

prediction unit, whether the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode; and

executing the MERGE mode on the non-square prediction unit in response to determining that the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode, and skipping forward ME coding, backward ME coding, and bidirectional ME coding of the non-square prediction unit.

8. The method according to claim 7, wherein in response to determining that the current coding block is recursively coded, the information of the executed mode further comprises a ratio at which a best mode of the current coding block is a SKIP mode; and

determining, based on the decision information, whether to skip the ME coding of the current coding block further comprises:

executing the MERGE mode on the non-square prediction unit in response to determining that the SATD cost of the MERGE mode is less than the SATD cost of the temporary best mode, and that the ratio at which the best mode of the current coding block is the SKIP mode is greater than a predetermined ratio threshold, and skipping the forward ME coding, the backward ME coding, and the bidirectional ME coding of the non-square prediction unit.

9. An apparatus for video predictive coding, comprising:

a decision information acquiring module, configured to acquire decision information related to a current coding block in inter-frame predictive coding, wherein the decision information comprises one of: pre-analysis information determined by a coder in a lightweight video coding pre-analysis, coding information of a plurality of sub-blocks acquired by recursively coding the current coding block, and information of an executed mode determined based on the executed mode; and

a skipping ME determining module, configured to determine, based on the decision information, whether to skip motion estimation (ME) coding of the current coding block, wherein the skipping the ME coding comprises skipping all ME and skipping partially ME.

10. An electronic device for video predictive coding, comprising: a memory, a processor, and

a computer program stored on the memory and runnable on the processor, wherein the processor, when loading and running the program, is caused to perform the method as defined in any one of claims 1 to 8.

11. A computer-readable storage medium, storing a computer program therein, wherein the computer program, when loaded and run by a processor, causes the processor to perform the method as defined in any one of claims 1 to 8.