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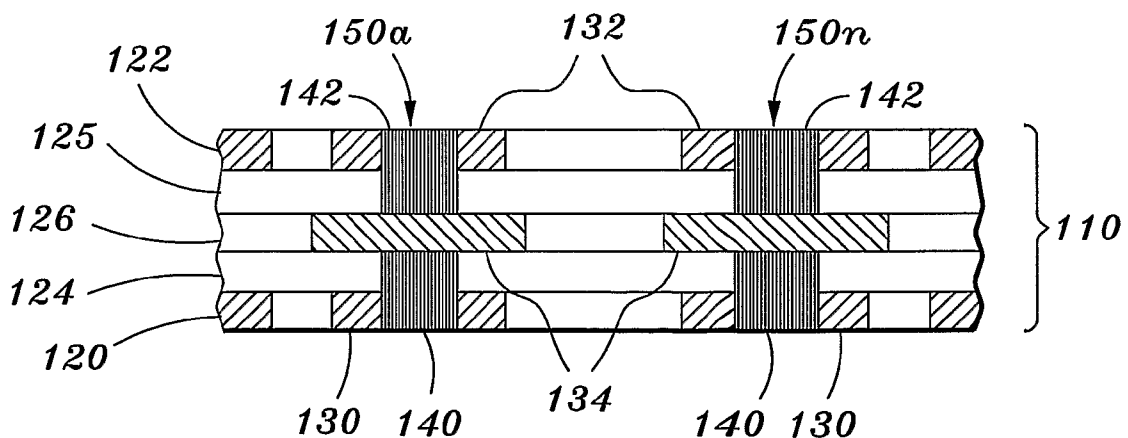
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— as to applicant's entitlement to apply for and be granted
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[Continued on next page]

(54) Title: TELESCOPING BLIND VIA IN THREE-LAYER CORE



(57) Abstract: A multilayer PCB including at least one carrier, wherein the at least one carrier comprises a pseudo three-layer core (110). Each three-layer core (110) includes a first metal layer (120), a first dielectric layer (124), an internal bridge layer (126), a second dielectric layer (125), and a second metal layer (122). The bridge layer includes a plurality of bridge pads (134). Each carrier includes a plurality of interlayer interconnection units (150a, 150n) for interconnecting the first and second metal layers. Each interlayer interconnection unit comprises a pair of opposed blind vias (140, 142) and a bridge pad (134) disposed between, and in electrical contact with, the pair of blind vias.

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