

【Abstract】

An electronic device includes a battery, a power management module, a charging circuit, and a processor operationally connected to the battery, the power management module and the charging circuit and including a central
5 processing unit (CPU) and a plurality of intellectual property (IP) blocks, wherein the charging circuit is connected to the processor and includes a first pin to output a first overcurrent warning signal when a value of current flowing through the electronic device is greater than or equal to a first threshold current, wherein the power management module is connected to the
10 processor and includes a second pin to output a reset warning signal based on a voltage value of the power management module, and wherein the processor includes a first general purpose input output (GPIO) pin to receive the first overcurrent warning signal output from the first pin, and is to reduce at least one clock frequency among a plurality of clock frequencies set to each of the
15 CPU and the plurality of IP blocks, or reduce at least one operation clock frequency of components inside the electronic device when the first overcurrent warning signal is received through the first GPIO pin. In addition, it is possible to implement various embodiment understood through the disclosure.

20 (Fig.2b)