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(54) **Title:** RESPONSE CONTROL IN MEMORY SYSTEMS

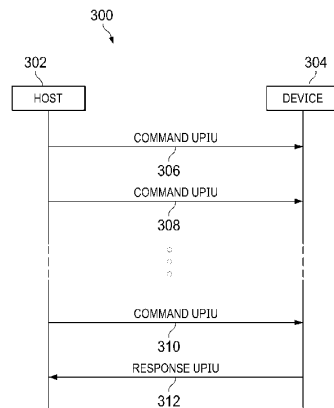


FIG. 3

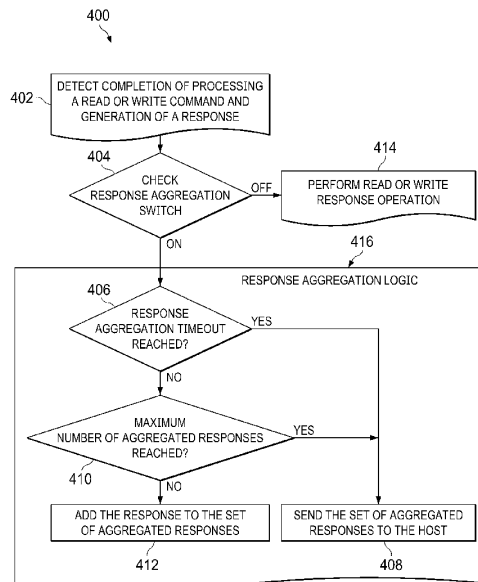


FIG. 4

(57) **Abstract:** Example memory systems, methods, and media for aggregating responses from memory systems to mitigate interrupt overhead in hosts coupled to the memory systems are disclosed. One example method includes receiving multiple commands from a host. Multiple responses are sent to the host at once, where each of the responses is generated in a memory system during a time period and responsive to a respective one of the commands.

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