

SEMICONDUCTOR MEMORY AND CAPACITY CONFIGURION METHOD FOR STORAGE BLOCK THEREON

ABSTRACT

Embodiments of the present application provide a semiconductor memory and a capacity configuration method for a storage block thereon. The method comprises: determining a target storage block to be configured on a memory; determining a capacity configuration parameter of the target storage block, and determining a target code from a group of codes of the target storage block, wherein the target code corresponds to a storage region to be trimmed in the target storage block; on the basis of the target code, generating a region selection signal used for selecting the storage region to be trimmed in the target storage block; and on the basis of the region selection signal, trimming the storage region to be trimmed, so as to configure the capacity of the target storage block. The present application can flexibly select the storage region to be trimmed, thereby improving the effective utilization rate of the capacity configuration for the storage block on the semiconductor memory.

[Fig. 2]