

Abstract

LOW DROP-OUT VOLTAGE REGULATOR

5 The voltage regulator comprises a regulation loop (2), which
comprises at least a pass transistor (18), a source transistor (28), a
sensing transistor (22) and a retention transistor (24), and a stability
compensation circuit (10), which comprises a first MOS resistor (12)
and a second MOS resistor (14) coupled with the first MOS resistor
10 (12). The gate of the second MOS resistor (14) is coupled to the gate of
the pass transistor (18).

Figure 1