

ABSTRACT

VIA/LINE INDUCTOR ON SEMICONDUCTOR MATERIAL

A spiral inductor [122], and manufacturing method therefore, is provided including a substrate [112] and an inductor dielectric layer [114] over the substrate [112] having a spiral opening provided therein. A spiral inductor [122] is in the spiral opening with the spiral
5 inductor [122] including a plurality of parallel spiral vias [144] [148] connected together at center proximate and center distal ends of the spiral inductor [122].

Figure 3



162162



G00002