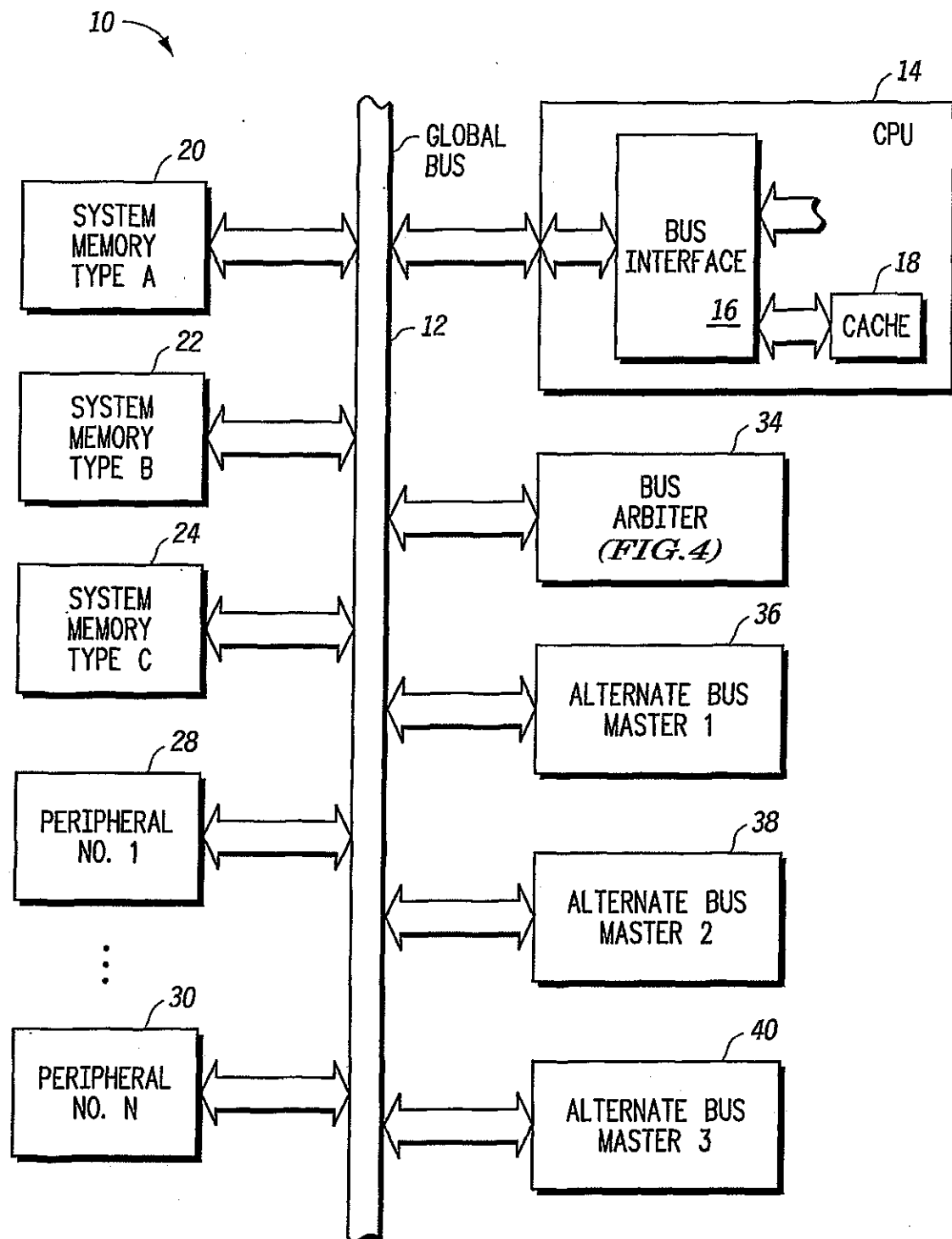


1/4

**FIG. 1**

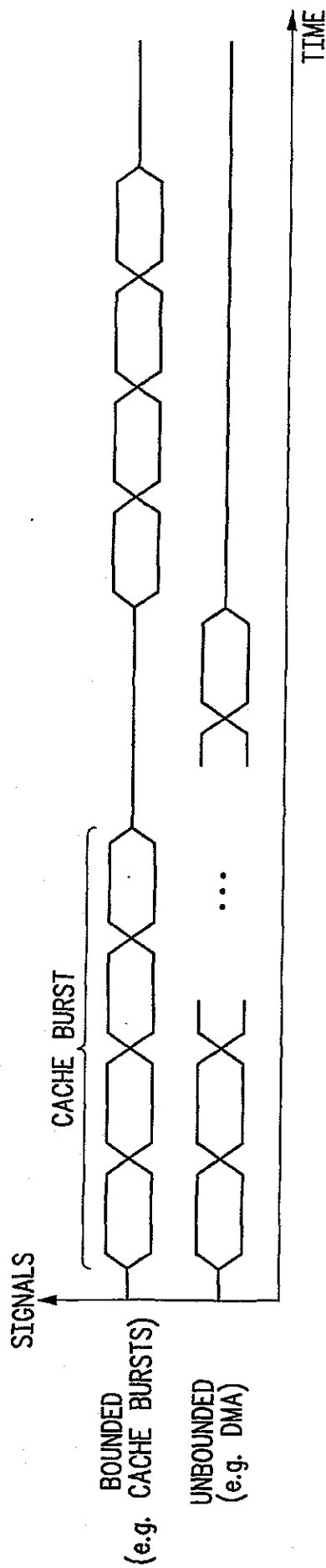


FIG. 2

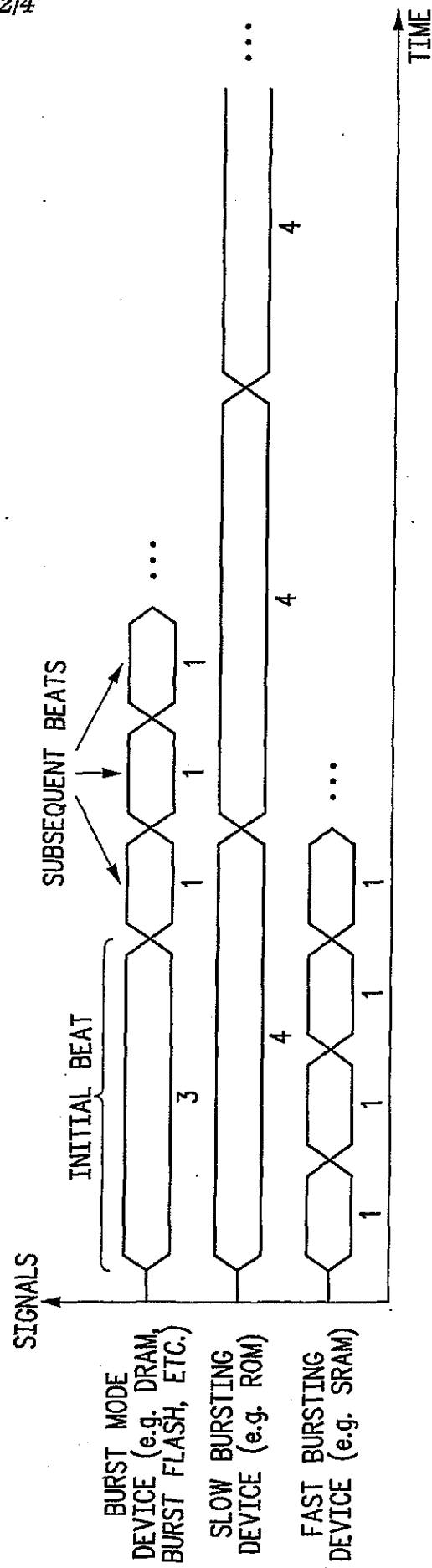


FIG. 3

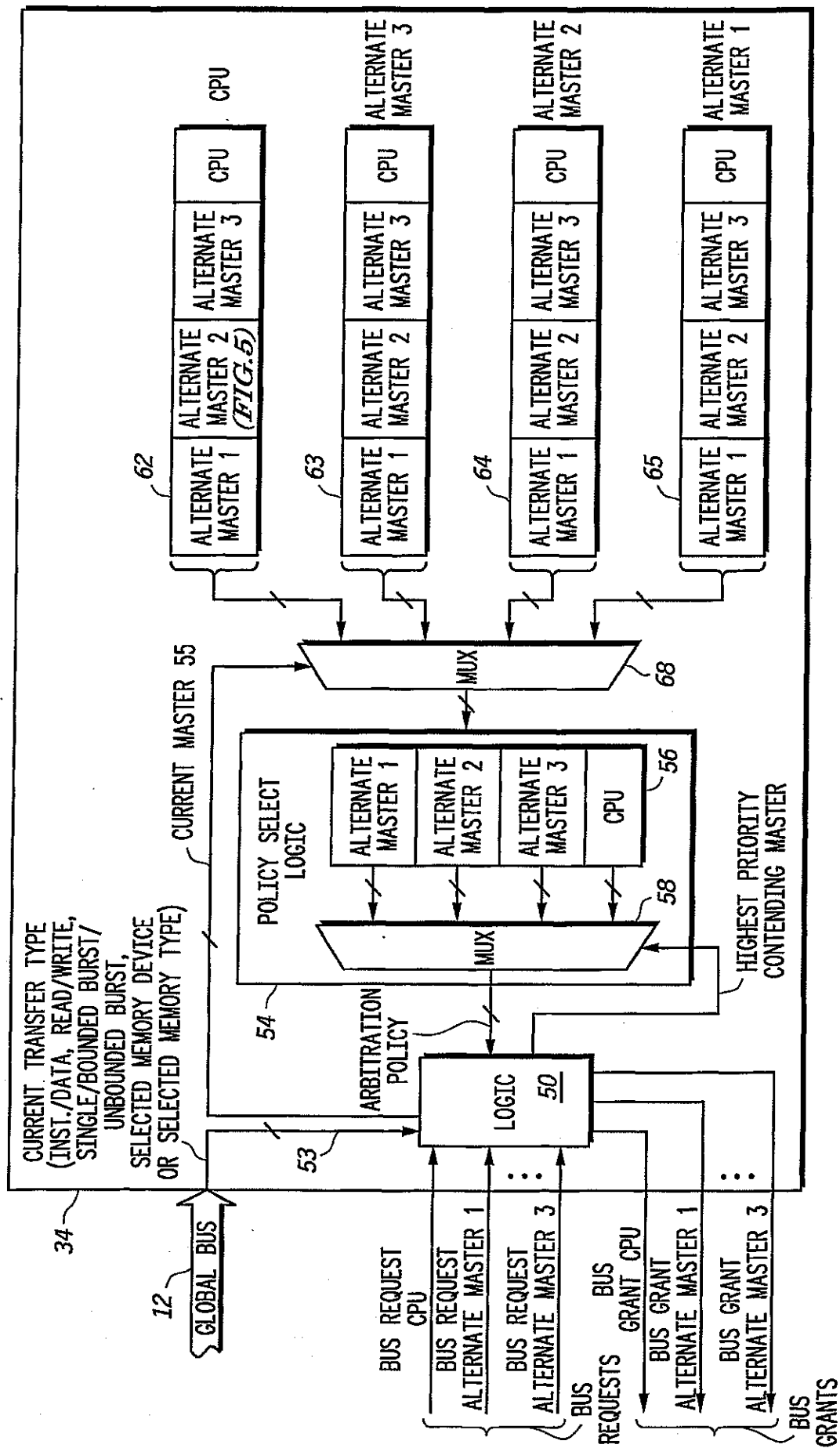


FIG. 4

4/4

CONTROL FIELD ENCODING	
0 0 0	NO BUS OWNERSHIP CHANGE ALLOWED DURING CACHE BURST CYCLES
0 0 1	BUS OWNERSHIP CHANGE ALLOWED ONLY DURING INSTRUCTION CACHE BURST CYCLES OR UNBOUNDED BURST CYCLES
0 1 0	BUS OWNERSHIP CHANGE ALLOWED ONLY DURING DATA CACHE BURST READ CYCLES OR UNBOUNDED BURST CYCLES
0 1 1	BUS OWNERSHIP CHANGE ALLOWED ONLY DURING CACHE BURST READ CYCLES OR UNBOUNDED BURST CYCLES
1 0 0	BUS OWNERSHIP CHANGE ALLOWED ONLY DURING CACHE BURST WRITE CYCLES OR UNBOUNDED BURST CYCLES
1 0 1	BUS OWNERSHIP CHANGE ALLOWED ONLY DURING DATA CACHE BURST WRITE CYCLES OR INSTRUCTION CACHE BURST CYCLES OR UNBOUNDED BURST CYCLES
1 1 0	BUS OWNERSHIP CHANGE ALLOWED ONLY DURING DATA CACHE BURST CYCLES OR UNBOUNDED BURST CYCLES
1 1 1	TRANSFER OF BUS OWNERSHIP ALLOWED DURING ANY BURST CYCLES

FIG.5

CONTROL FIELD ENCODING	
0 0	NO BUS OWNERSHIP CHANGE ALLOWED DURING BURST CYCLES
0 1	NO BUS OWNERSHIP CHANGE ALLOWED DURING ACCESSES TO A BURST MODE DEVICE
1 0	NO BUS OWNERSHIP CHANGE ALLOWED DURING ACCESSES TO A FAST MEMORY DEVICE
1 1	TRANSFER OF BUS OWNERSHIP ALLOWED DURING ANY BURST CYCLES

FIG.6