Fig.1

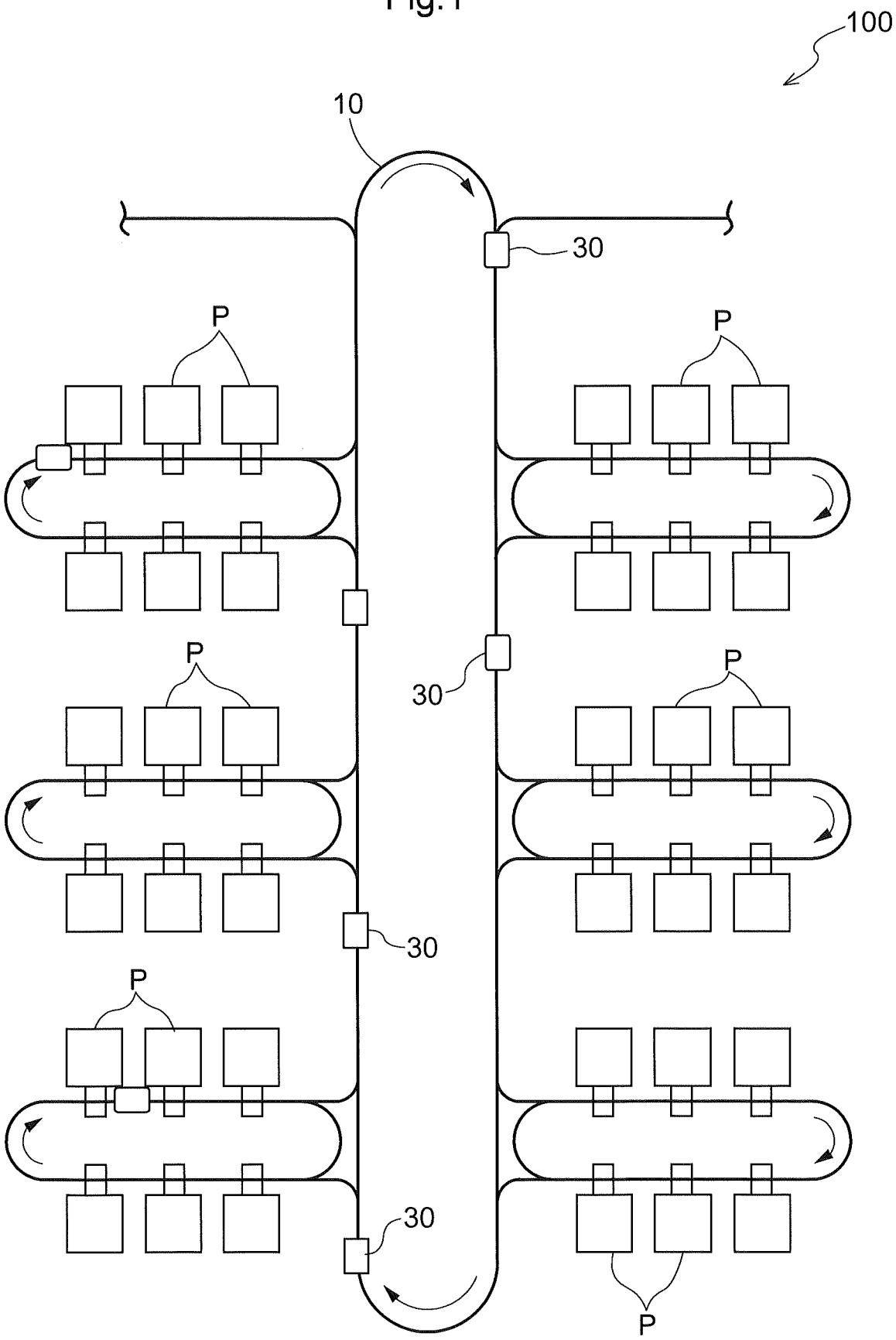


Fig.2

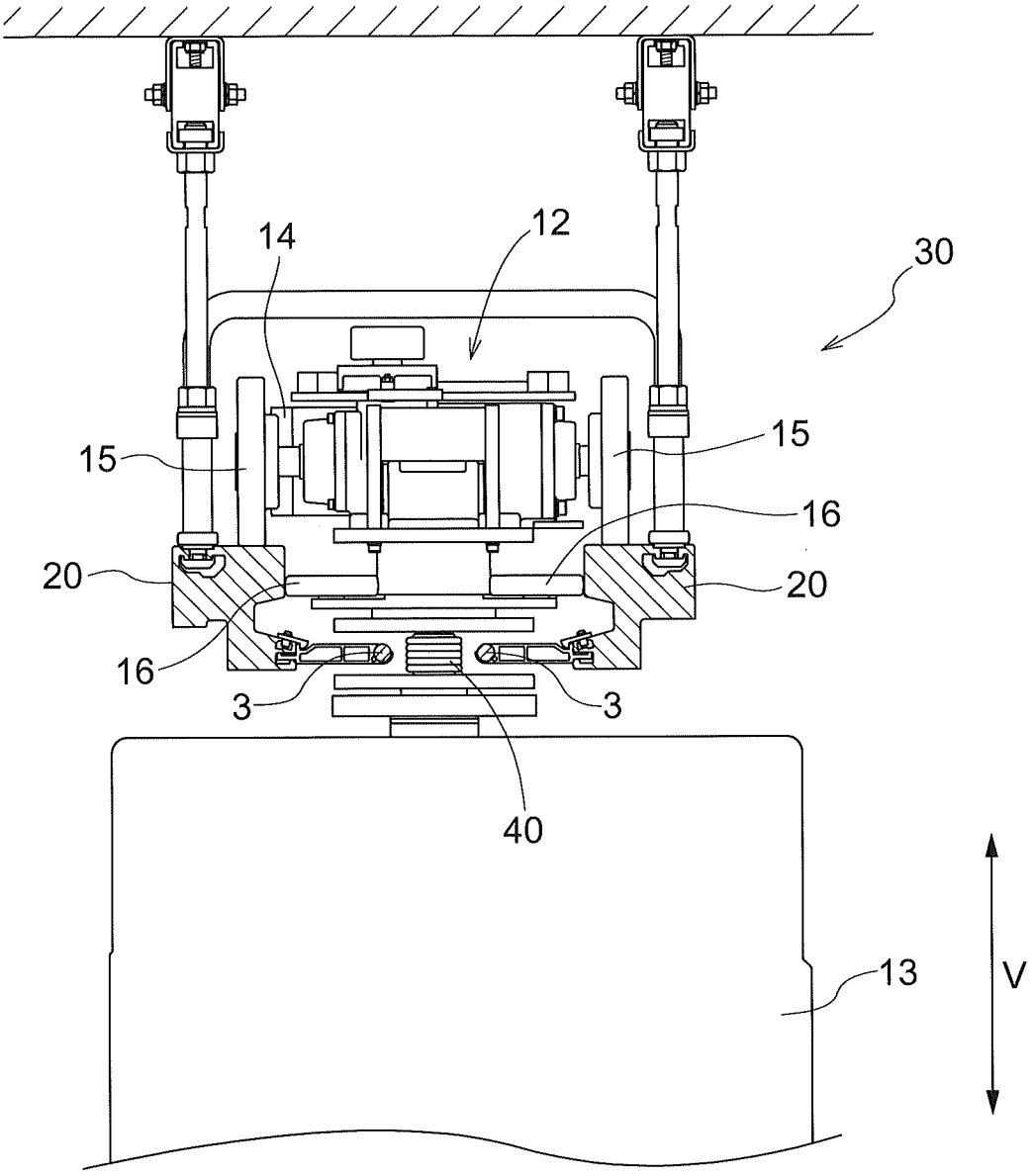


Fig.3

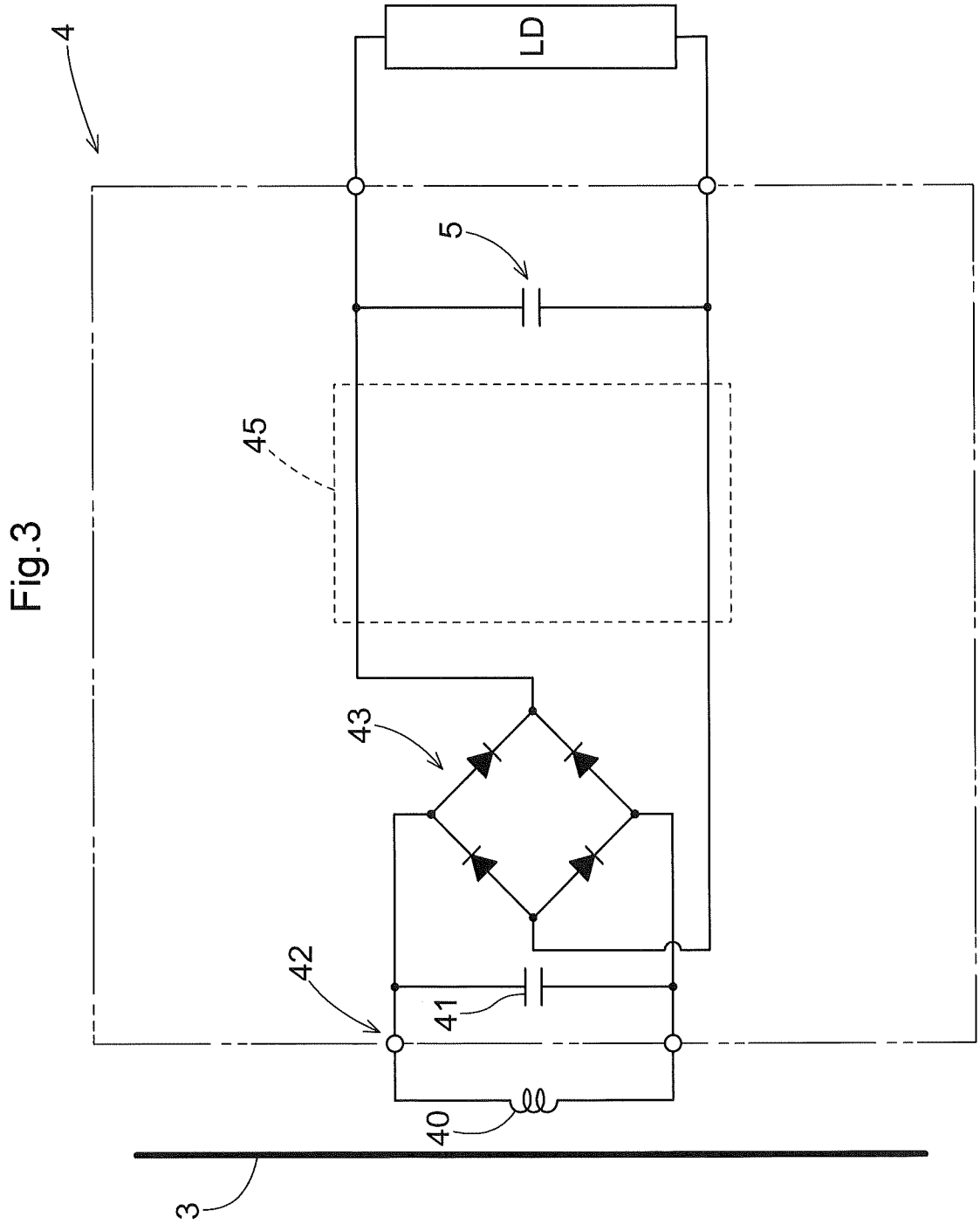


Fig.4

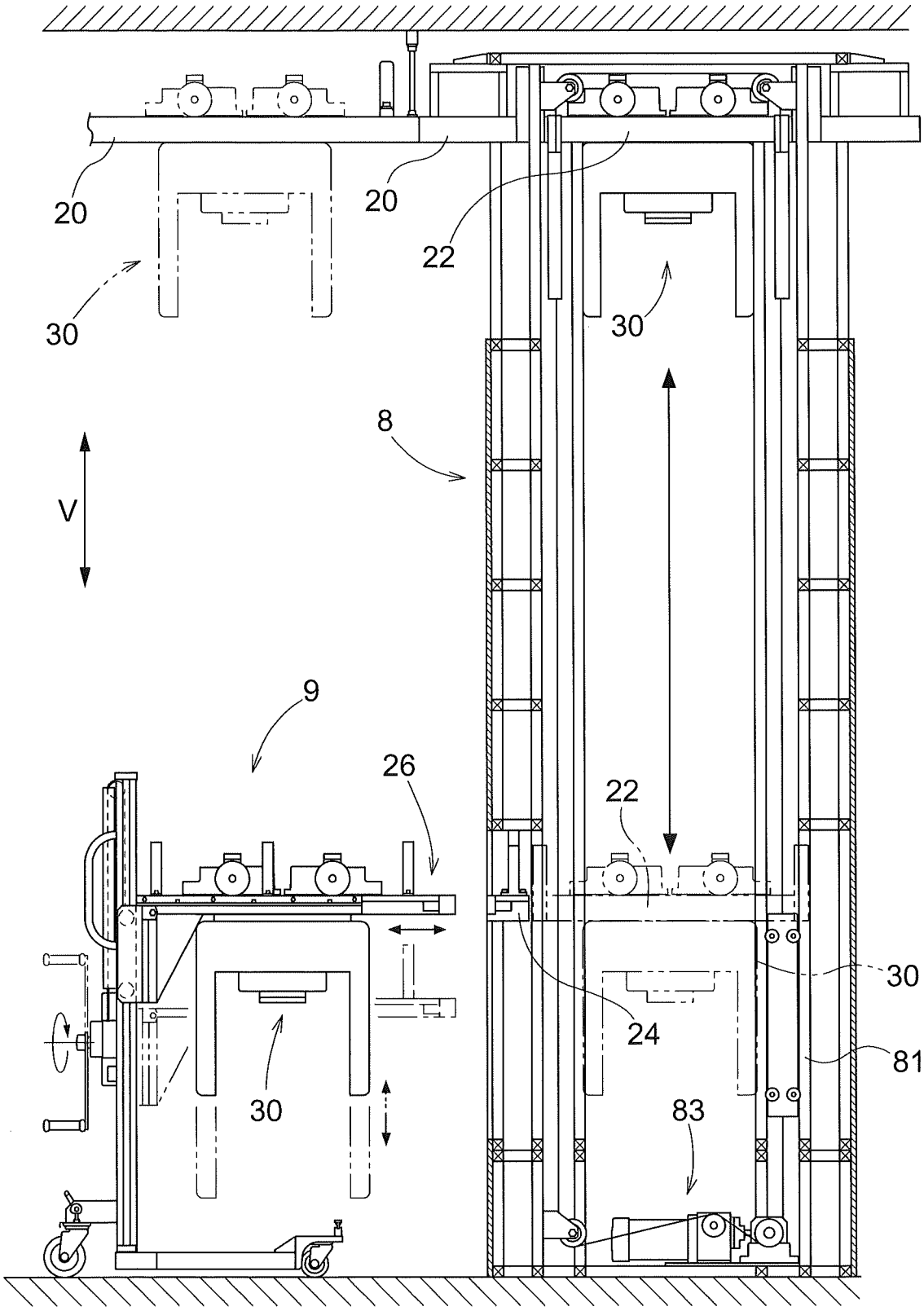


Fig.5

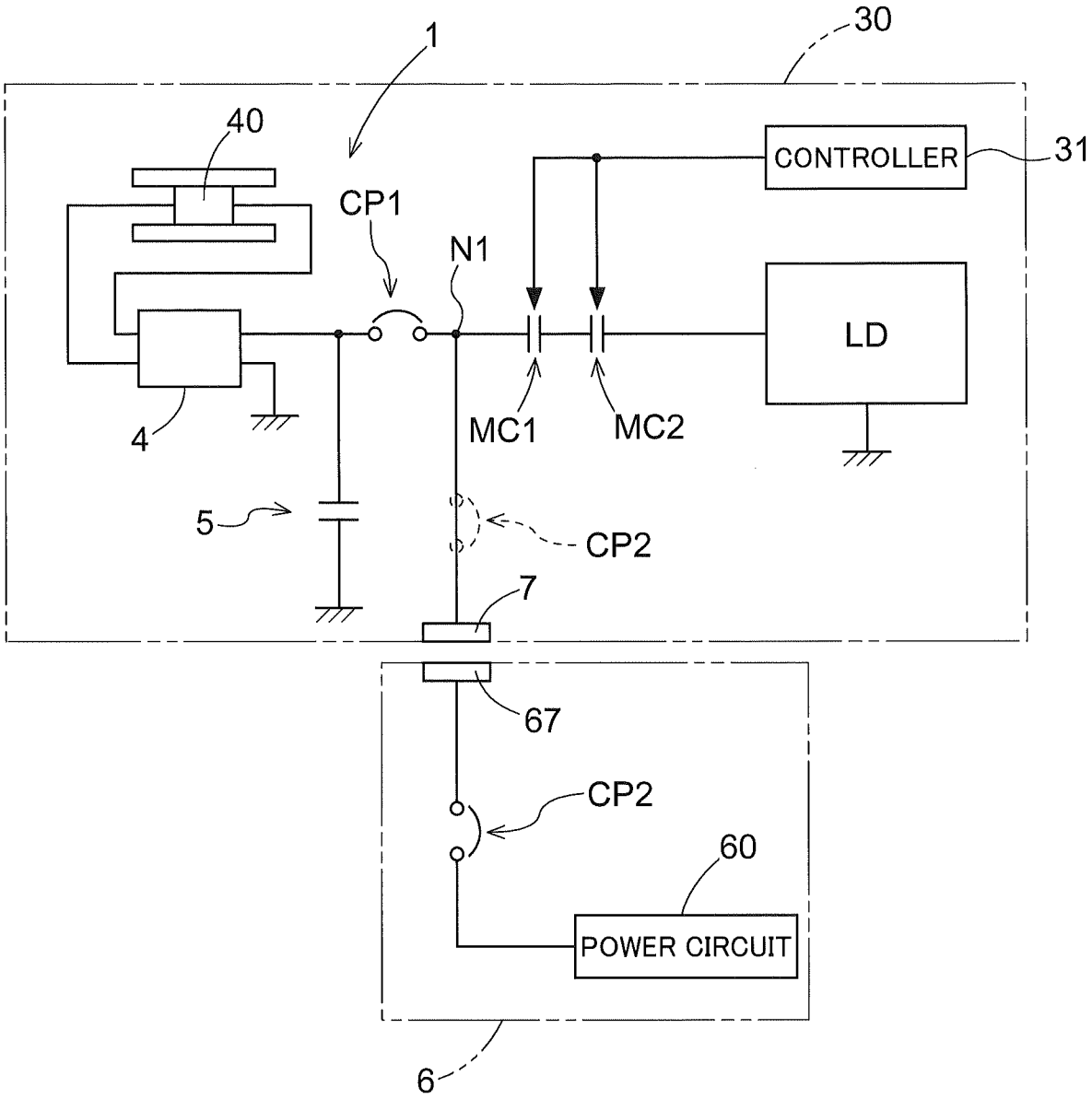
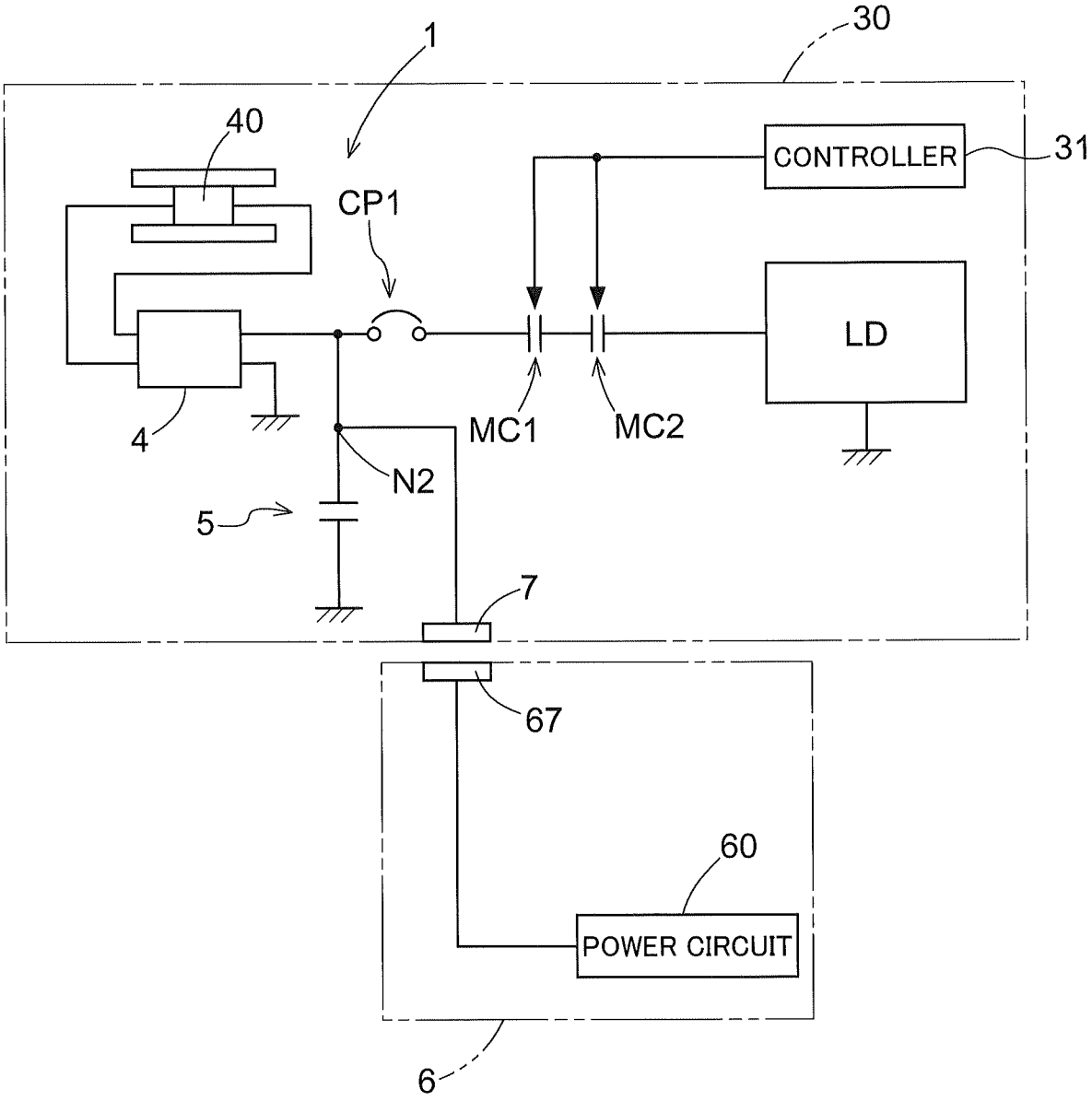

30
1
40
CONTROLLER
31
CP1
N1
LD
MC1
MC2
4
5
CP2
7
67
CP2
60
POWER CIRCUIT
6

Fig.6


30
1
40
CONTROLLER
31
CP1
LD
MC1
MC2
4
N2
5
7
67
60
POWER CIRCUIT
6