WE CLAIM:

1. A driver system, comprising:

a counter unit having a first input terminal and a plurality of first output terminals, wherein the counter unit is adapted to receive a driving signal via the first input terminal and sequentially activate the first output terminals following a predetermined counting sequence, thereby outputting a control signal;

a buffer unit electrically connected to the counter unit, comprising a plurality of second input terminals connected to the first output terminals, respectively, and a plurality of second output terminals adapted to output an isolated control signal; and

a voltage regulation unit, comprising a plurality of resistors connected to the second output terminals, respectively, so that a corresponding control voltage for driving an electrical device is output from the resistor receiving the isolated control signal.

2. The driver system according to Claim 1, further comprising a driver unit for driving a light source module, the driver unit comprising:

a rectifier circuit for connecting an AC power source and converting the AC power source into a DC power source; and

a switching power supply having a primary side and a secondary side, wherein the primary side is connected with the rectifier circuit and the voltage regulation unit, and wherein the secondary side is adapted to supply a predetermined voltage to drive the light source module.

3. The driver system according to Claim 1, further comprising a driver unit for driving a light source module, the driver unit comprising:

a rectifier circuit for connecting an AC power source and converting the AC power source into a DC power source;

a voltage stabilization circuit comprising a resistor, a Zener diode connected in series with the resistor, and a first operational amplifier, wherein the serially connected resistor and Zener diode are electrically connected to the rectifier circuit to receive the DC power source, thereby generating a constant voltage, and the first operational amplifier is adapted to output a predetermined voltage upon receiving the constant voltage and the control voltage; and

a constant current circuit comprising a transistor and a resistor connected in series with the transistor, wherein the transistor is adapted to drive the light source module upon receiving the predetermined voltage.

4. The driver system according to Claim 1, further comprising a driver unit for driving a light source module, the driver unit comprising:

a rectifier circuit for connecting an AC power source and converting the AC power source into a DC power source;

a voltage stabilization circuit comprising a resistor, a Zener diode connected in series with the resistor, and a first operational amplifier, wherein the serially connected resistor and Zener diode are electrically connected to the rectifier circuit to receive the DC power source, thereby generating a constant voltage, and the first operational amplifier is adapted to output a reference voltage upon receiving the constant voltage and the control voltage;

a voltage regulation circuit comprising a first resistor and a second resistor connected in series, and a second operational amplifier, wherein the serially connected first and second resistors are adapted to receive and divide the DC power source into a divided voltage, and the second operational amplifier is adapted to output a predetermined voltage upon receiving the reference voltage and the divided voltage; and

a constant current circuit comprising a transistor and a resistor connected in series with the transistor, wherein the transistor is adapted to drive the light source module upon receiving the predetermined voltage.

5. The driver system according to Claim 4, wherein the second operational amplifier comprise a differential amplifier.

6. The driver system according to Claim 5, wherein the first operational amplifier comprises a differential amplifier.

7. The driver system according to Claim 6, wherein the counter unit comprises a Johnson counter.

8. The driver system according to Claim 7, wherein the buffer unit comprises 3-state buffers.

9. The driver system according to Claim 8, further comprising a wireless receiver unit and a filter unit, wherein the wireless receiver unit is adapted to receive a wireless signal, and wherein the filter unit is adapted to filter the wireless signal based on a specific frequency range, so that the driving signal is generated and transmitted to the counter unit.

10. The driver system according to Claim 9, wherein the resistors in the voltage regulation unit which are coupled to the second output terminals are connected in parallel with one another, and wherein the voltage regulation unit comprises an additional resistor connected in series with the resistors coupled to the second output terminals.

11. The driver system according to any one of Claims 1-8, wherein the counter unit is adapted to receive the driving signal in a wired manner.

12. The driver system according to Claim 11, wherein the counter unit is connected to a switch through a wired connection.