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(54) Title: PRINTED CIRCUIT BOARD CAVITY MODE SUPPRESSION

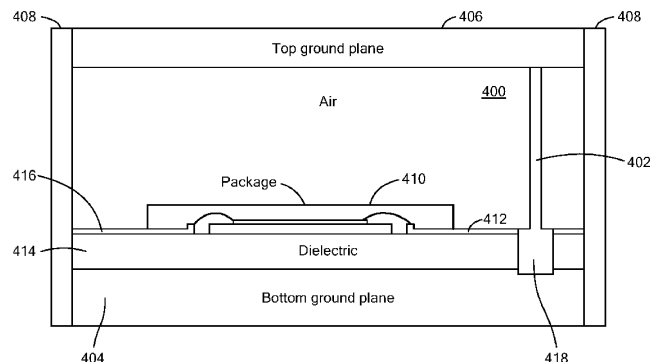


FIG. 4

(57) Abstract: Methods and apparatus for providing a cavity defined by conductive walls, a printed circuit board (PCB) within the cavity, and shorting posts extending into the cavity to suppress higher order modes generated by operation of the PCB.

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