

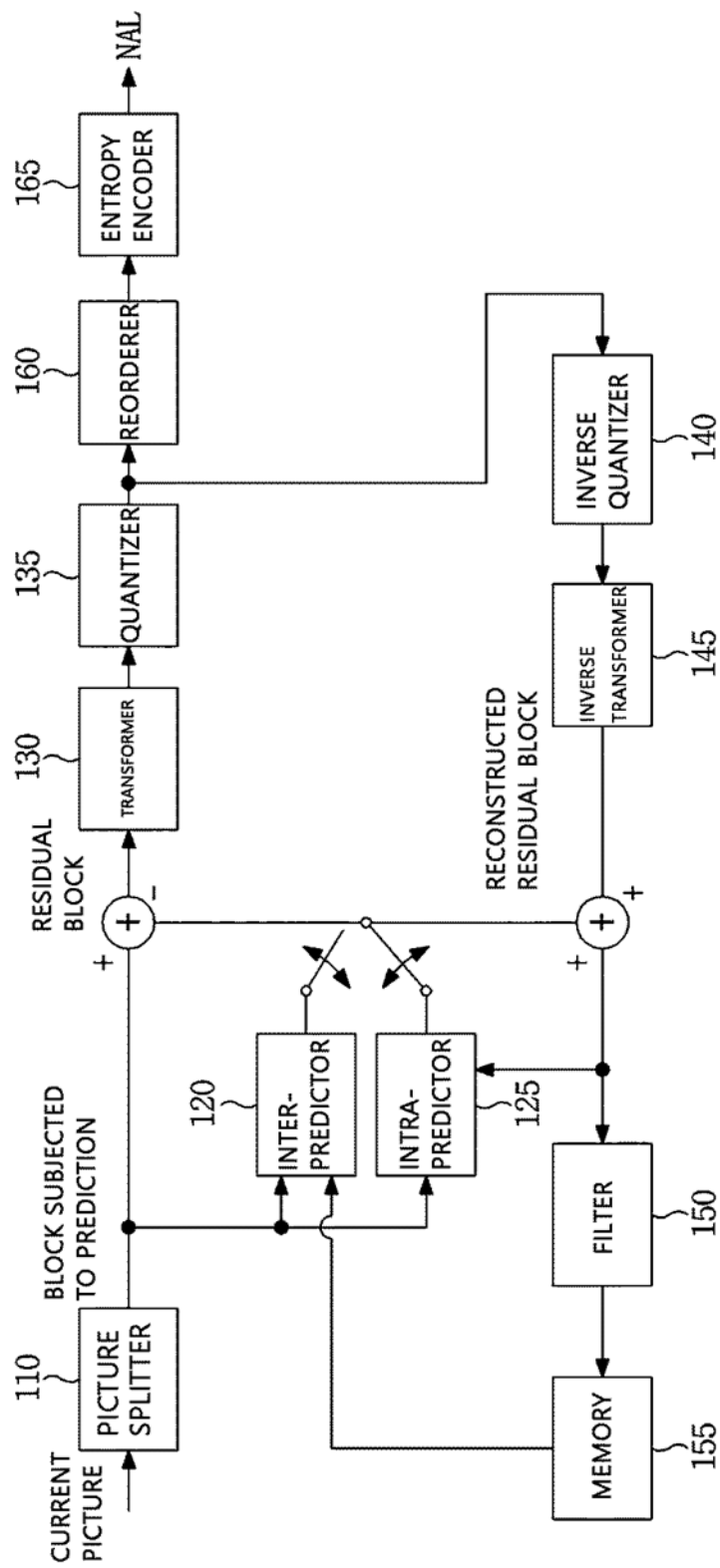


FIG. 1

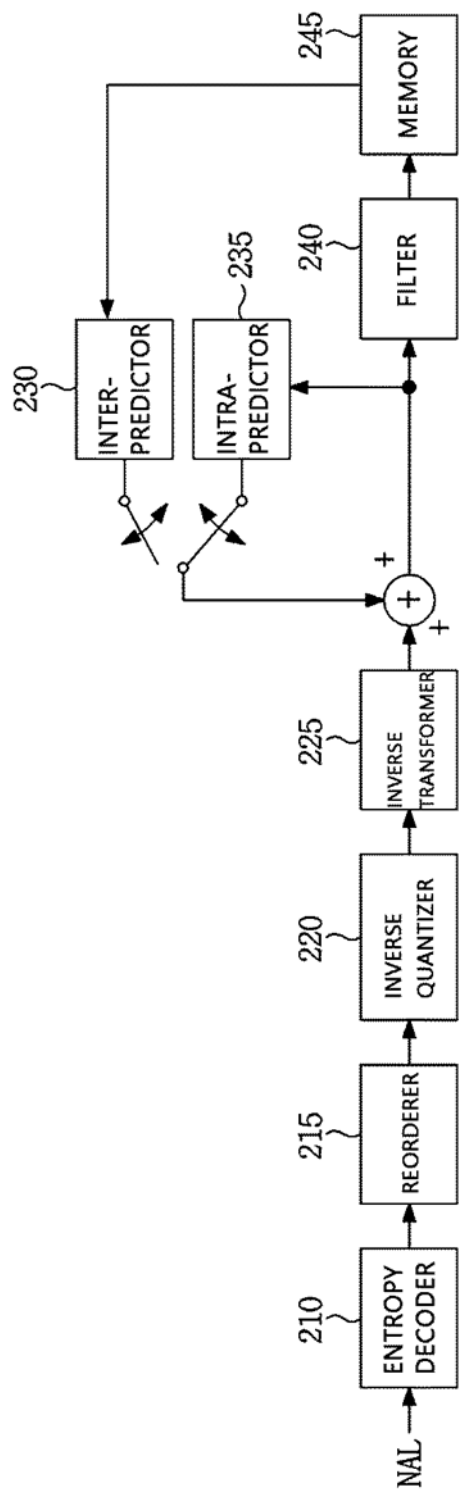


FIG. 2

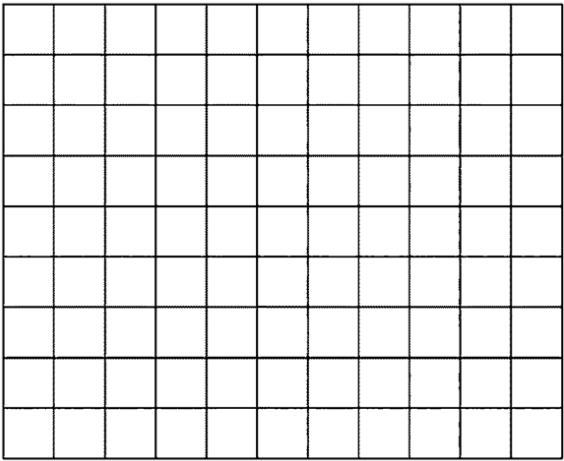
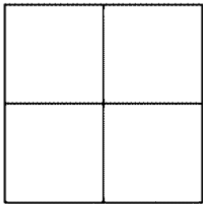
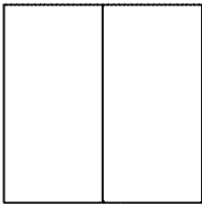


FIG. 3

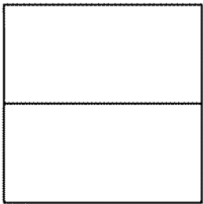
5



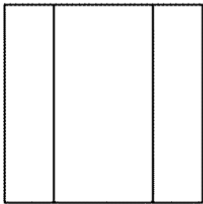
SPLIT_QT



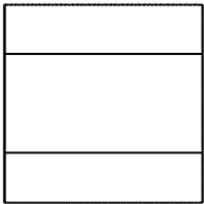
SPLIT_BT_VER



SPLIT_BT_HOR



SPLIT_TT_VER



SPLIT_TT_HOR

FIG. 4

10

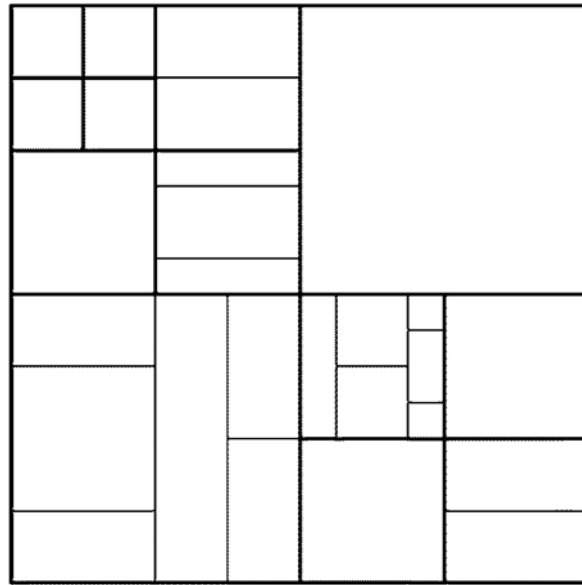


FIG. 5

5

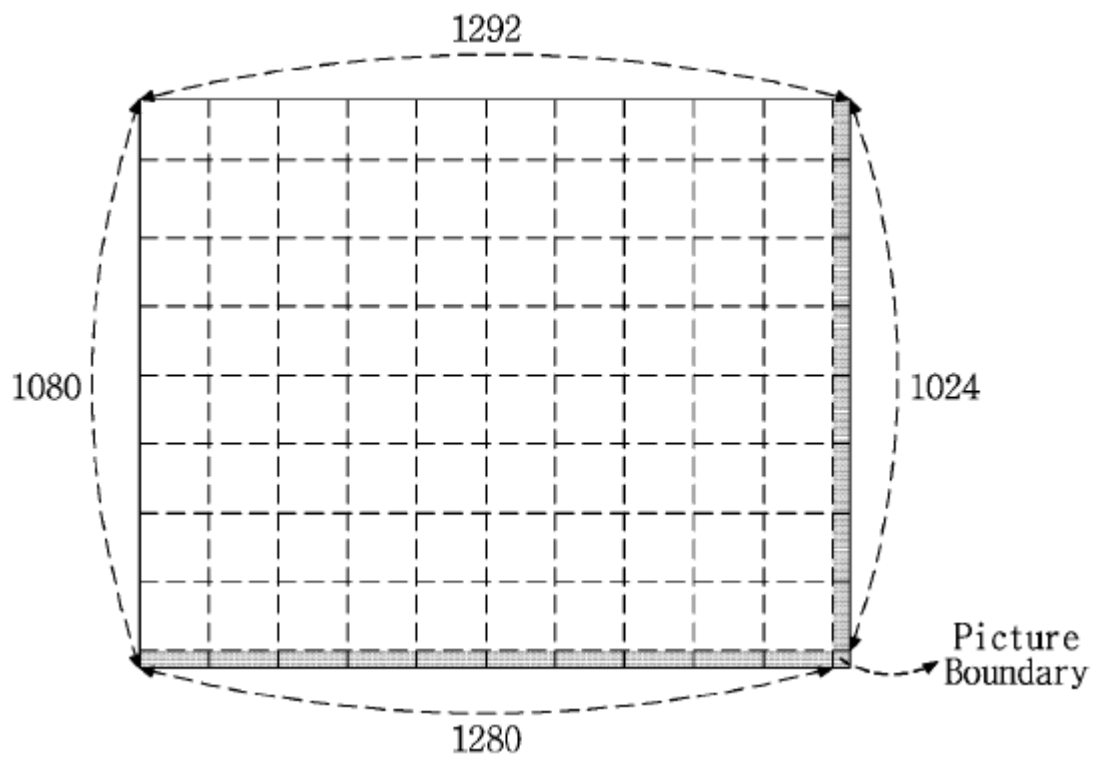


FIG. 6

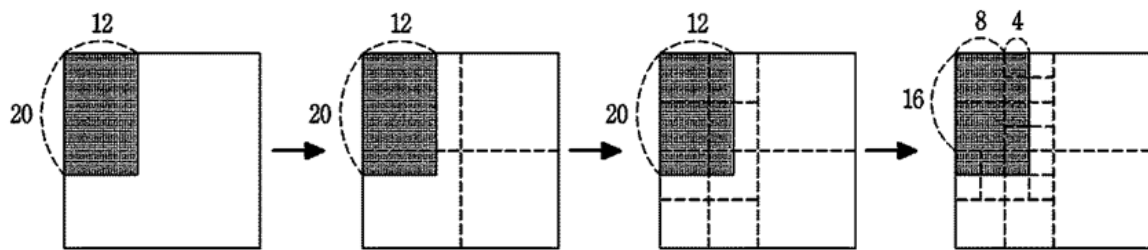


FIG. 7

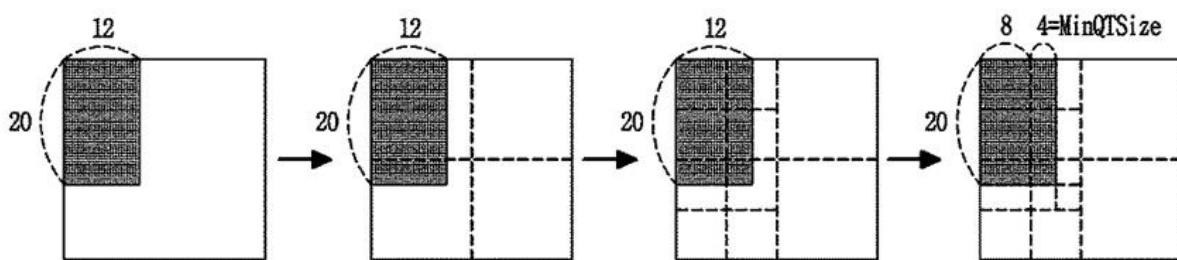


FIG. 8

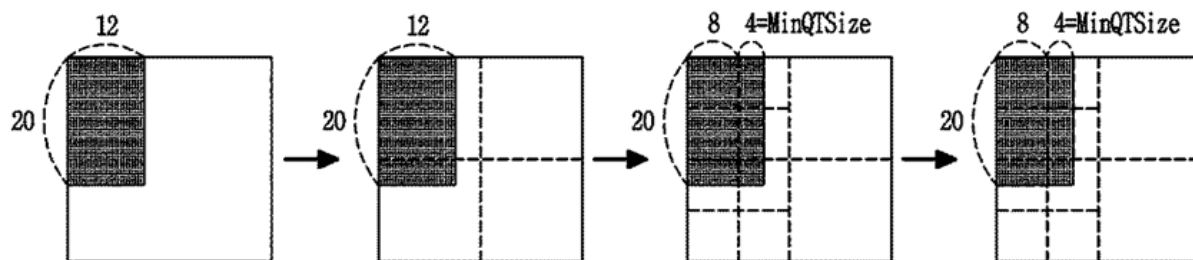


FIG. 9

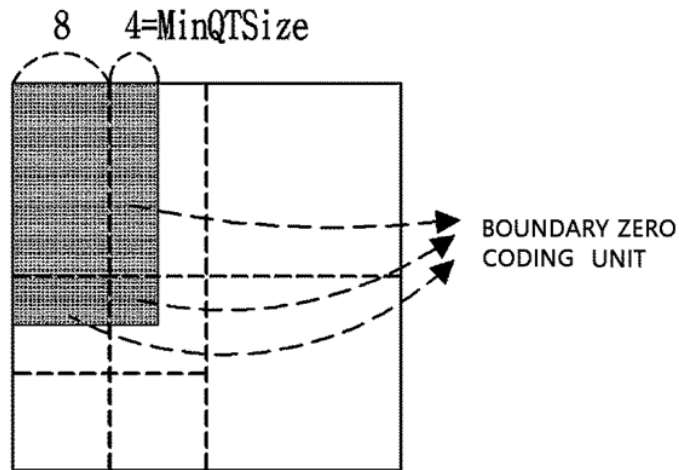


FIG. 10

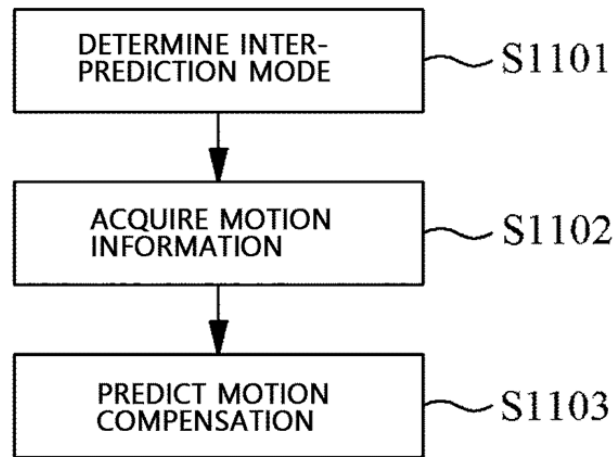


FIG. 11

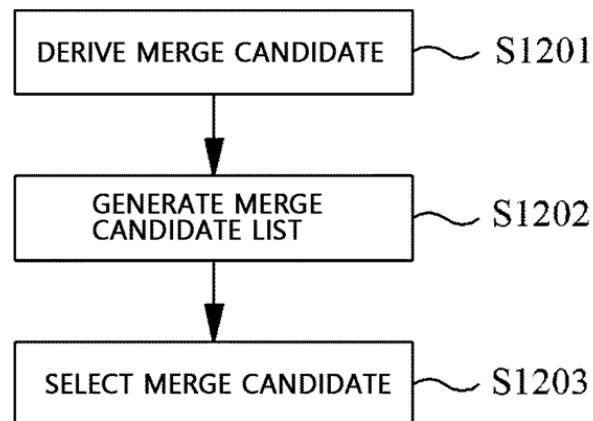


FIG. 12

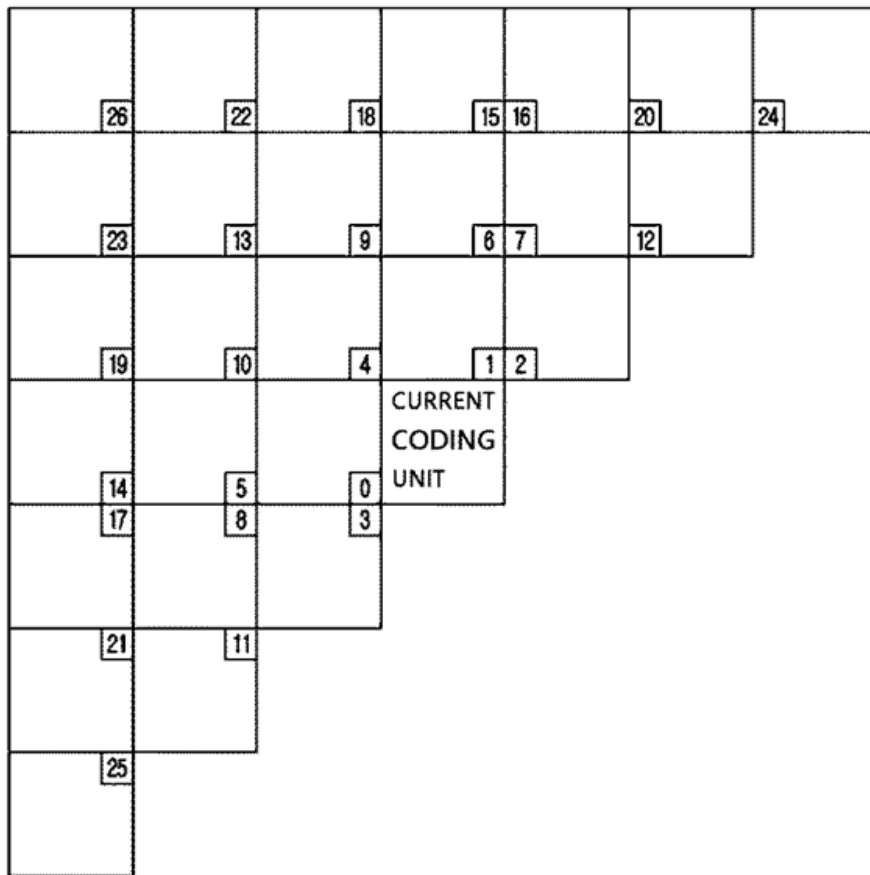


FIG. 13

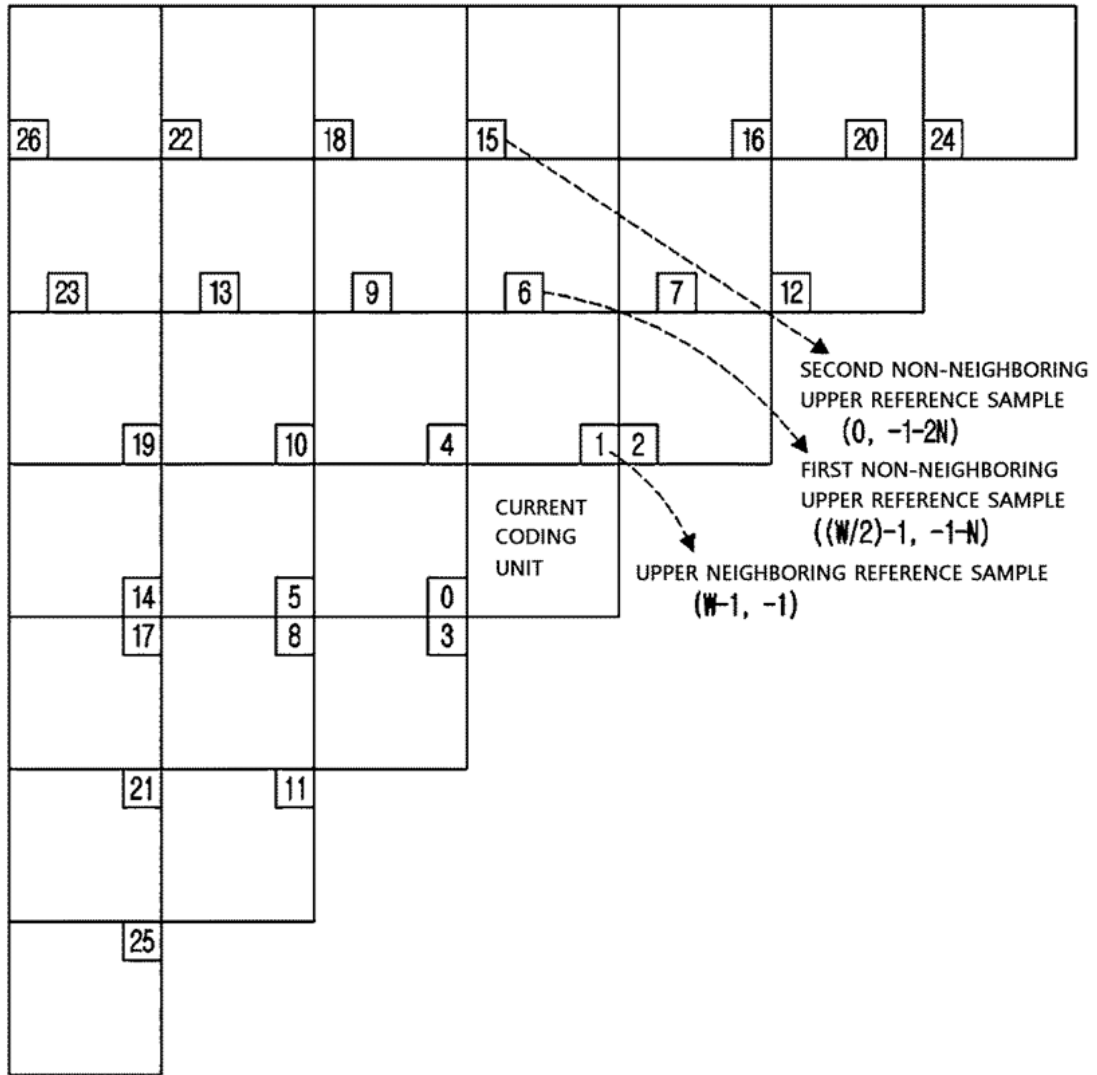


FIG. 14

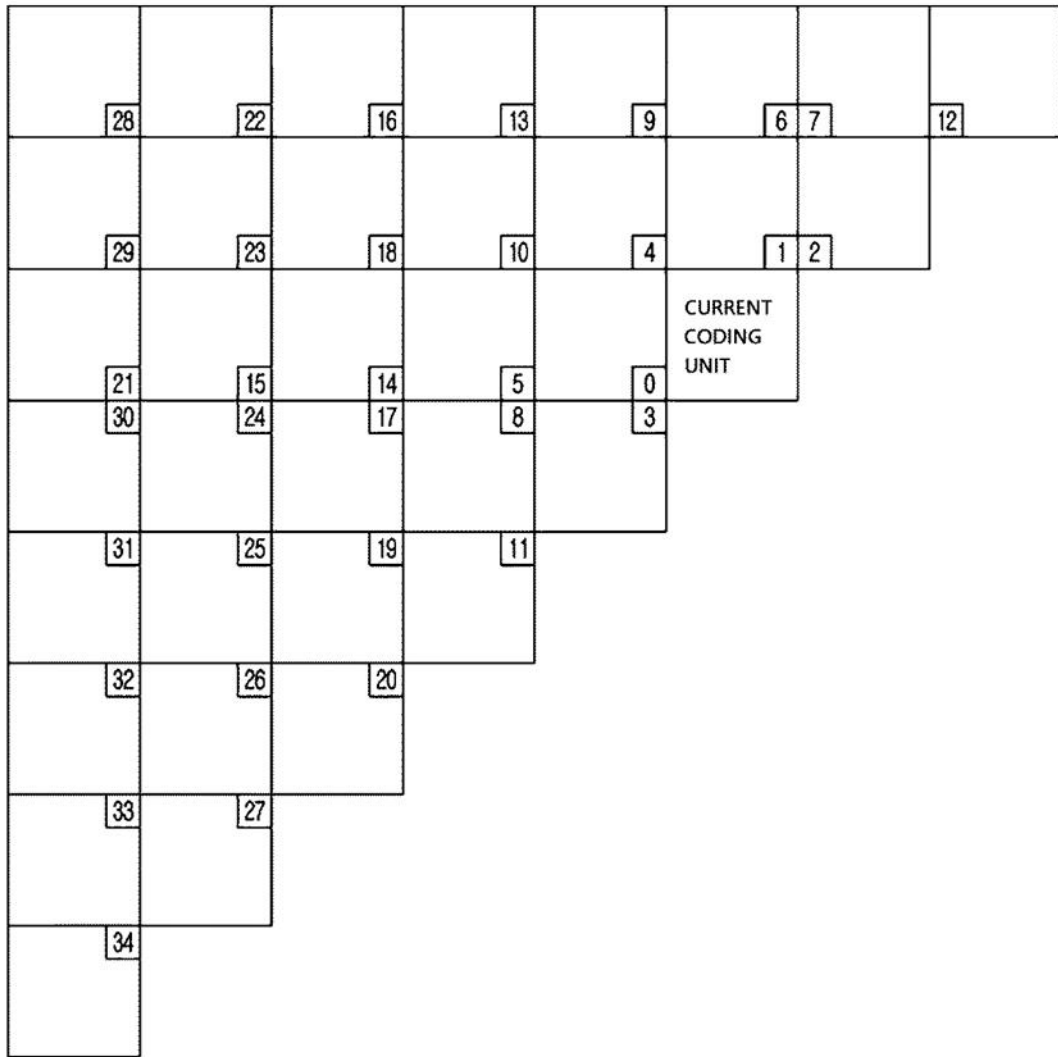


FIG. 15

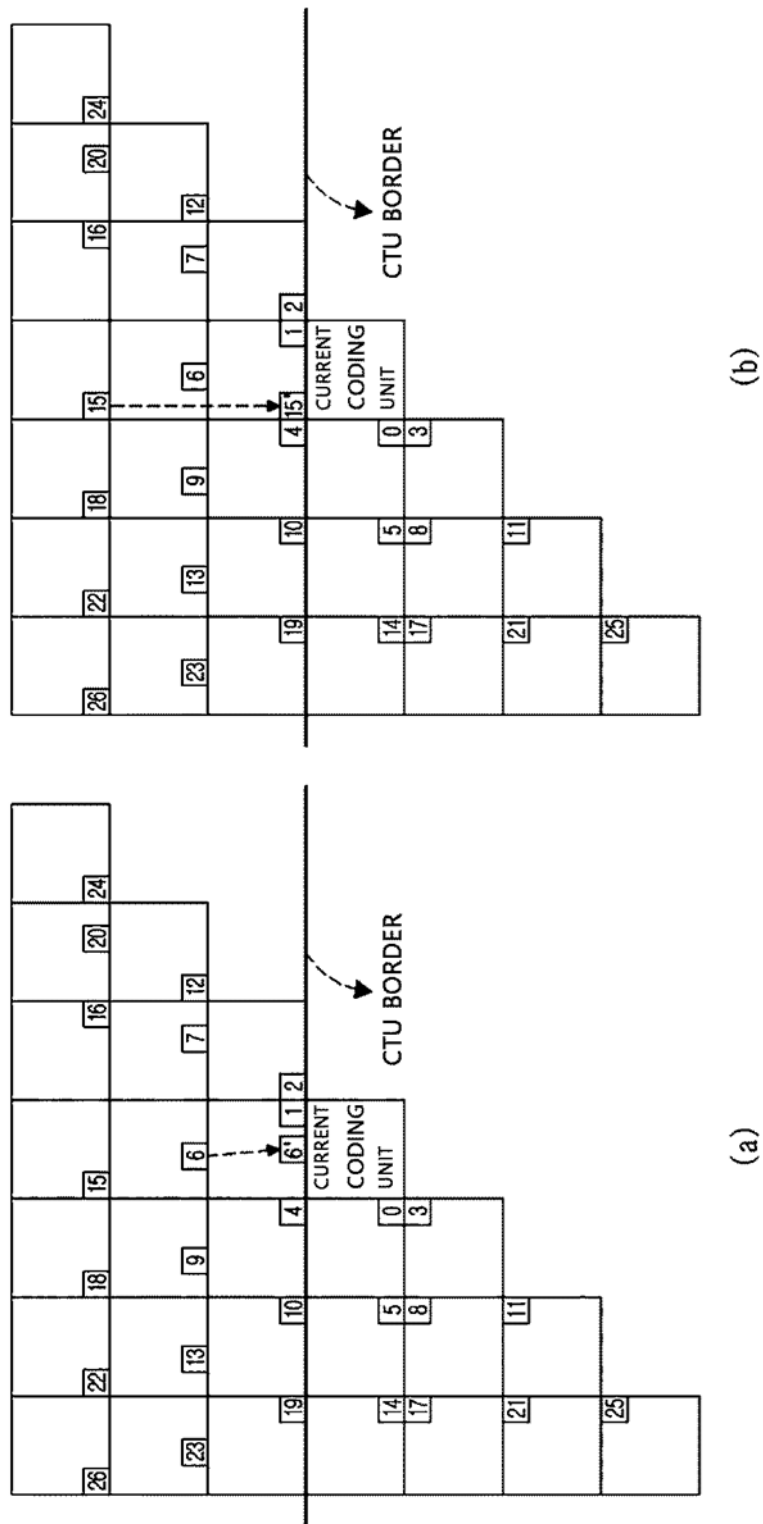


FIG. 16

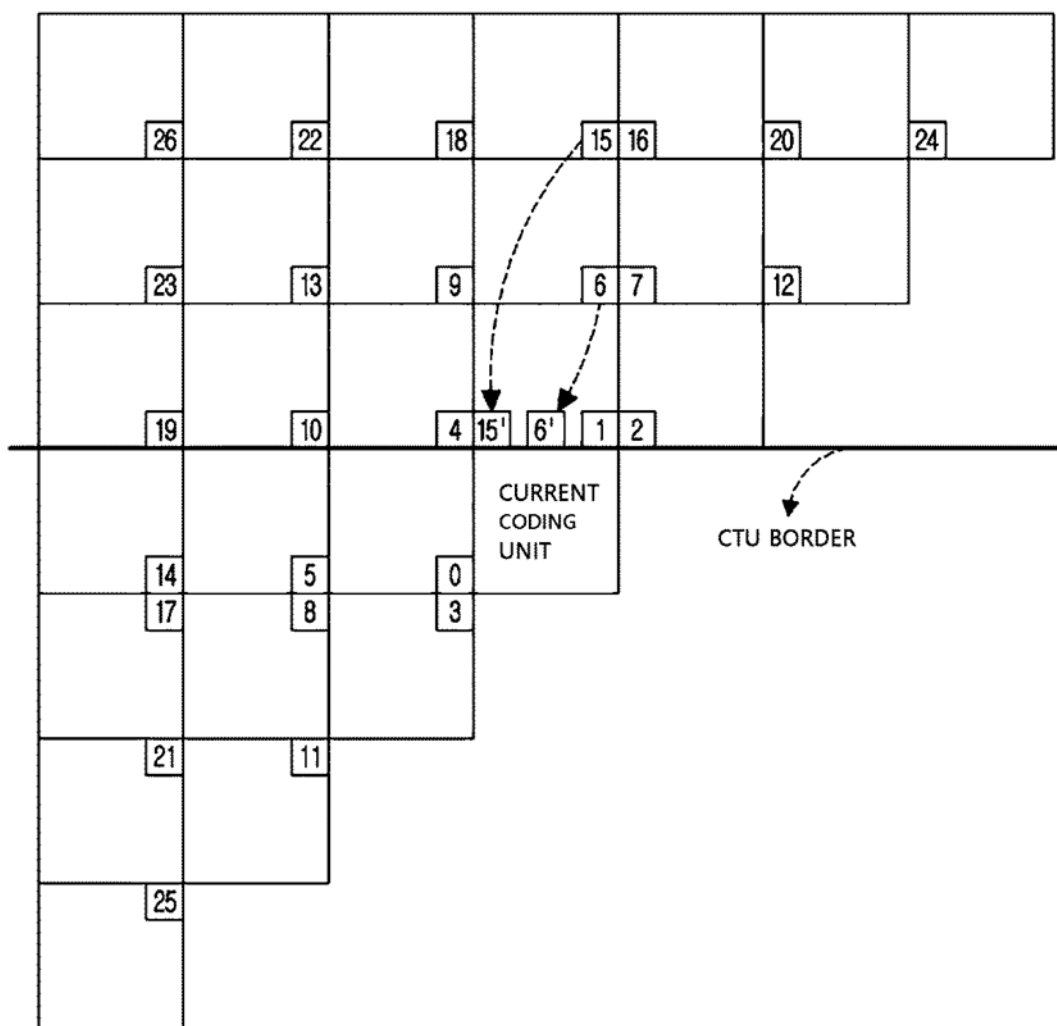


FIG. 17

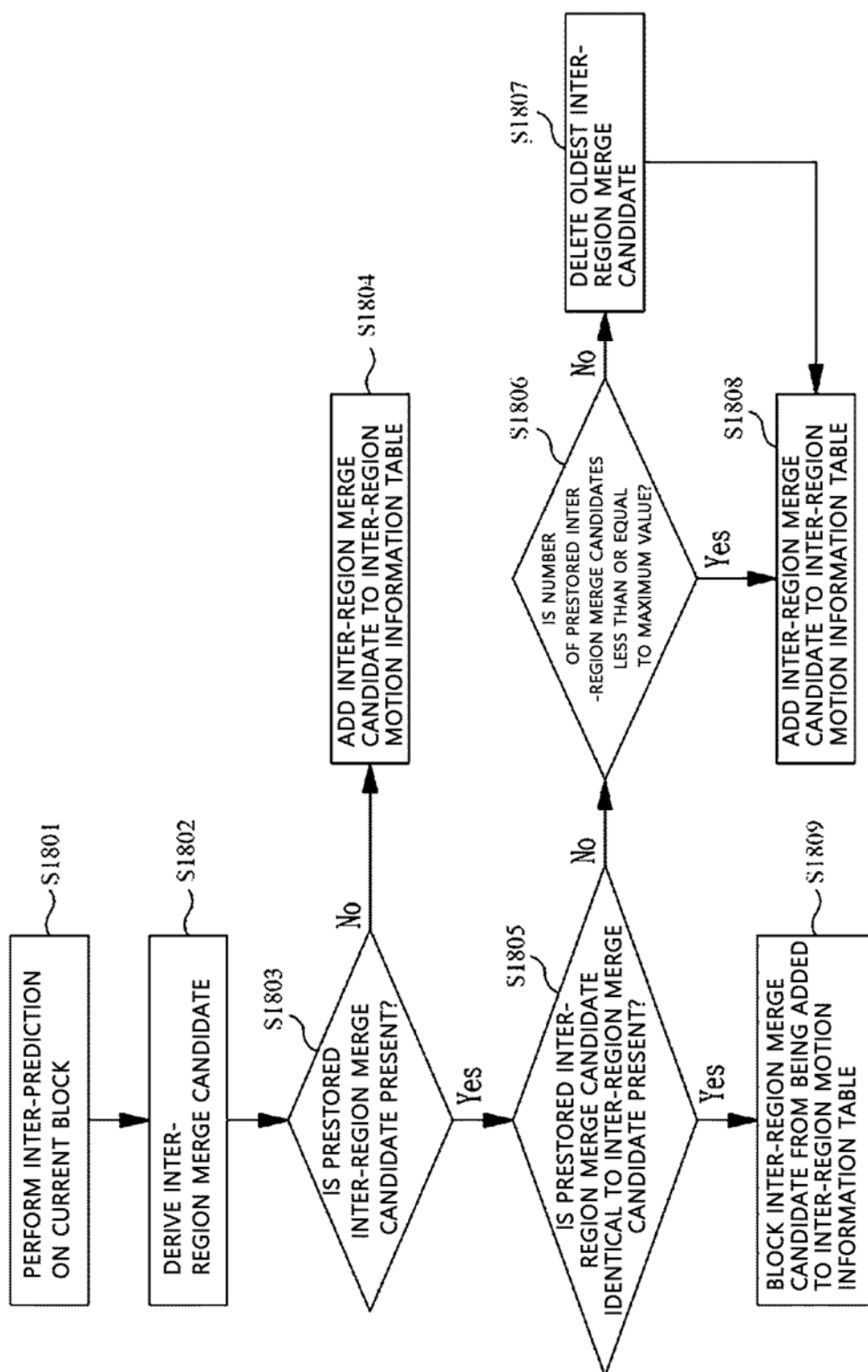


FIG. 18

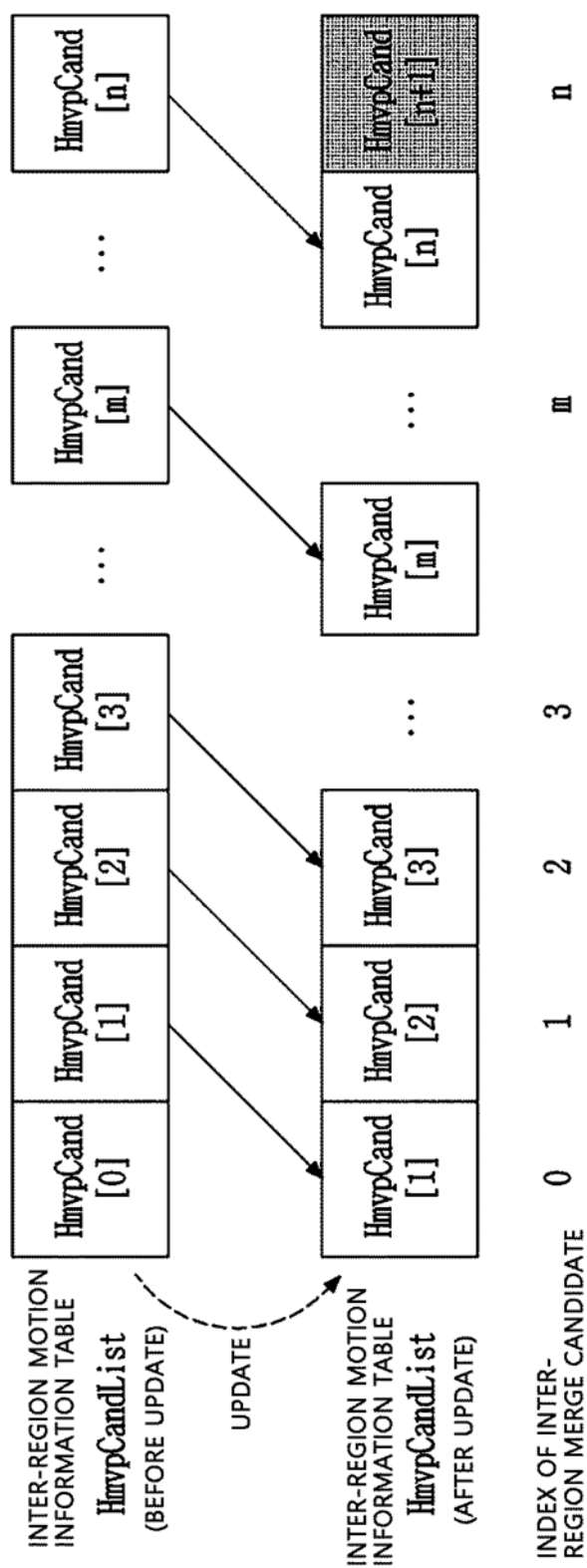


FIG. 19

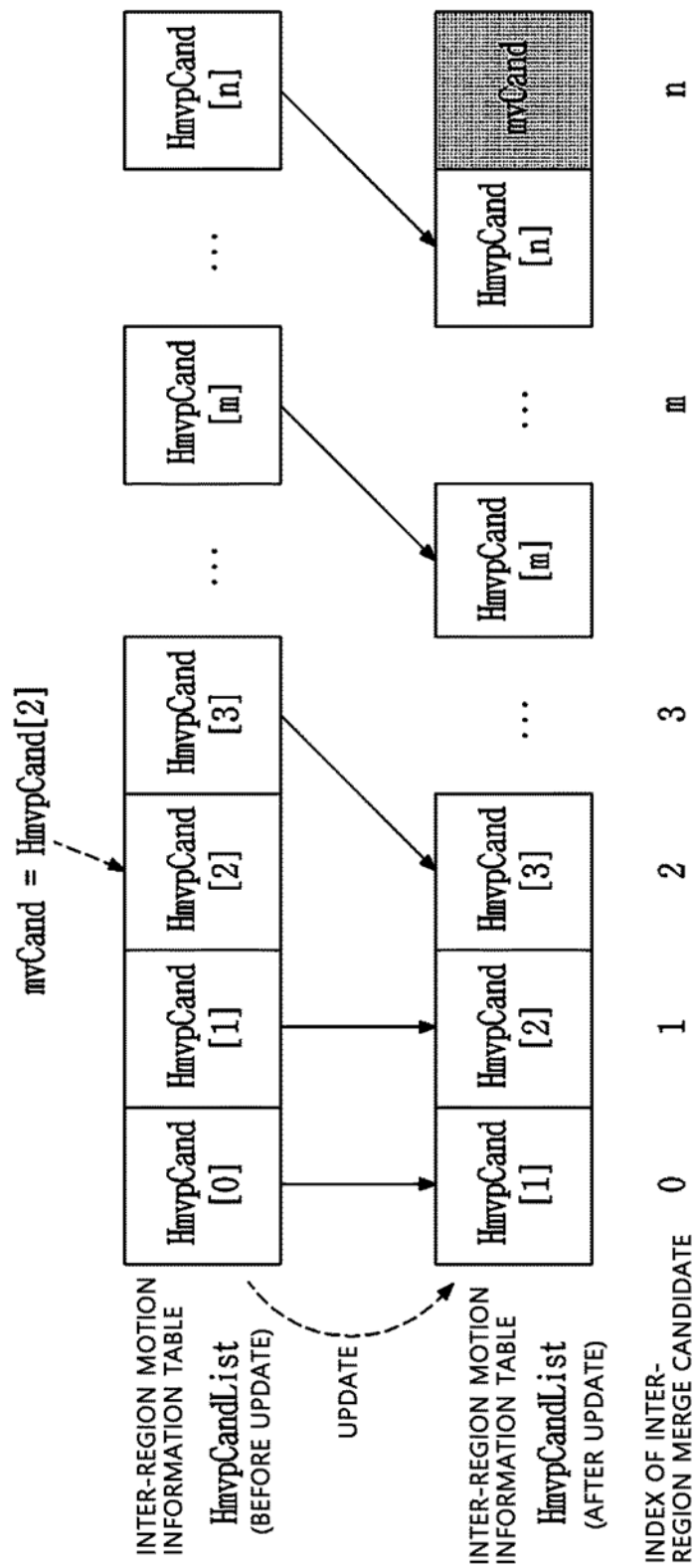
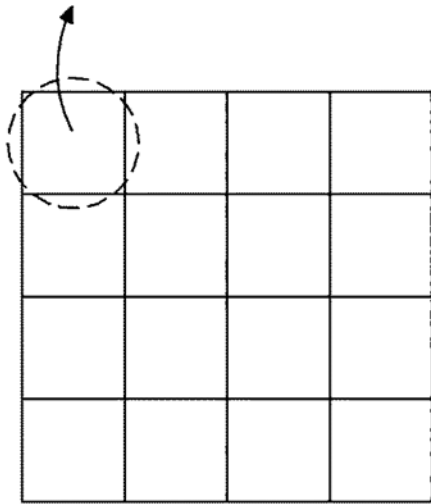


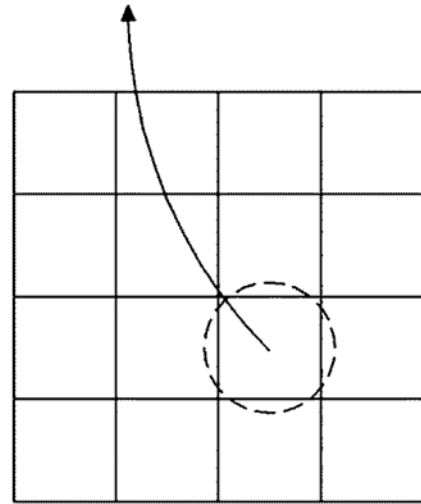
FIG. 20

REPRESENTATIVE SUB-BLOCK



(a)

REPRESENTATIVE SUB-BLOCK



(b)

FIG. 21

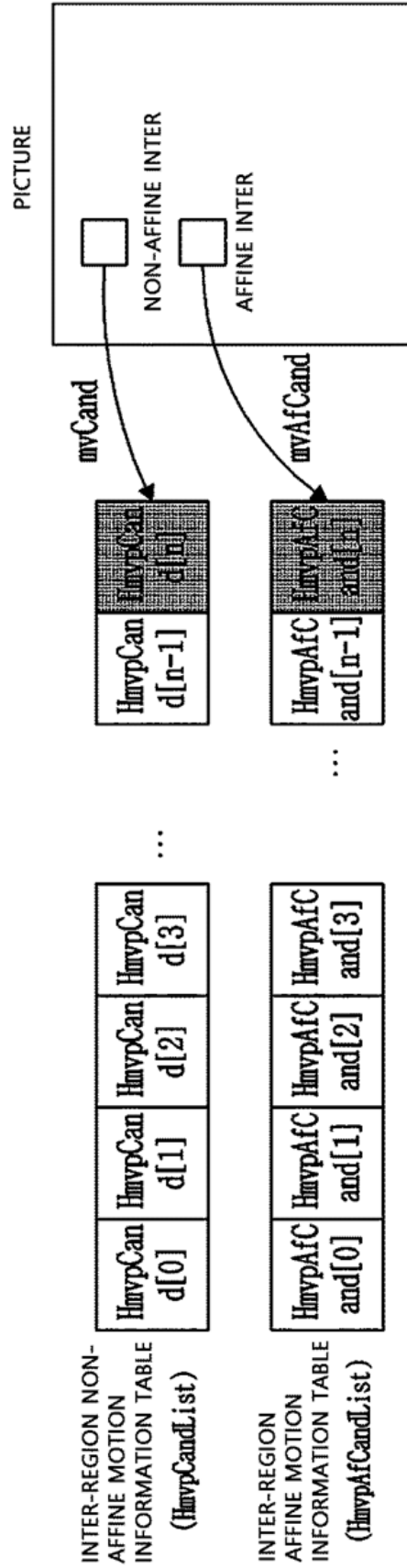


FIG. 22

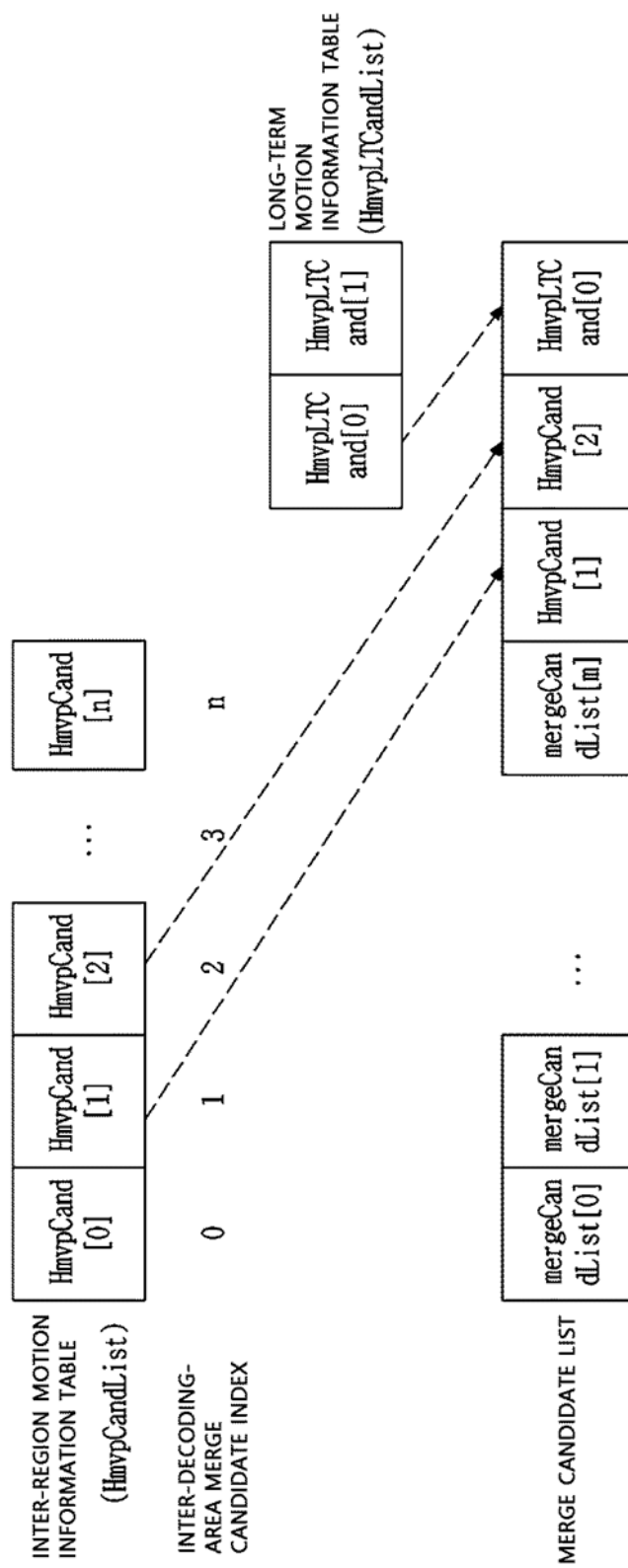


FIG. 23

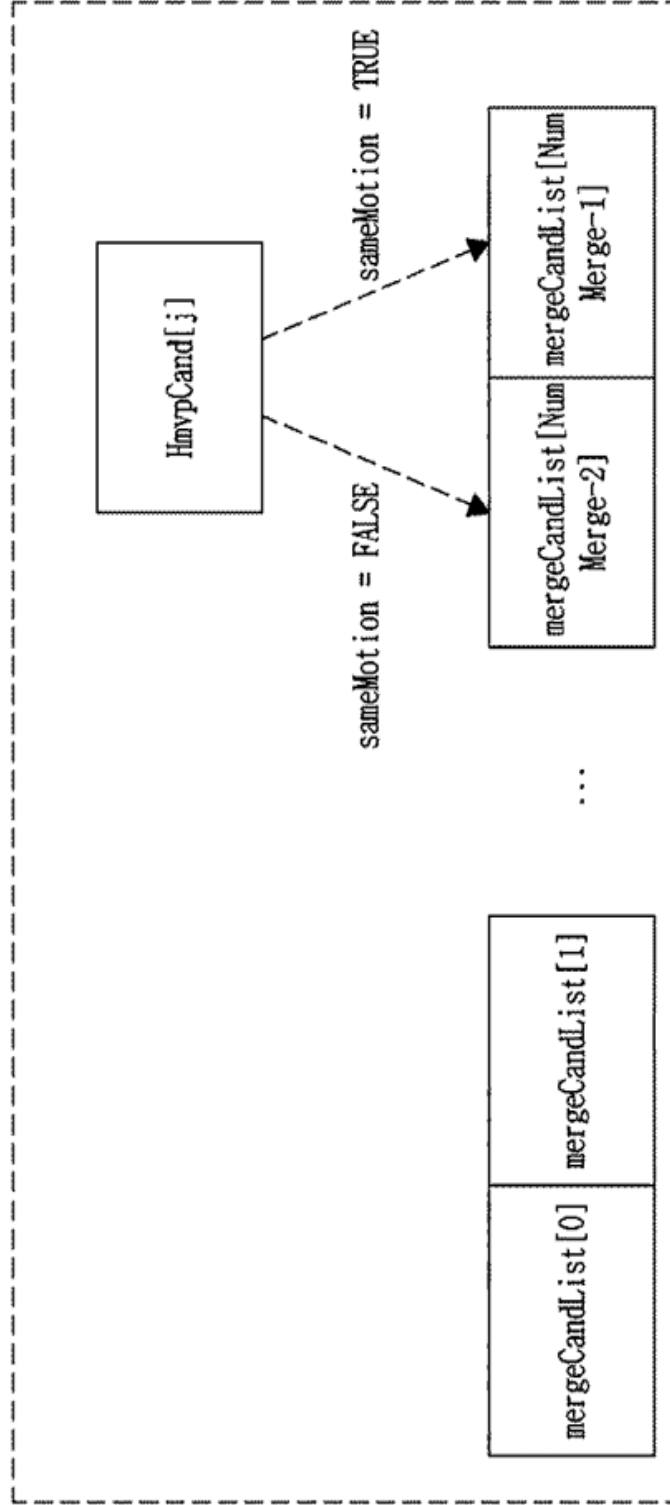


FIG. 24

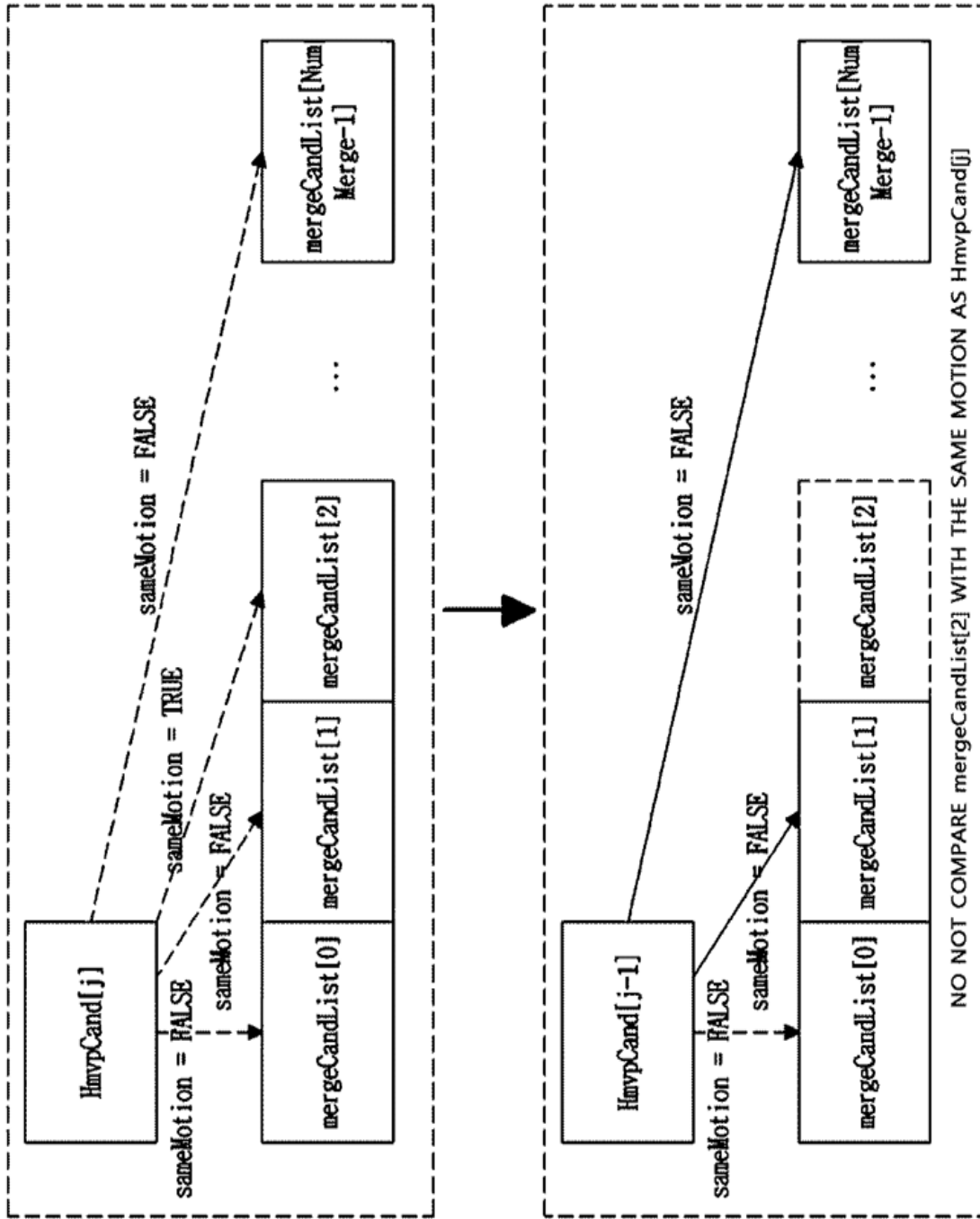


FIG. 25

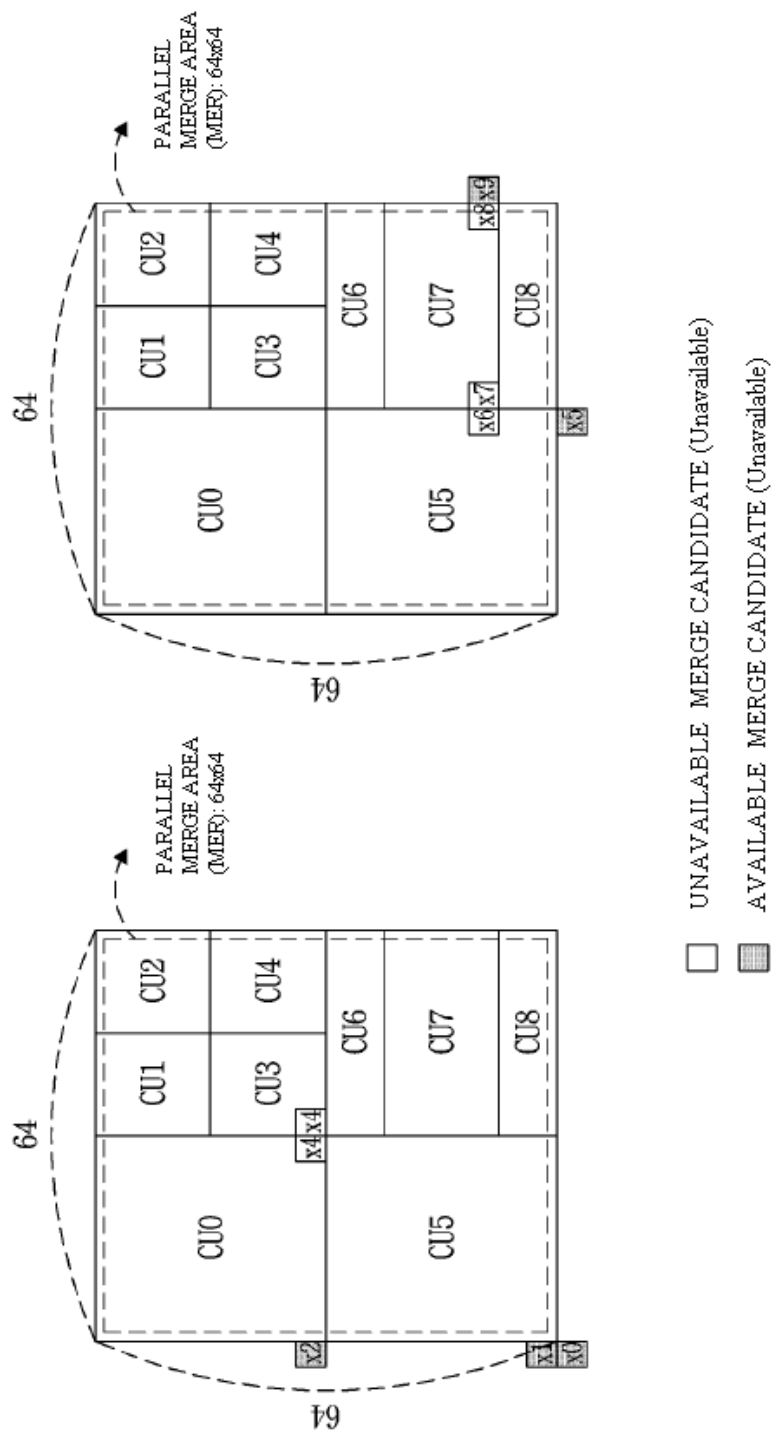


FIG. 26

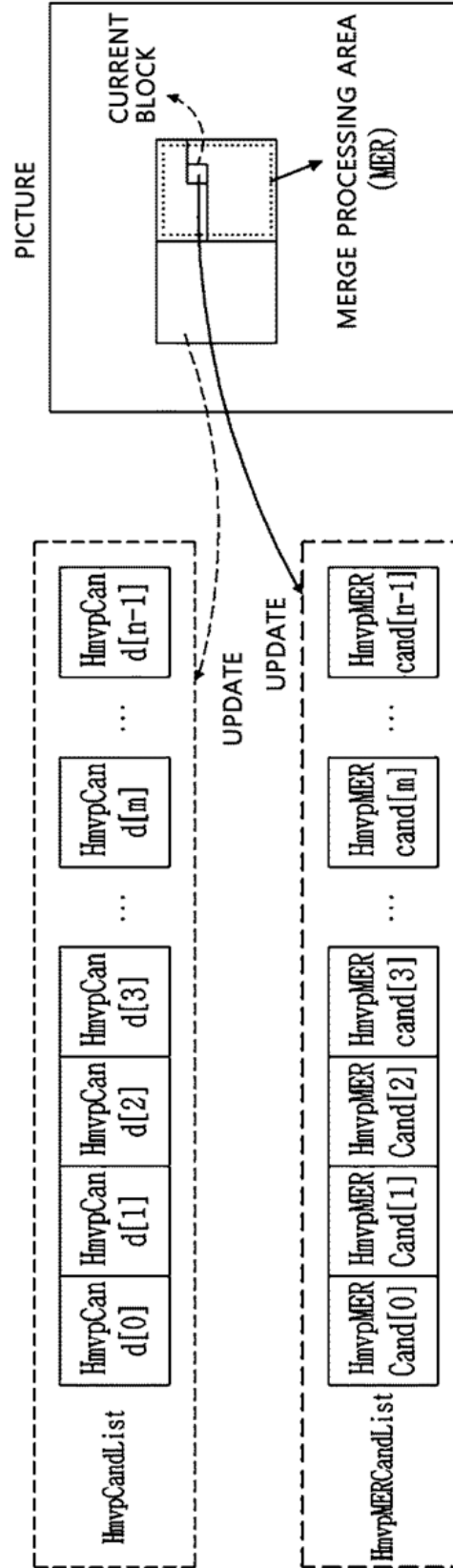


FIG. 27

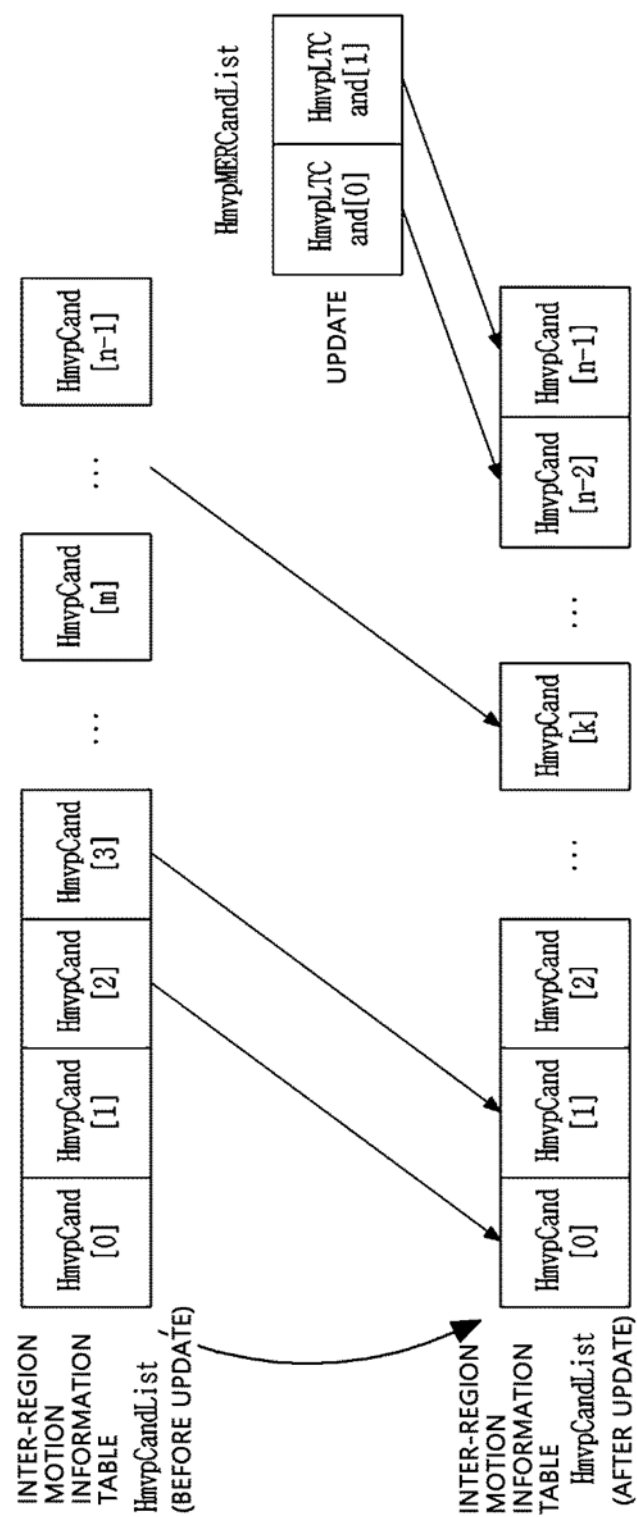


FIG. 28

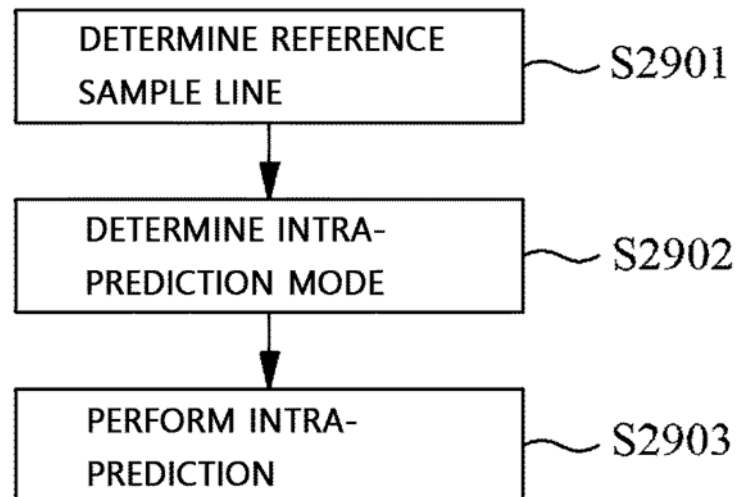


FIG. 29

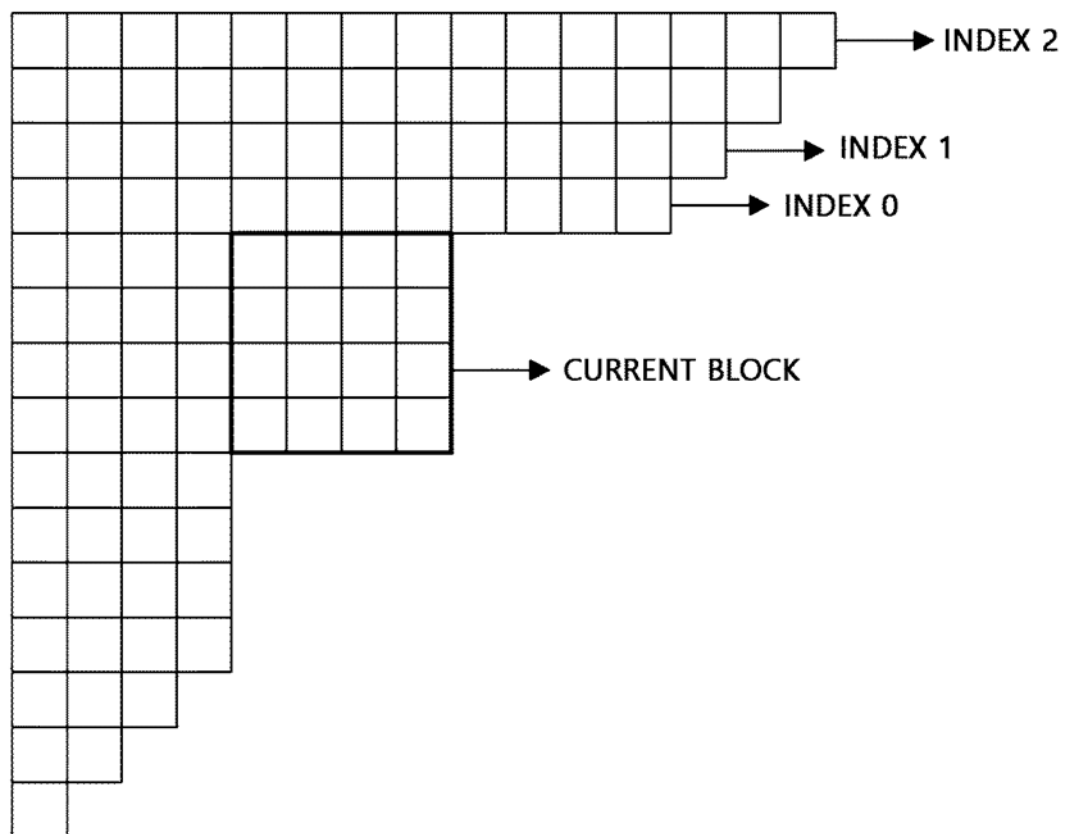


FIG. 30

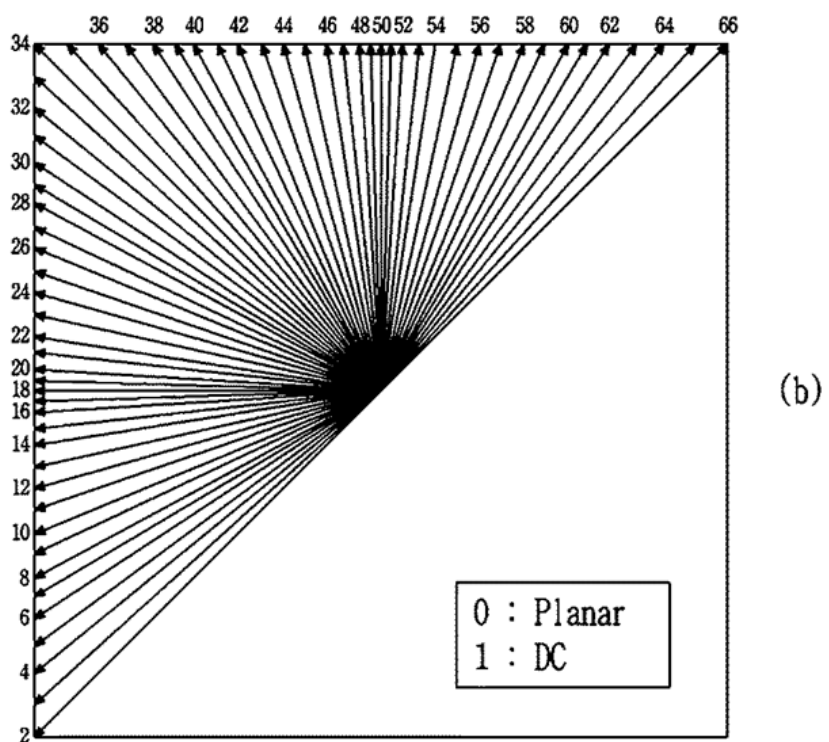
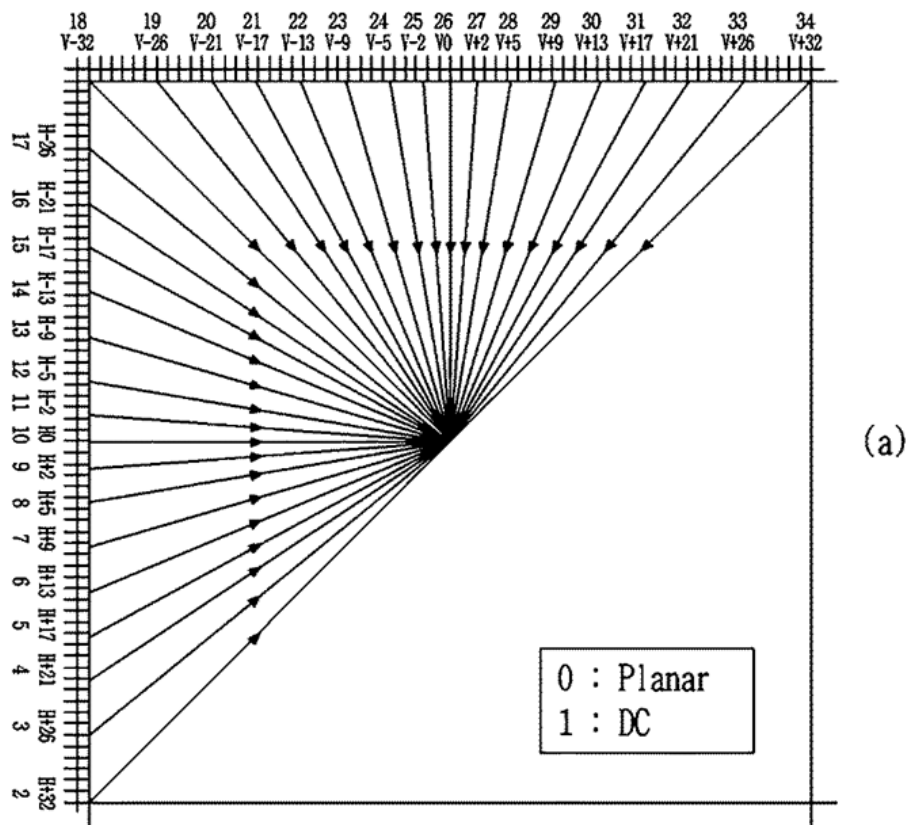
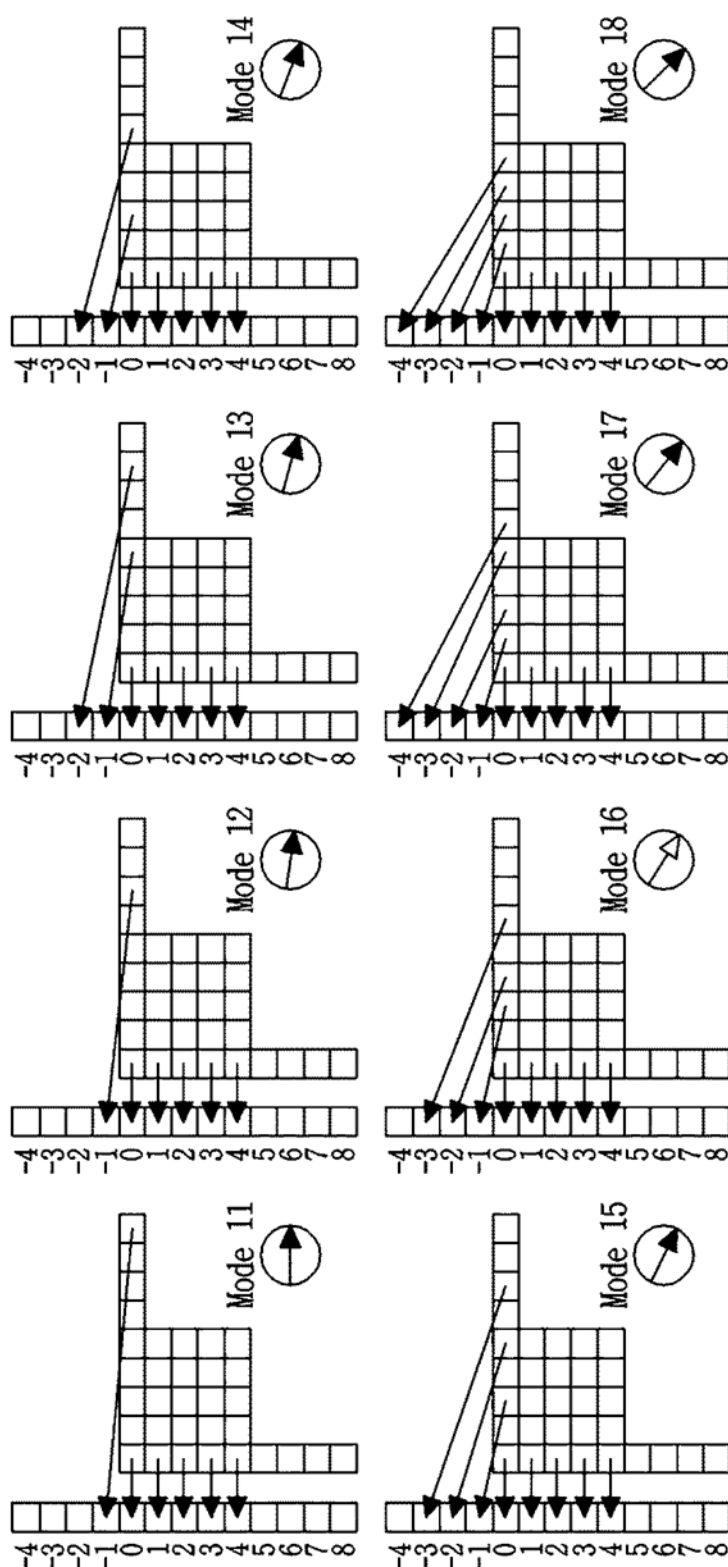


FIG. 31



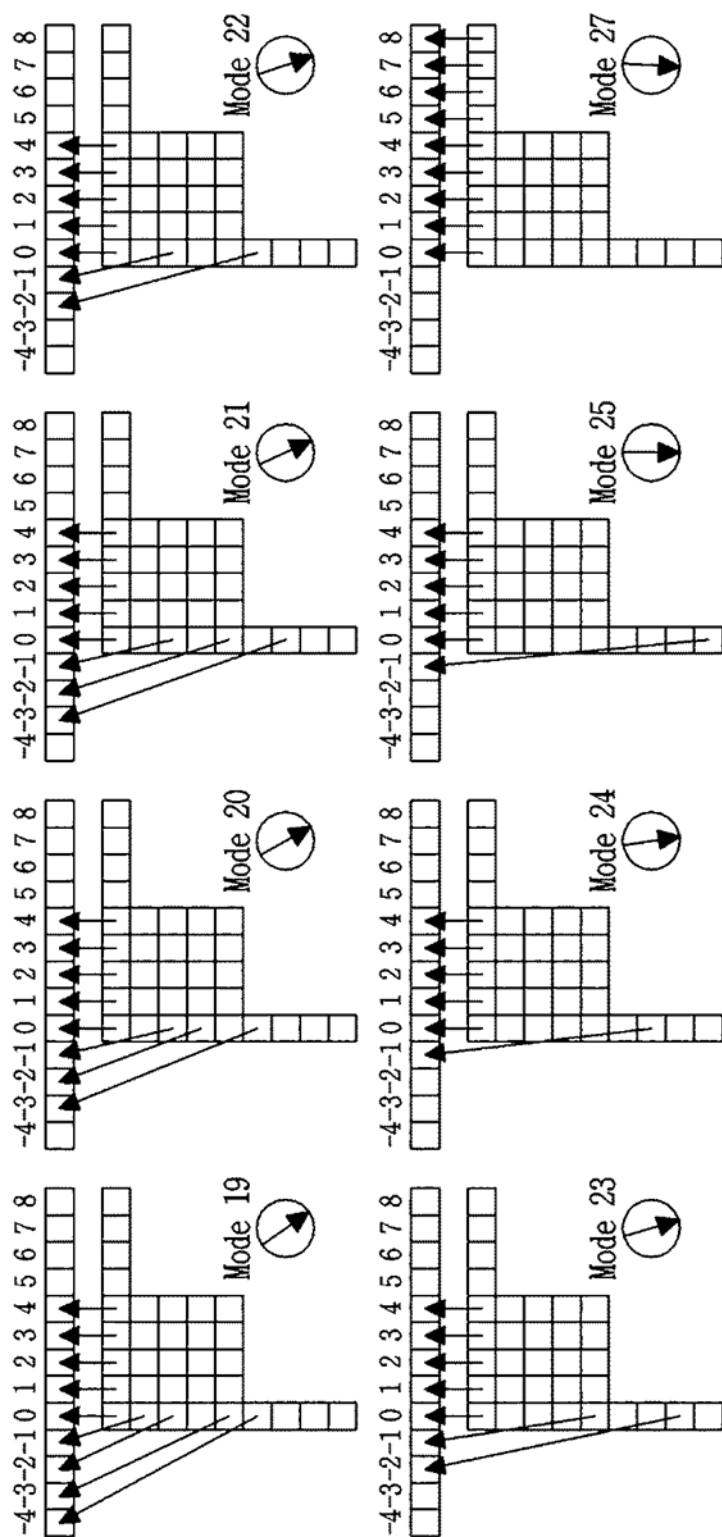


FIG. 33

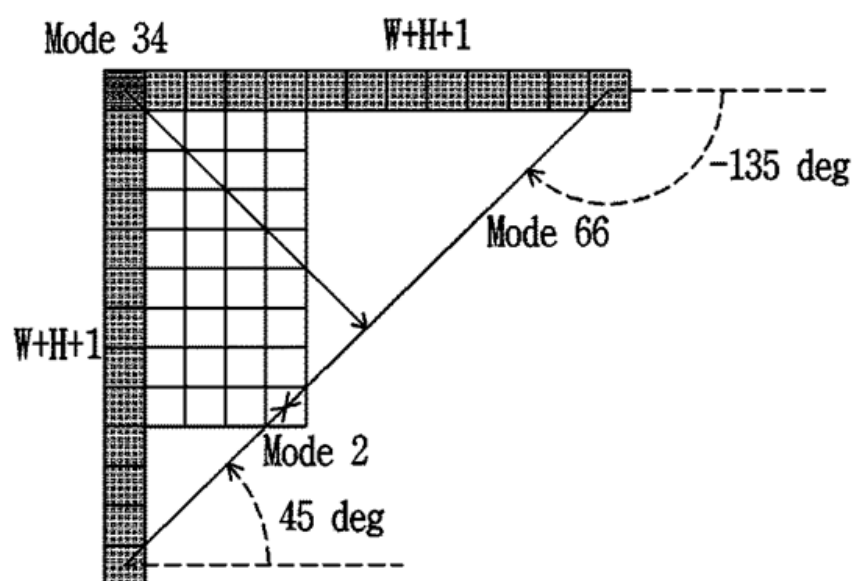


FIG. 34

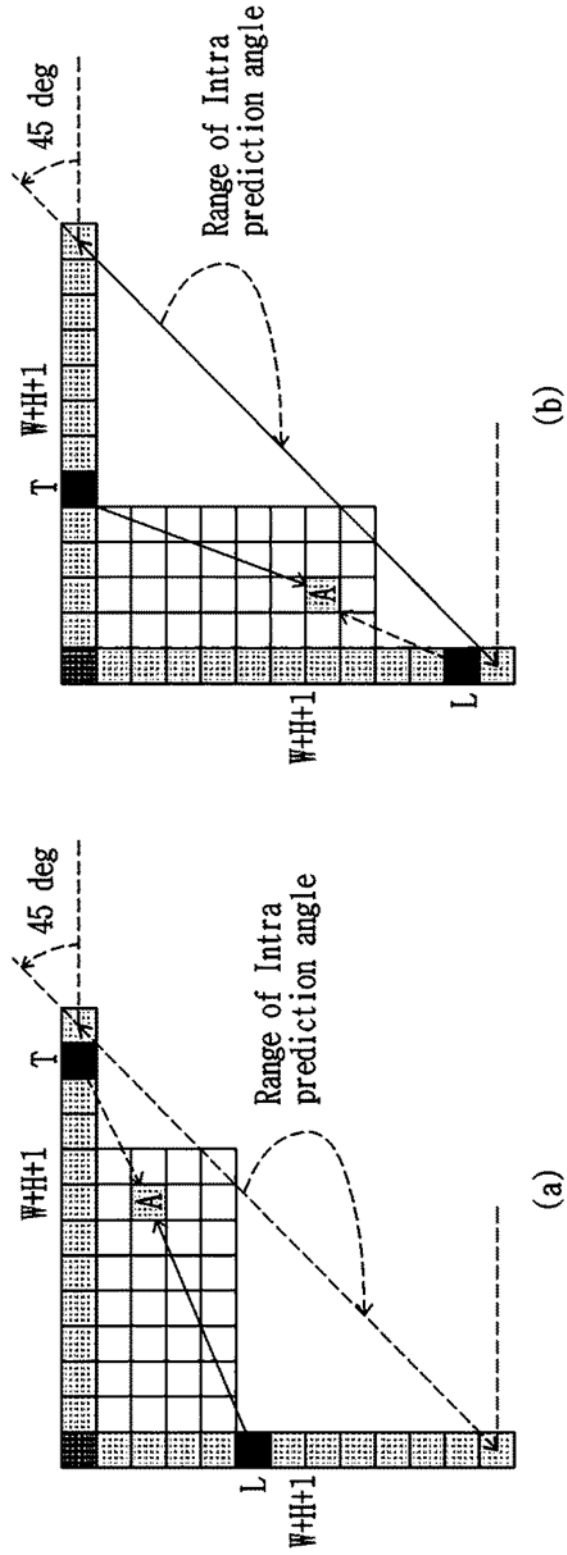


FIG. 35

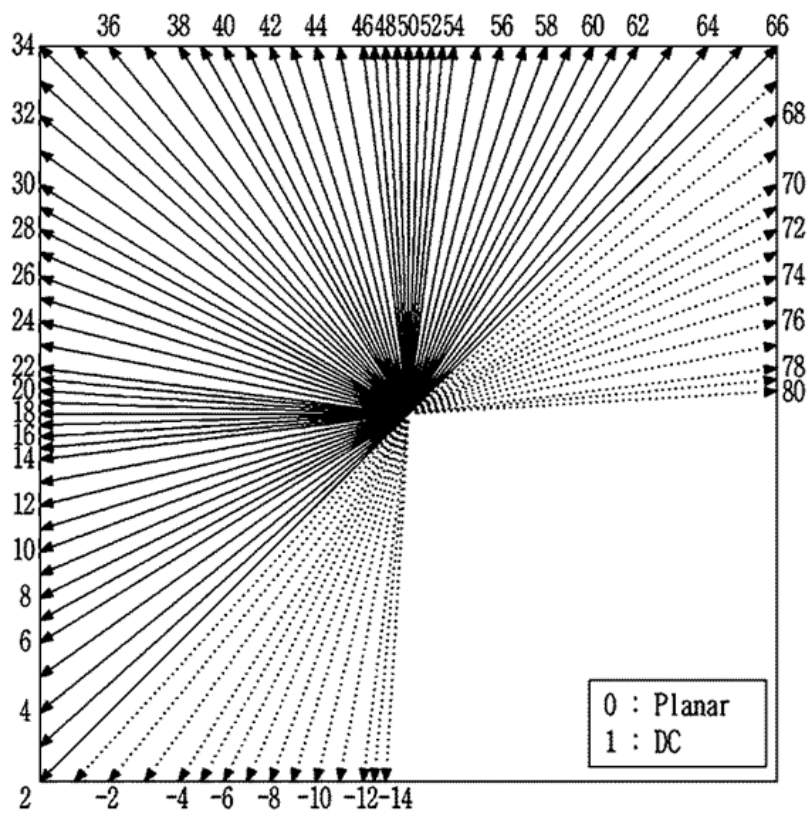


FIG. 36

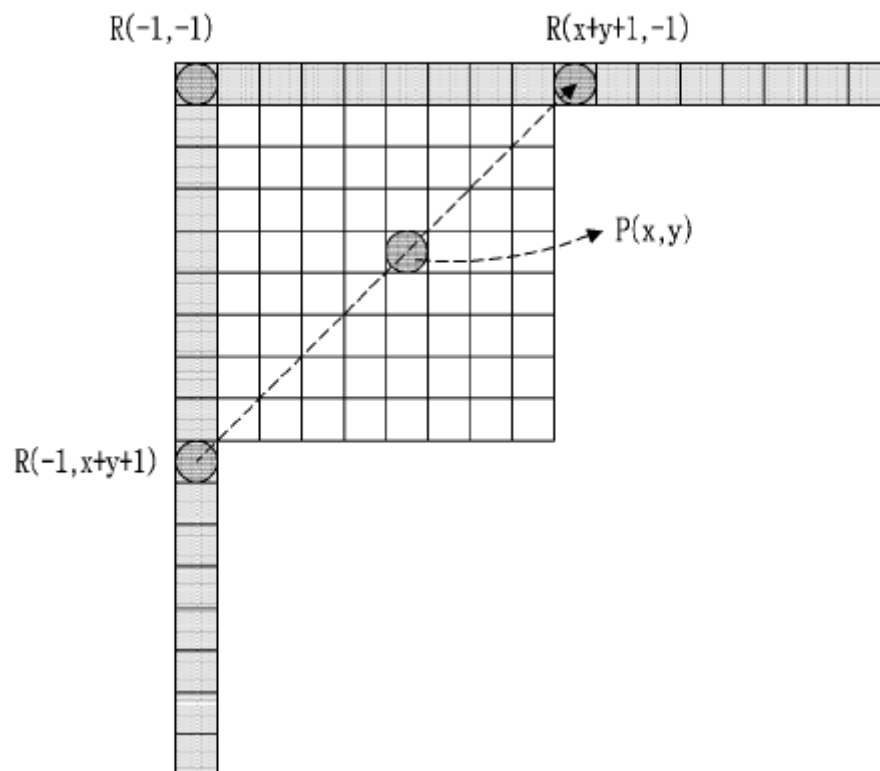


FIG. 37

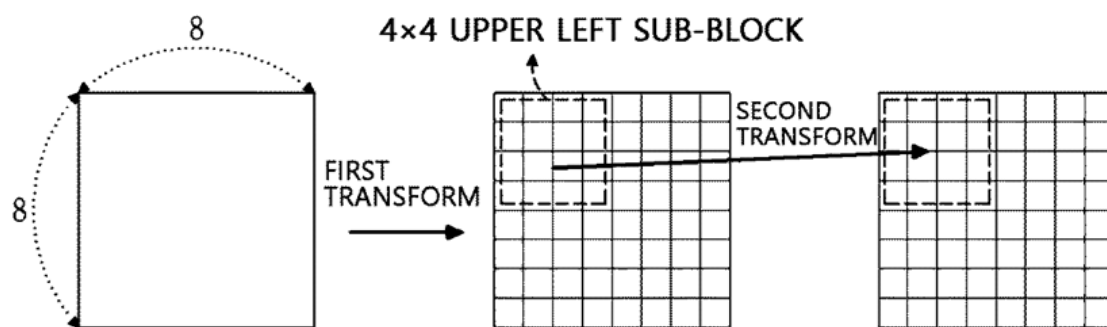


FIG. 38

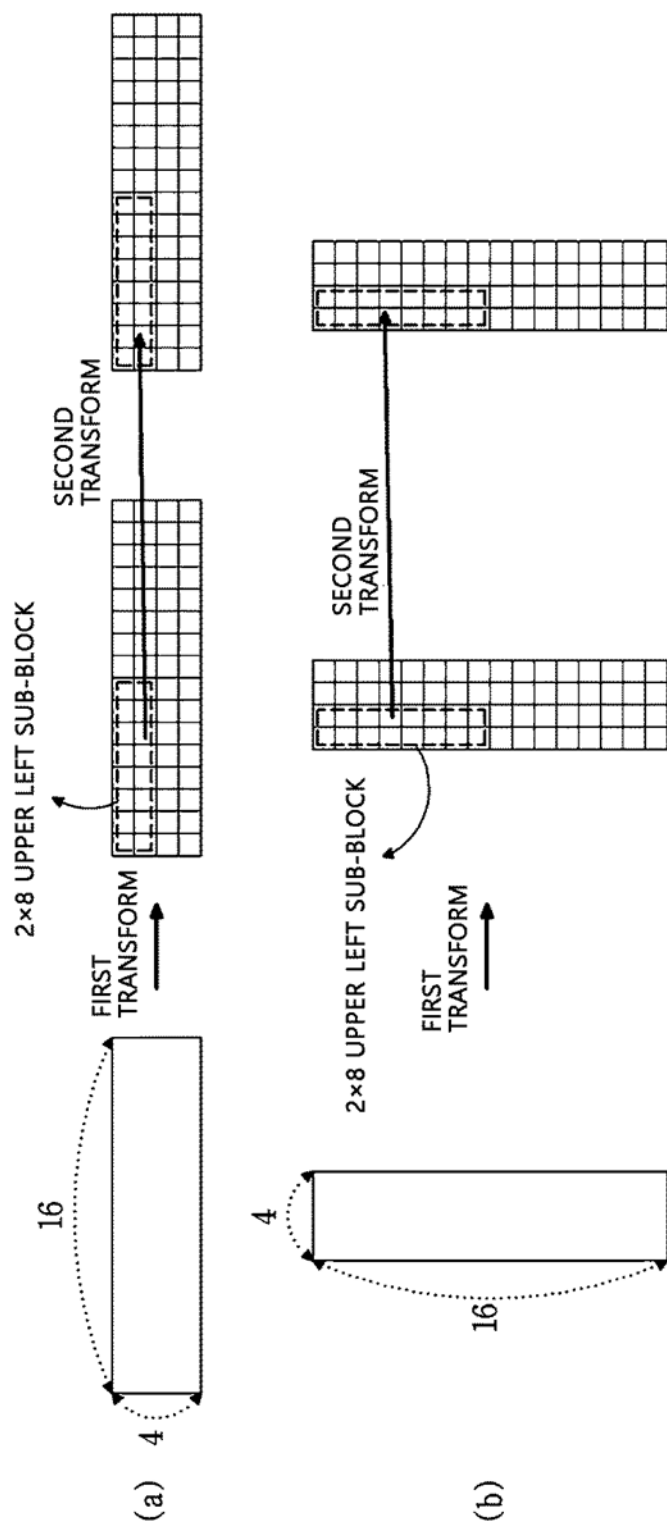
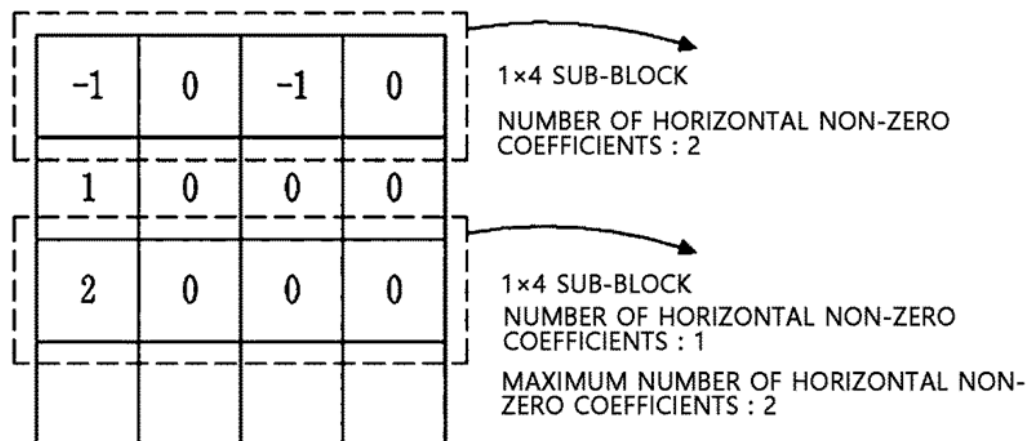
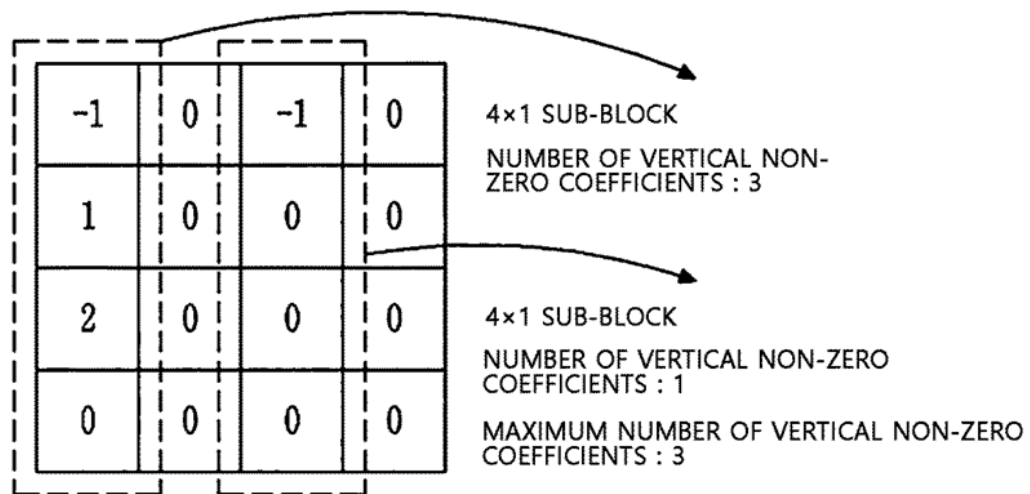


FIG. 39



(a)



(b)

FIG. 40

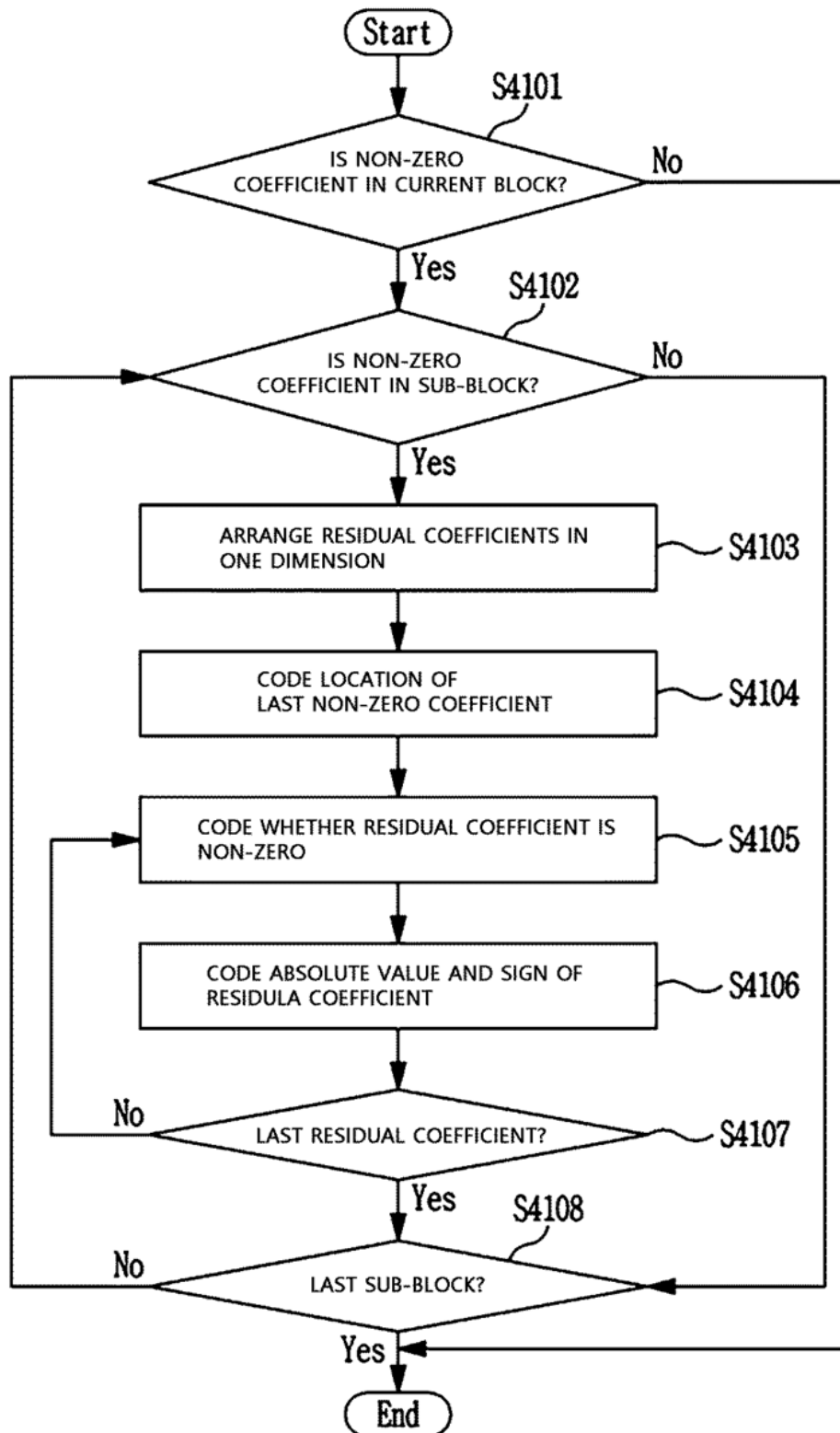


FIG. 41

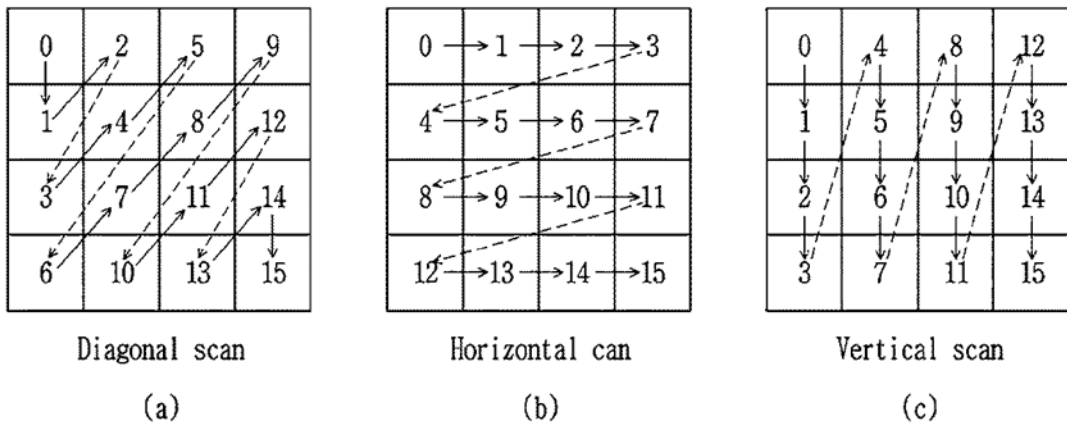


FIG. 42

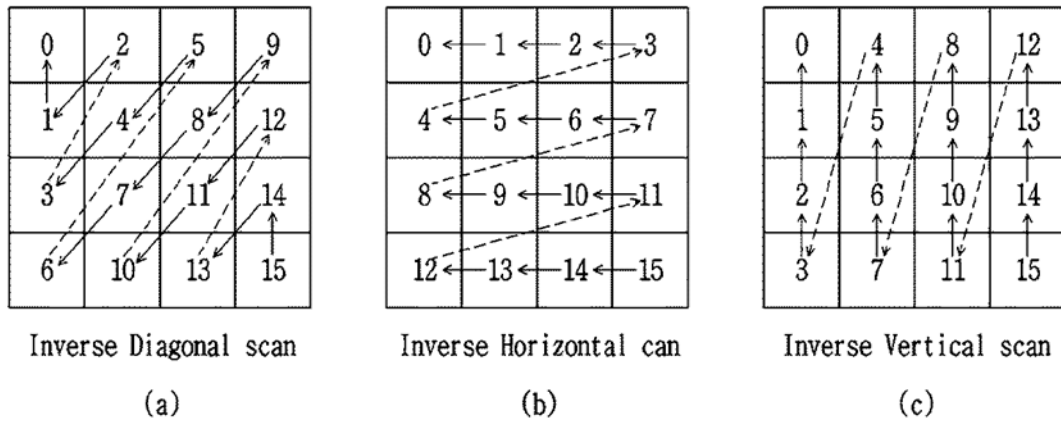


FIG. 43

5

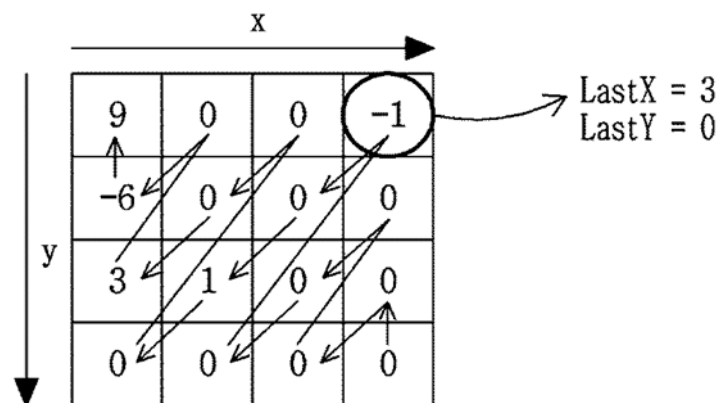


FIG. 44

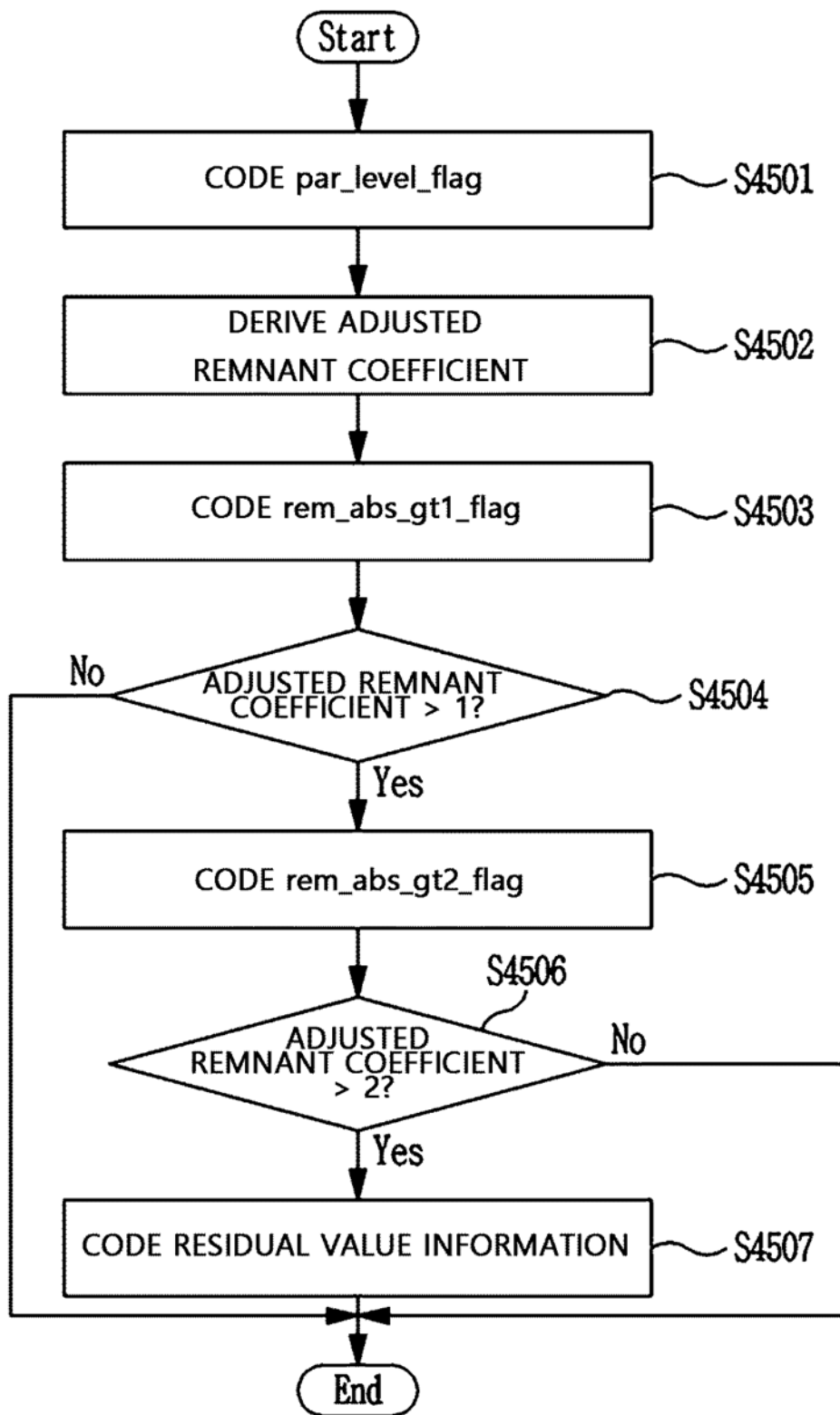


FIG. 45

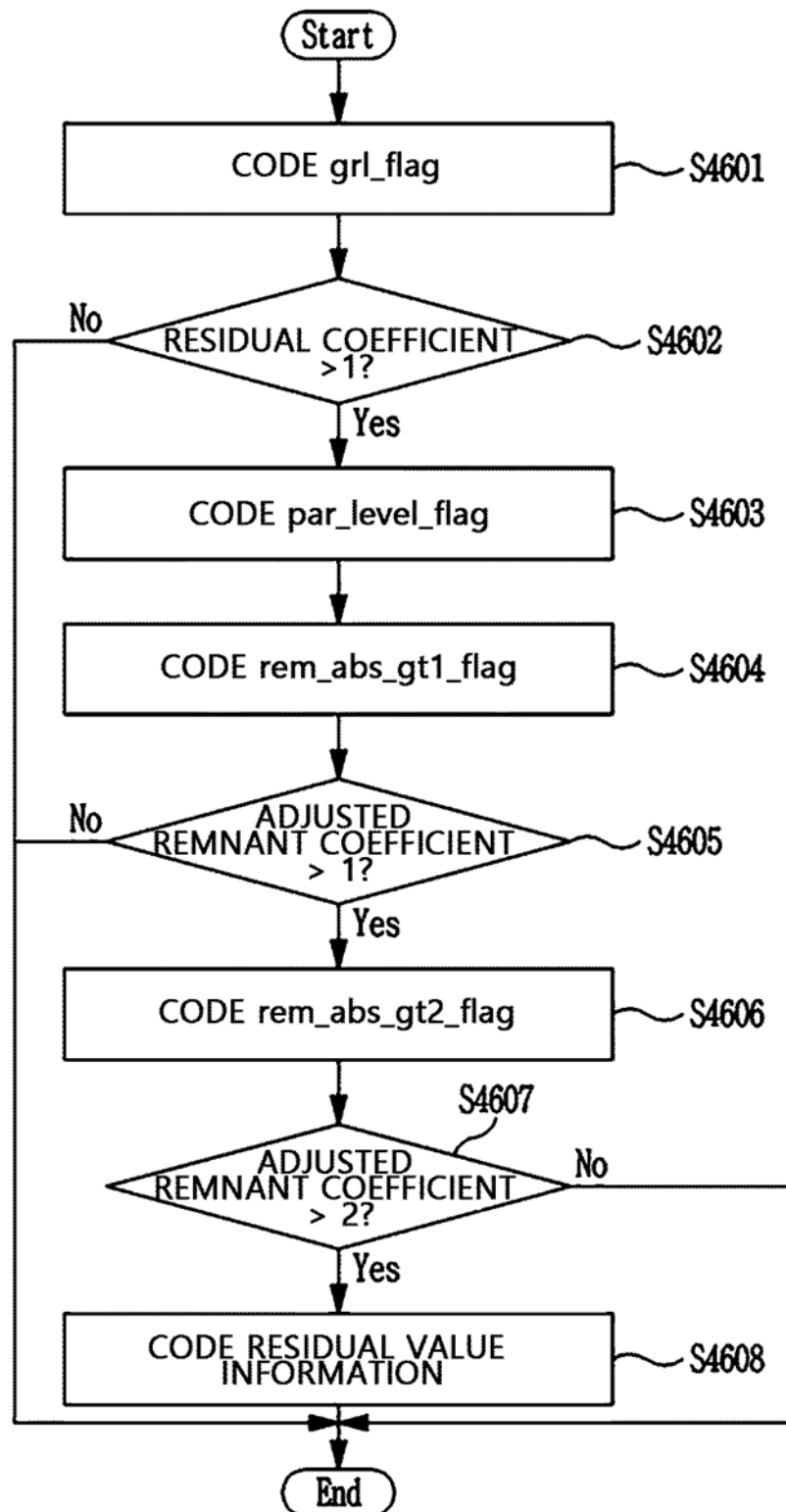


FIG. 46

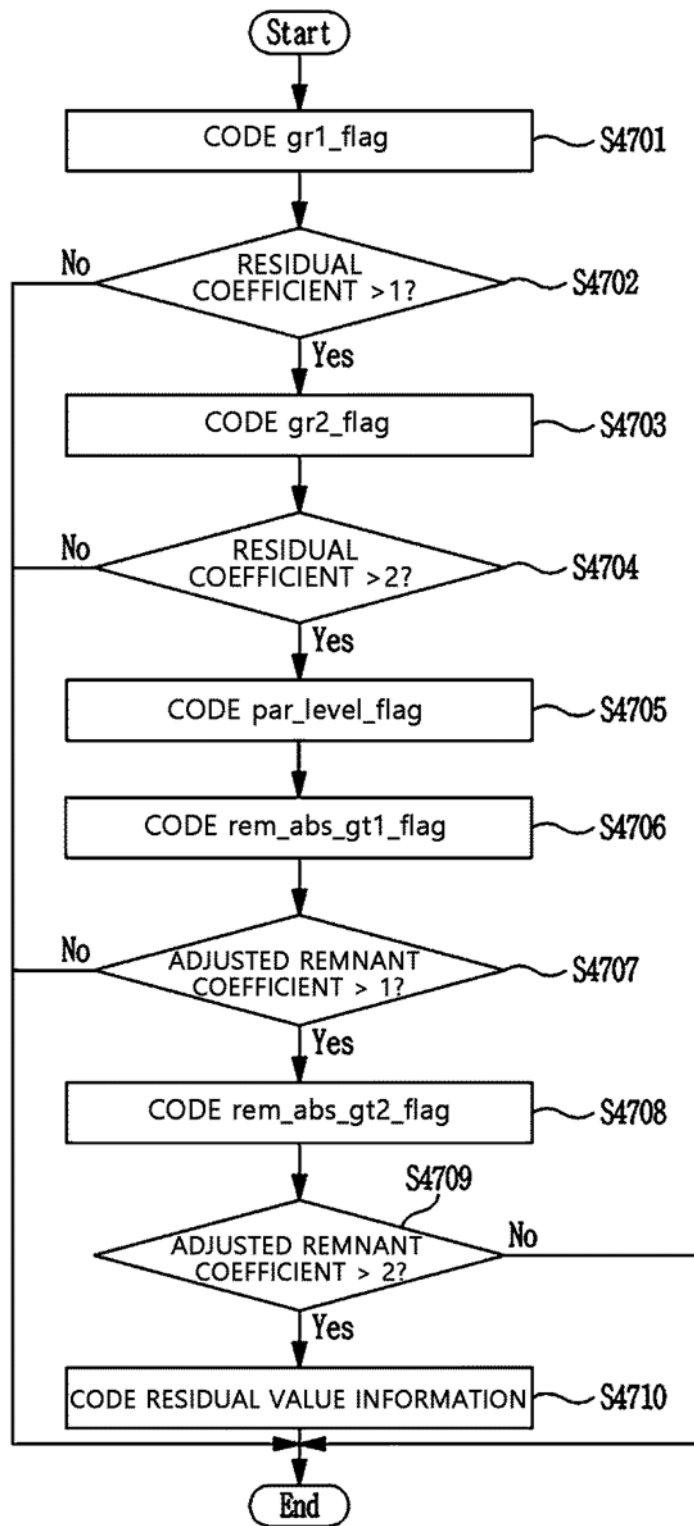


FIG. 47

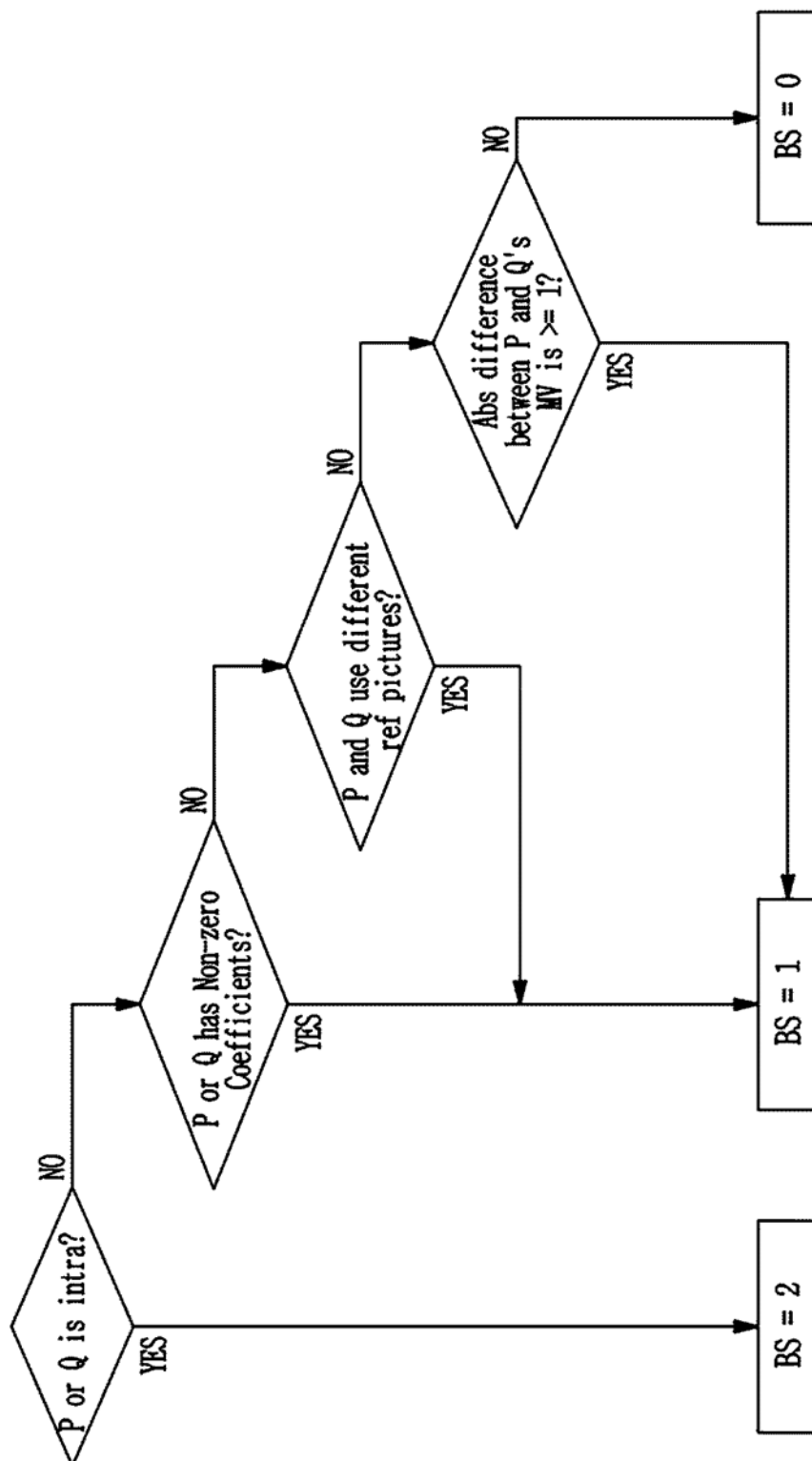


FIG. 48

