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ABSTRACT

A paging receiver has a synthesizer (16) for governing
5 the receive frequency. The paging receiver further has
characteristics (52, 54) which are varied in response to
the receive frequency. These characteristics (52, 54)
include varying the bandwidth (52) of a loop filter (26)
within a phase lock loop (24, 26, 28, 30) within the
10 synthesizer (16) as well as varying the time (52) in which
a detector circuit (20) used to extract a DC level from a
recovered audio signal is disabled. Furthermore, the
bandwidth (54) of the loop filter (26) is varied in
response to switching from a first receive frequency to a
15 second receive frequency in order to provide for either a
uniform frequency lock time (54) or for a rapid frequency
lock time (54). Furthermore, the time (54) in which the
detector circuit (20) is disabled is correspondingly
changed.

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Fig. 1 accompanies this abstract.

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