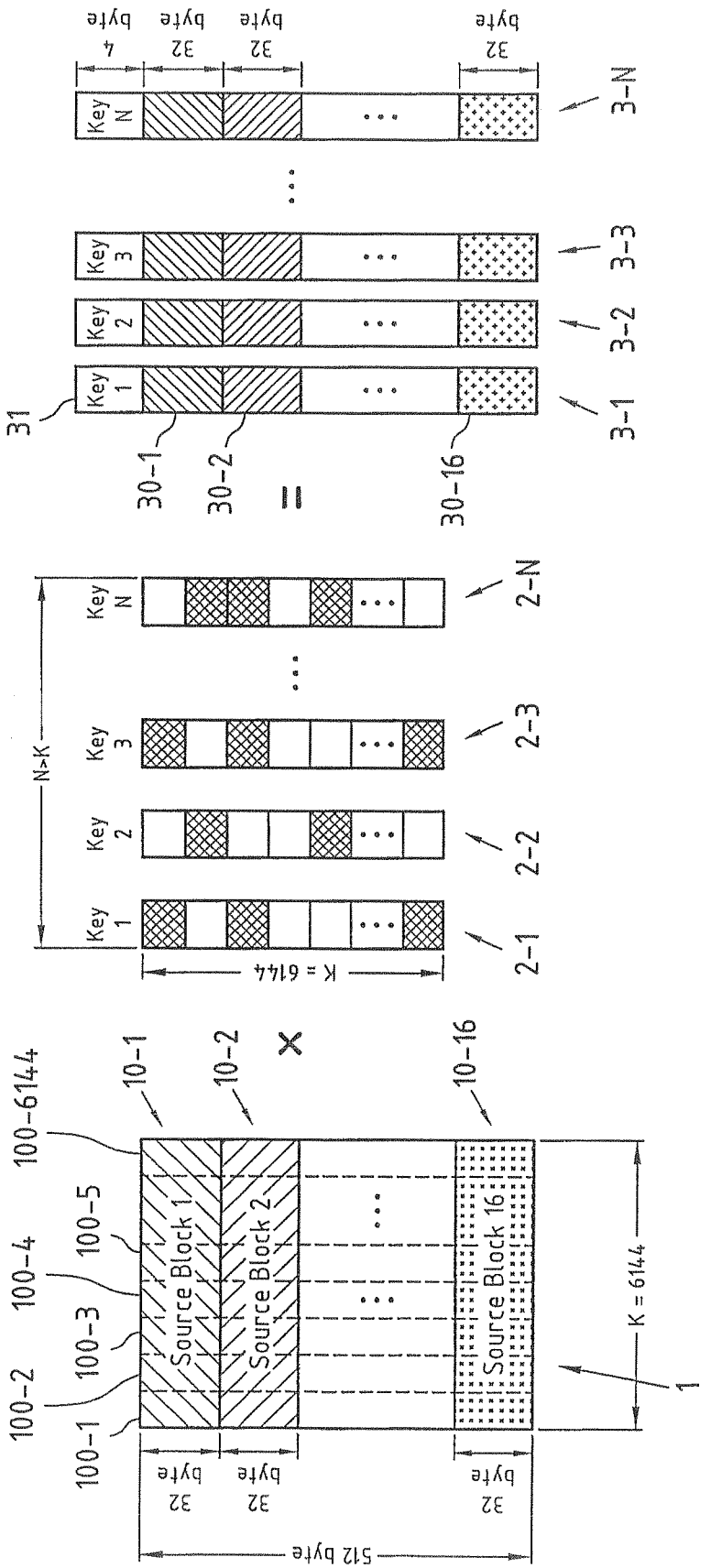

100-6144
100-5
100-4
100-3
100-2
100-1
10-1
10-2
10-16
Source Block 1
Source Block 2
Source Block 16
32 byte
32 byte
32 byte
512 byte
K = 6144
1
N>K
Key 1
Key 2
Key 3
Key N
2-1
2-2
2-3
2-N
K = 6144
31
30-1
30-2
30-16
3-1
3-2
3-3
3-N
4 byte
32 byte
32 byte
32 byte

Fig.1

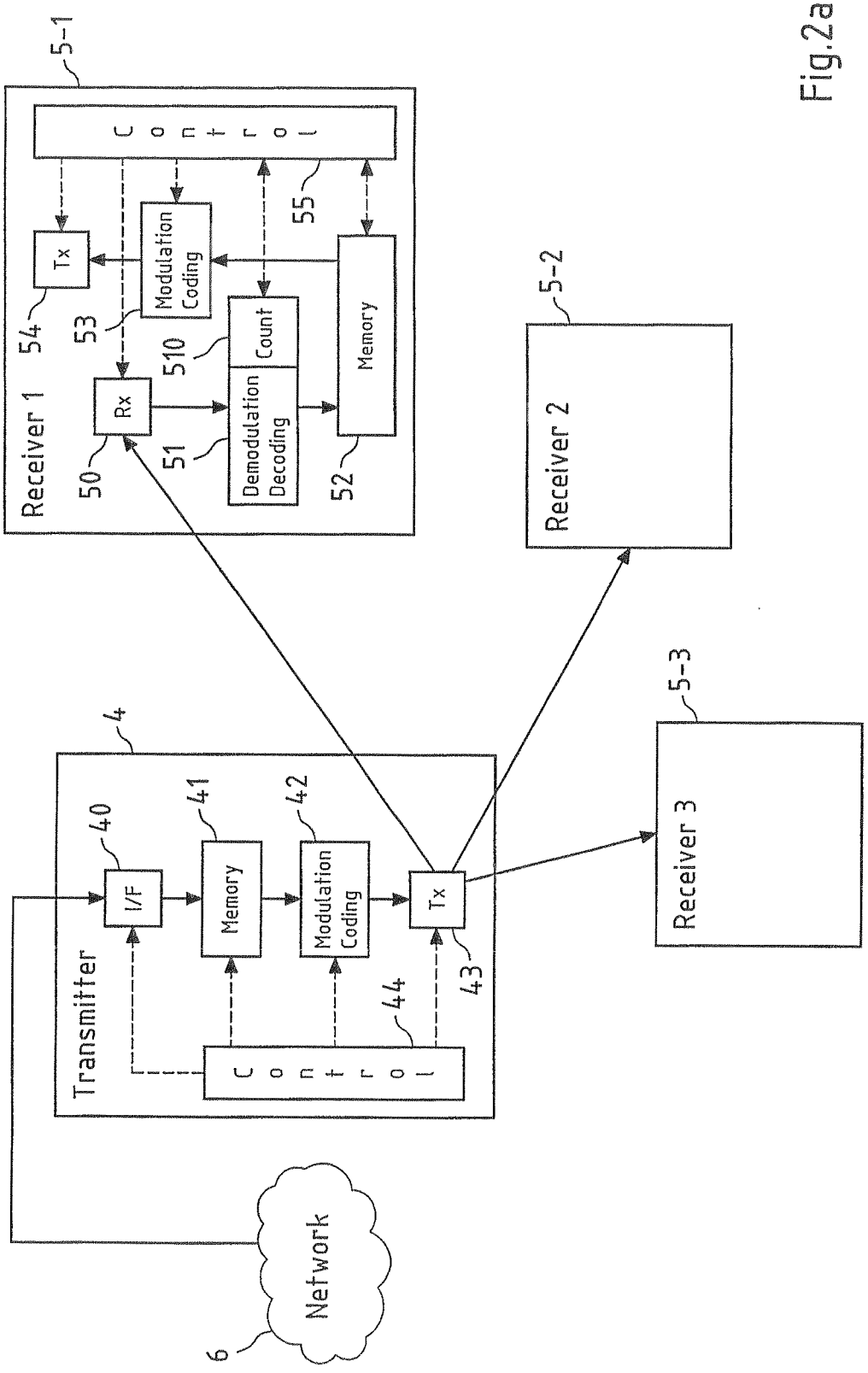

5-1
Receiver 1
Control
55
Tx
54
Modulation Coding
53
Count
510
Memory
Rx
50
Demodulation Decoding
51
52
5-2
Receiver 2
5-3
Receiver 3
4
Transmitter
40
I/F
41
Memory
42
Modulation Coding
Tx
43
44
Control
Network
6

Fig.2a

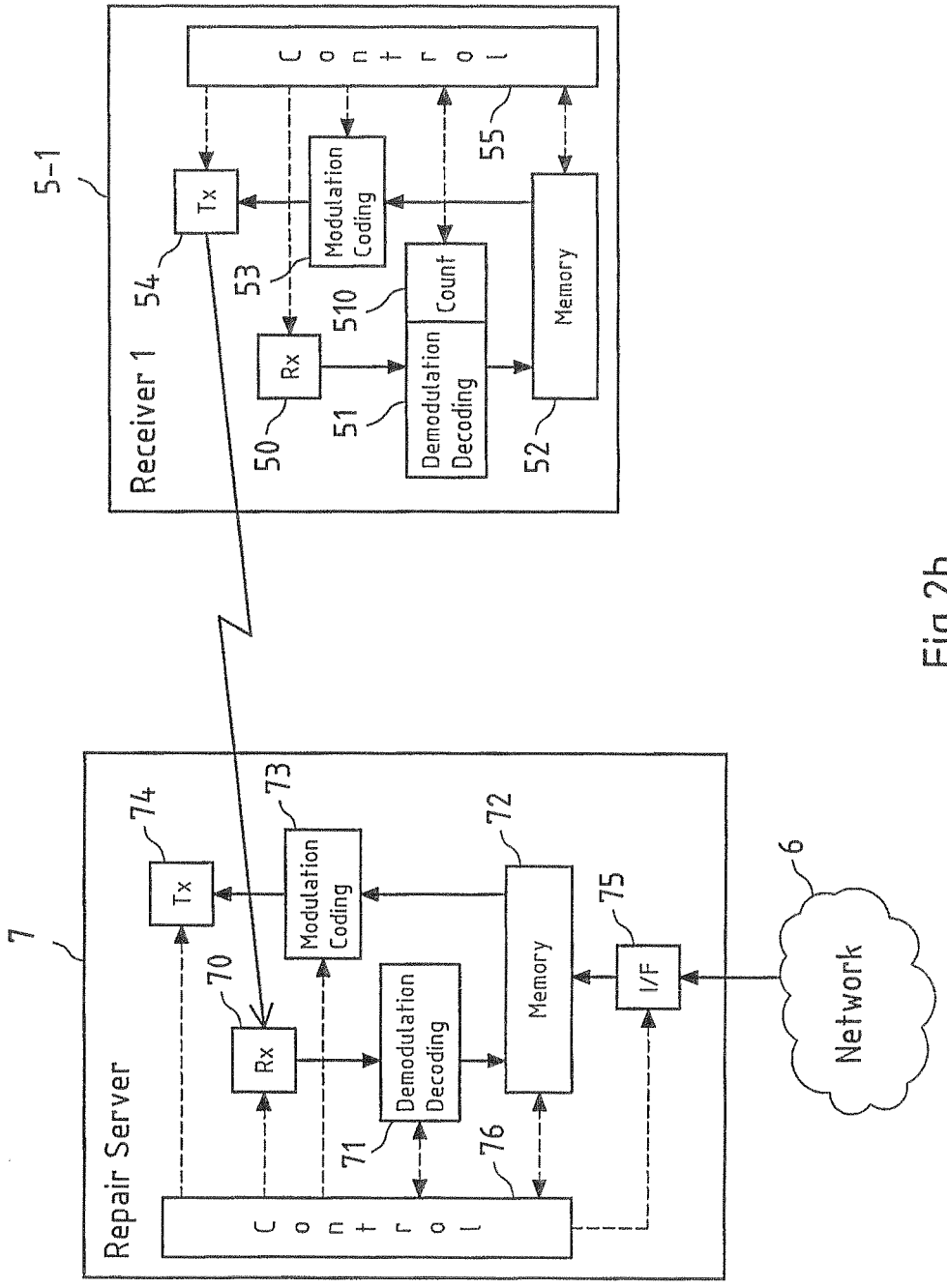

Repair Server
7
Control
Tx
74
Modulation Coding
73
Rx
70
Demodulation Decoding
71
Memory
72
I/F
75
76
Network
6
Receiver 1
5-1
Control
Tx
54
Modulation Coding
53
Rx
50
Demodulation Decoding
51
Count
510
Memory
52
55

Fig.2b

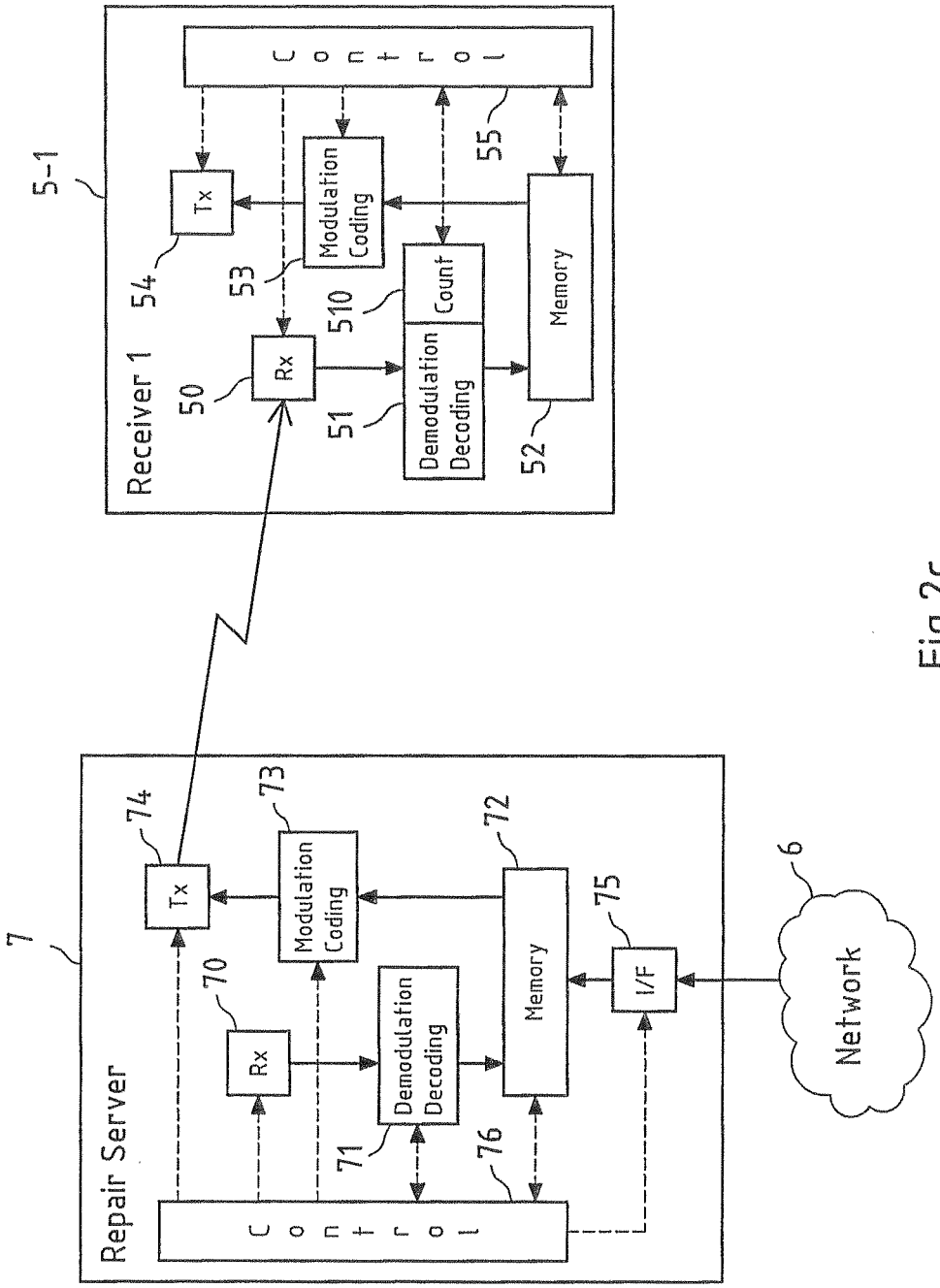

Receiver 1
5-1
Control
55
Tx
54
Modulation Coding
53
Count
510
Memory
Rx
50
Demodulation Decoding
51
52
7
Repair Server
74
Tx
73
Modulation Coding
72
Memory
75
I/F
6
Network
70
Rx
Demodulation Decoding
71
76
Control

Fig.2c

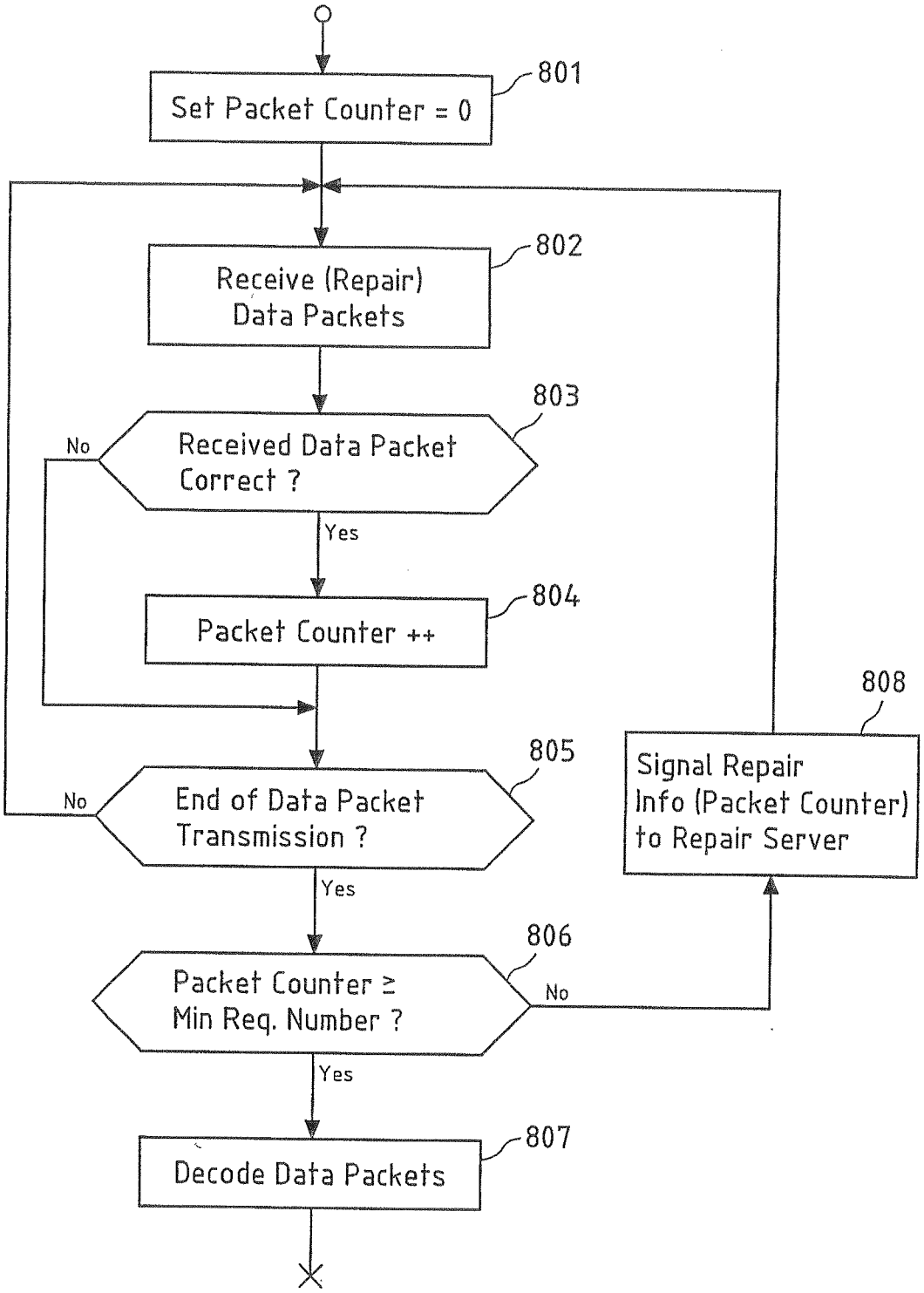

Set Packet Counter = 0
801
Receive (Repair) Data Packets
802
Received Data Packet Correct ?
803
No
Yes
Packet Counter ++
804
End of Data Packet Transmission ?
805
No
Yes
Signal Repair Info (Packet Counter) to Repair Server
808
Packet Counter ≥ Min Req. Number ?
806
No
Yes
Decode Data Packets
807

Fig.3