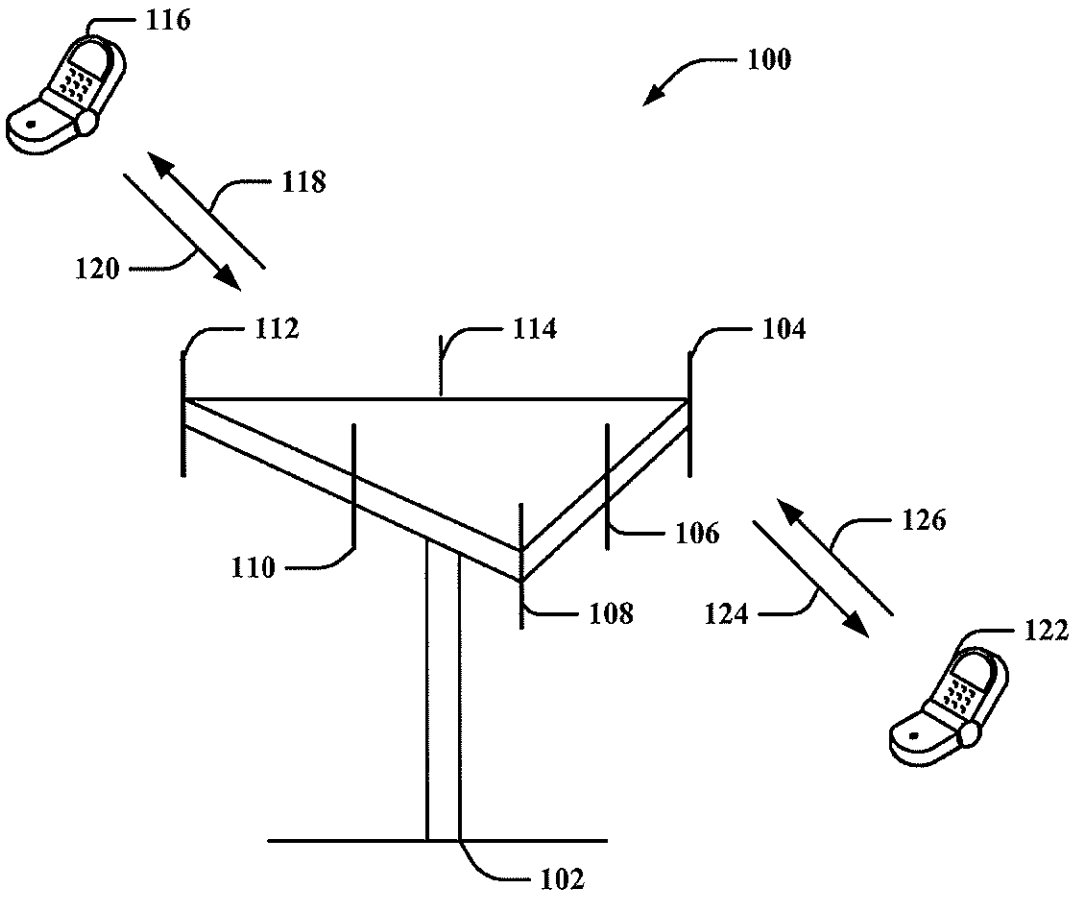


FIG. 1

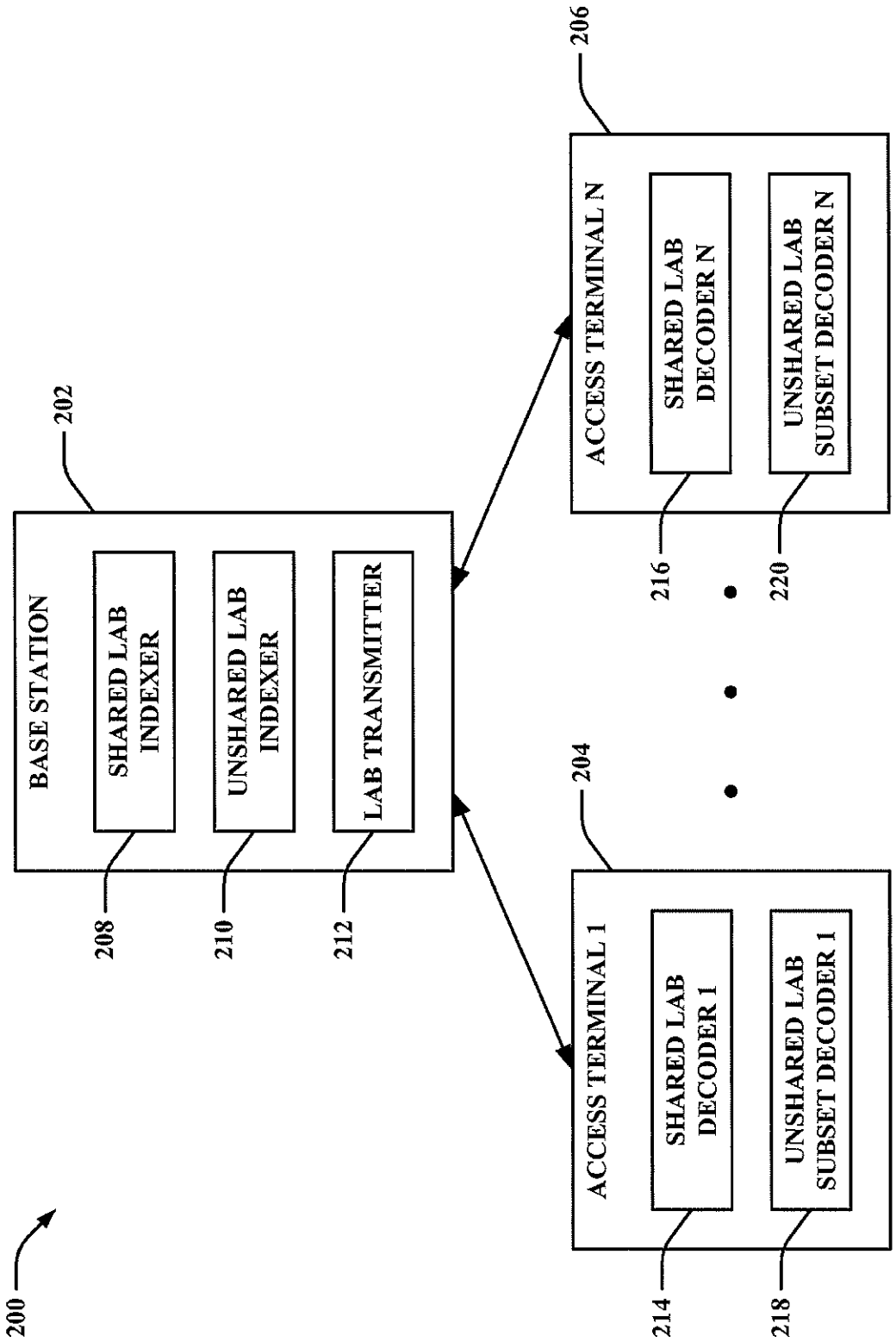

200
BASE STATION
202
SHARED LAB INDEXER
208
UNSHARED LAB INDEXER
210
LAB TRANSMITTER
212
ACCESS TERMINAL 1
204
SHARED LAB DECODER 1
214
UNSHARED LAB SUBSET DECODER 1
218
ACCESS TERMINAL N
206
SHARED LAB DECODER N
216
UNSHARED LAB SUBSET DECODER N
220

FIG. 2

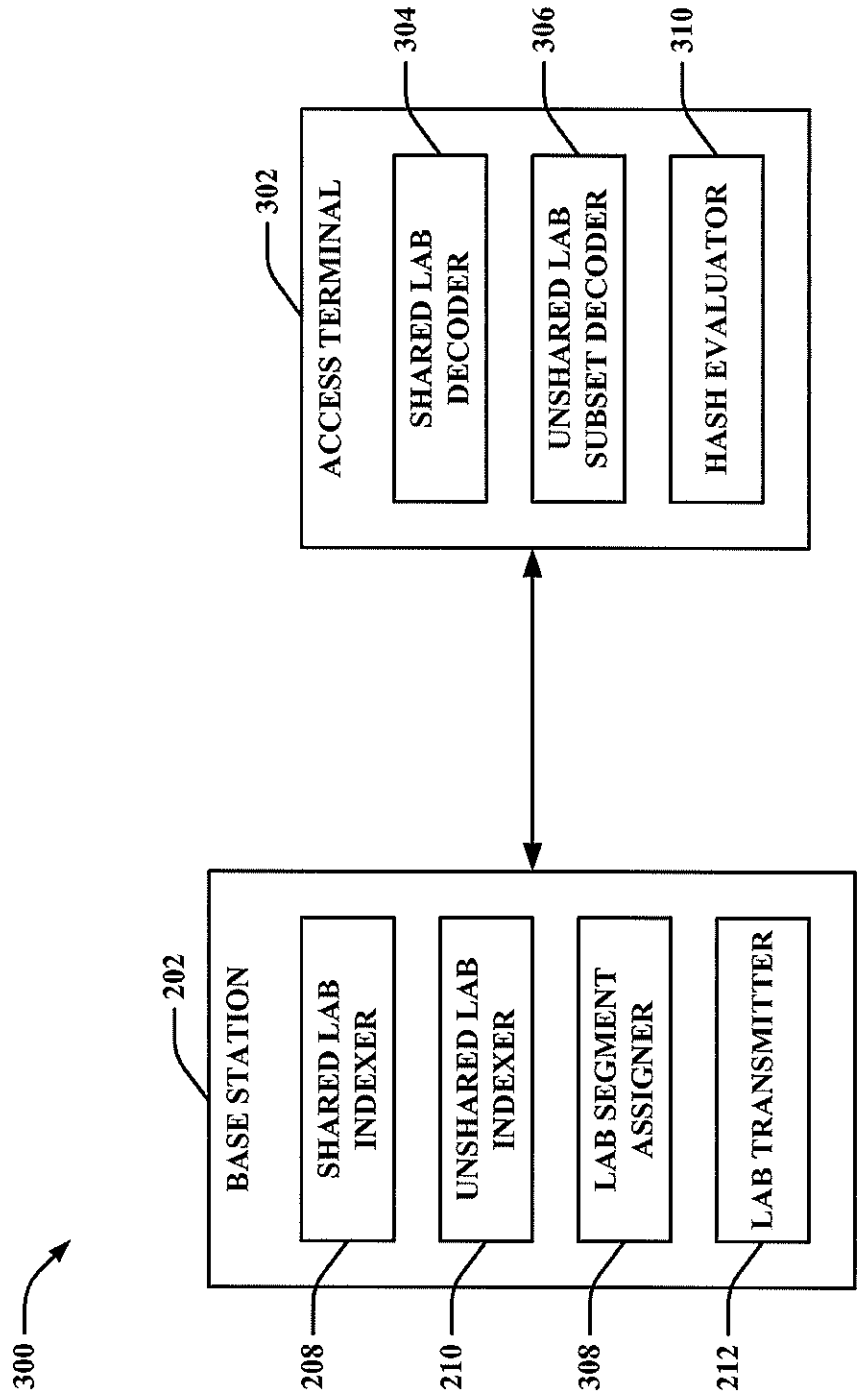

300
202
BASE STATION
SHARED LAB INDEXER
UNSHARED LAB INDEXER
LAB SEGMENT ASSIGNER
LAB TRANSMITTER
208
210
308
212
302
ACCESS TERMINAL
SHARED LAB DECODER
UNSHARED LAB SUBSET DECODER
HASH EVALUATOR
304
306
310

FIG. 3

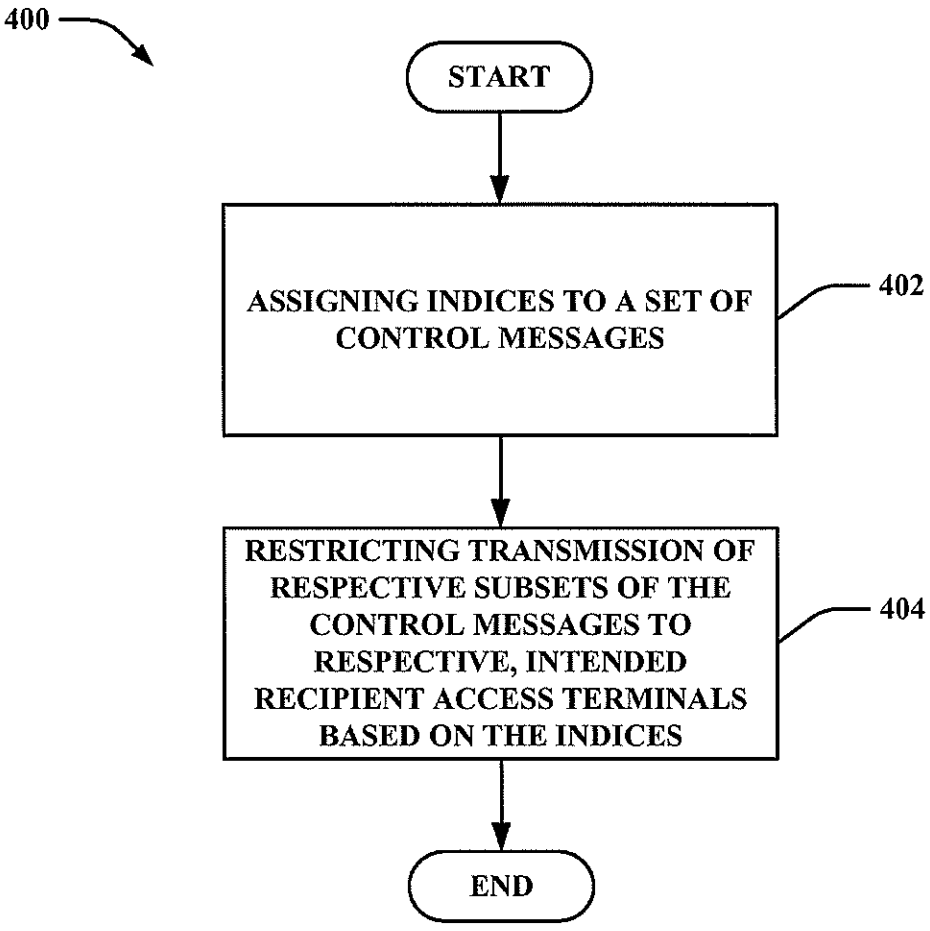

400
START
ASSIGNING INDICES TO A SET OF CONTROL MESSAGES
402
RESTRICTING TRANSMISSION OF RESPECTIVE SUBSETS OF THE CONTROL MESSAGES TO RESPECTIVE, INTENDED RECIPIENT ACCESS TERMINALS BASED ON THE INDICES
404
END

FIG. 4

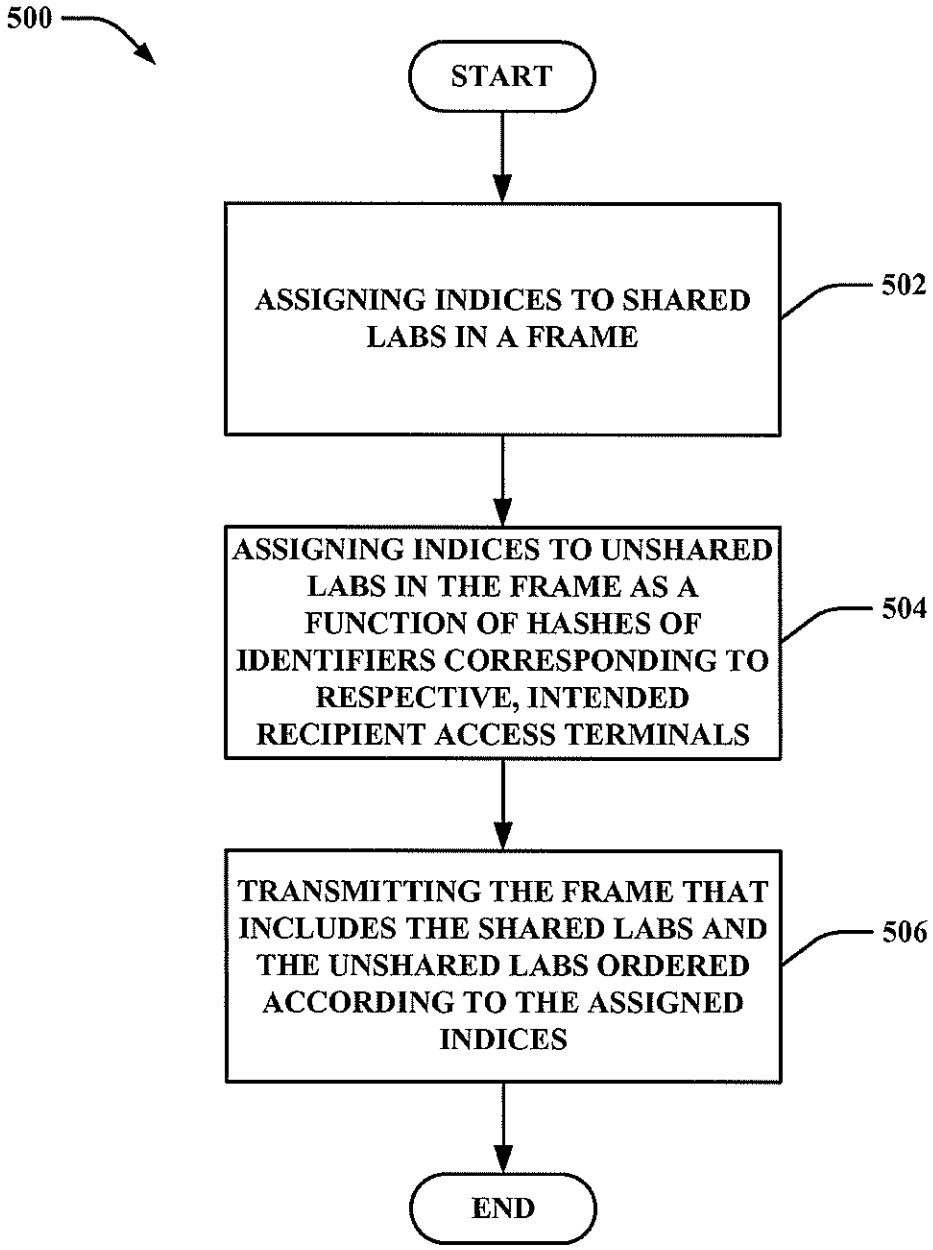

500
START
ASSIGNING INDICES TO SHARED LABS IN A FRAME
502
ASSIGNING INDICES TO UNSHARED LABS IN THE FRAME AS A FUNCTION OF HASHES OF IDENTIFIERS CORRESPONDING TO RESPECTIVE, INTENDED RECIPIENT ACCESS TERMINALS
504
TRANSMITTING THE FRAME THAT INCLUDES THE SHARED LABS AND THE UNSHARED LABS ORDERED ACCORDING TO THE ASSIGNED INDICES
506
END

FIG. 5

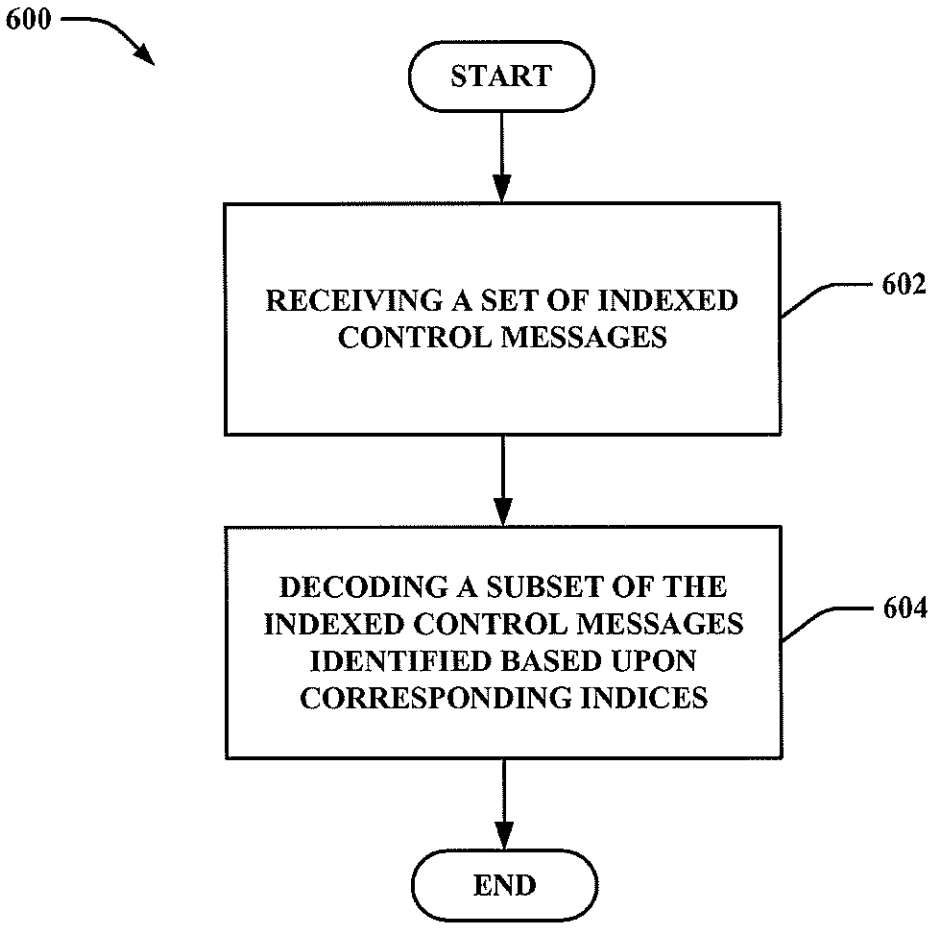

600
START
RECEIVING A SET OF INDEXED CONTROL MESSAGES
602
DECODING A SUBSET OF THE INDEXED CONTROL MESSAGES IDENTIFIED BASED UPON CORRESPONDING INDICES
604
END

FIG. 6

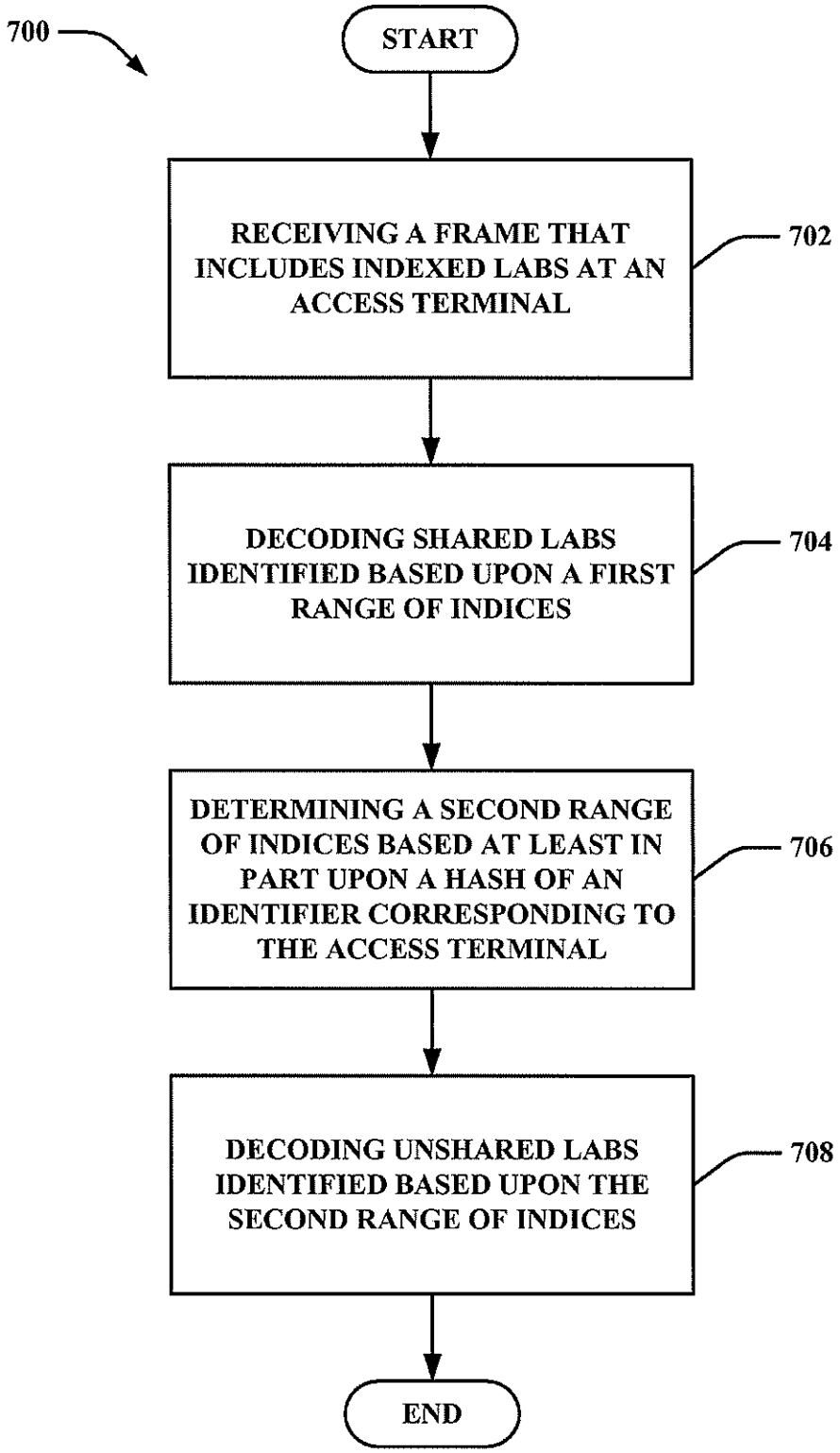

700
START
RECEIVING A FRAME THAT INCLUDES INDEXED LABS AT AN ACCESS TERMINAL
702
DECODING SHARED LABS IDENTIFIED BASED UPON A FIRST RANGE OF INDICES
704
DETERMINING A SECOND RANGE OF INDICES BASED AT LEAST IN PART UPON A HASH OF AN IDENTIFIER CORRESPONDING TO THE ACCESS TERMINAL
706
DECODING UNSHARED LABS IDENTIFIED BASED UPON THE SECOND RANGE OF INDICES
708
END

FIG. 7

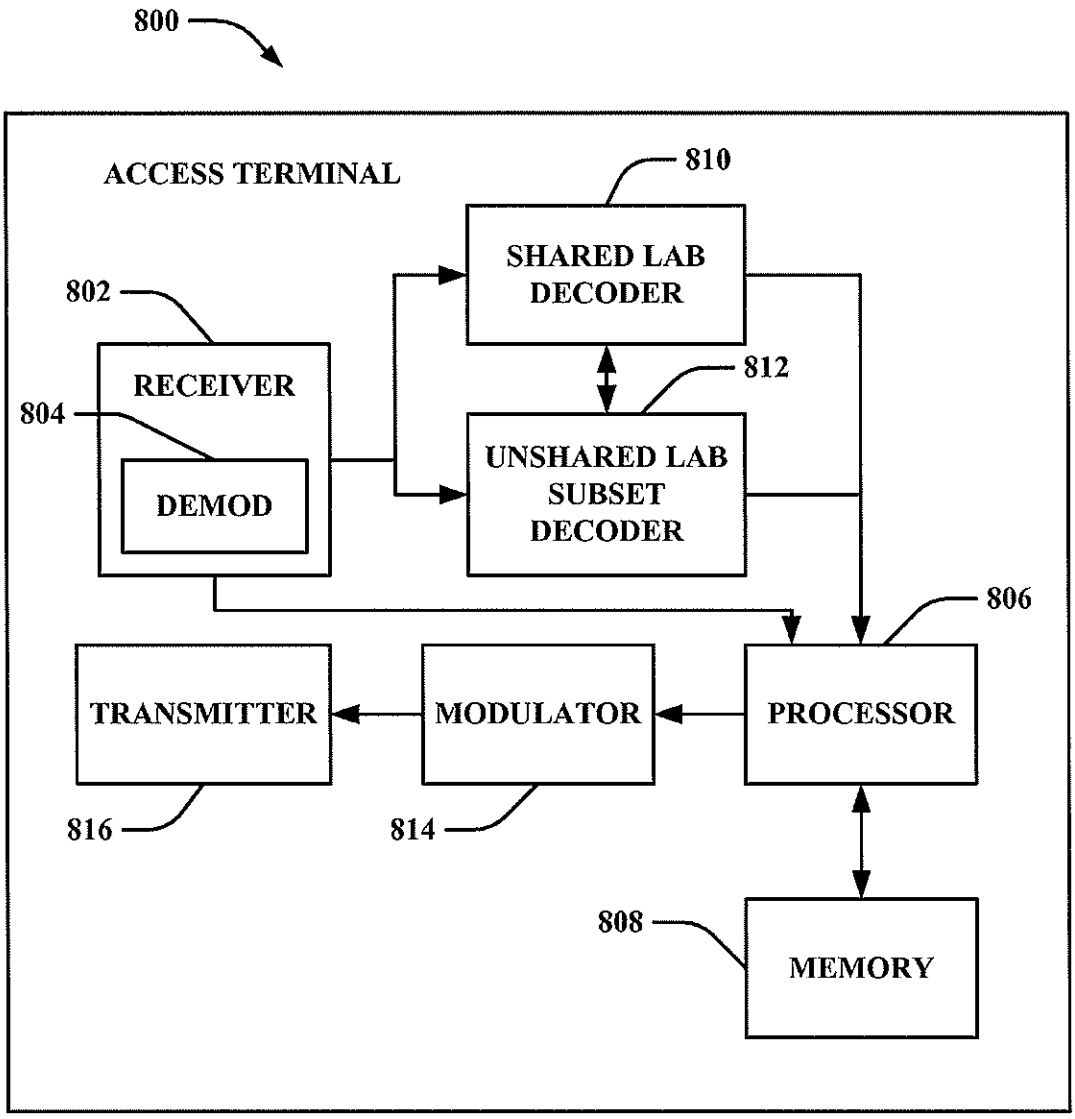

800
ACCESS TERMINAL
810
SHARED LAB DECODER
802
RECEIVER
804
DEMOD
812
UNSHARED LAB SUBSET DECODER
806
TRANSMITTER
MODULATOR
PROCESSOR
816
814
808
MEMORY

FIG. 8

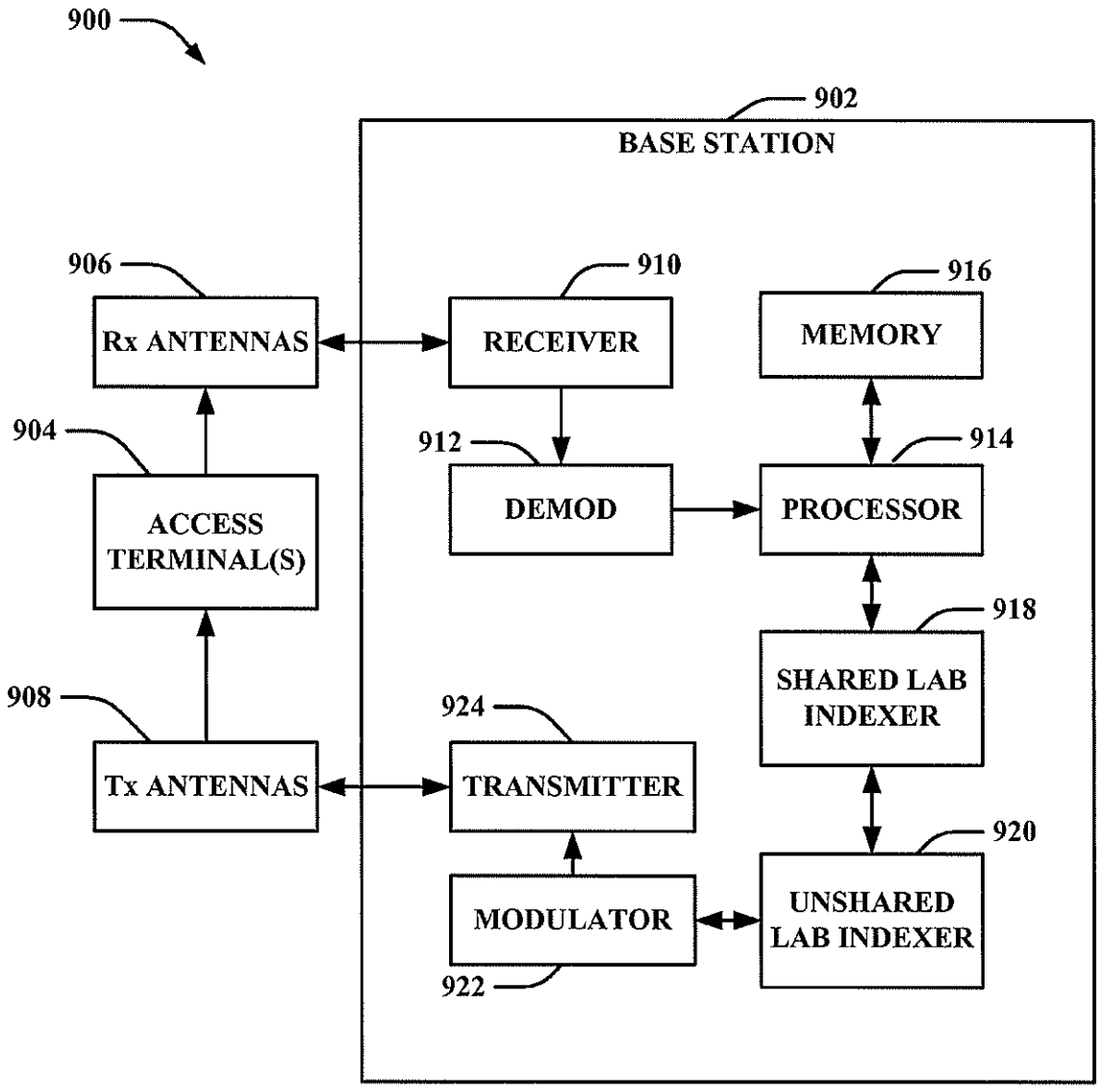

900
902
BASE STATION
906
Rx ANTENNAS
910
RECEIVER
916
MEMORY
904
ACCESS TERMINAL(S)
912
DEMOD
914
PROCESSOR
918
SHARED LAB INDEXER
908
Tx ANTENNAS
924
TRANSMITTER
920
MODULATOR
UNSHARED LAB INDEXER
922

FIG. 9

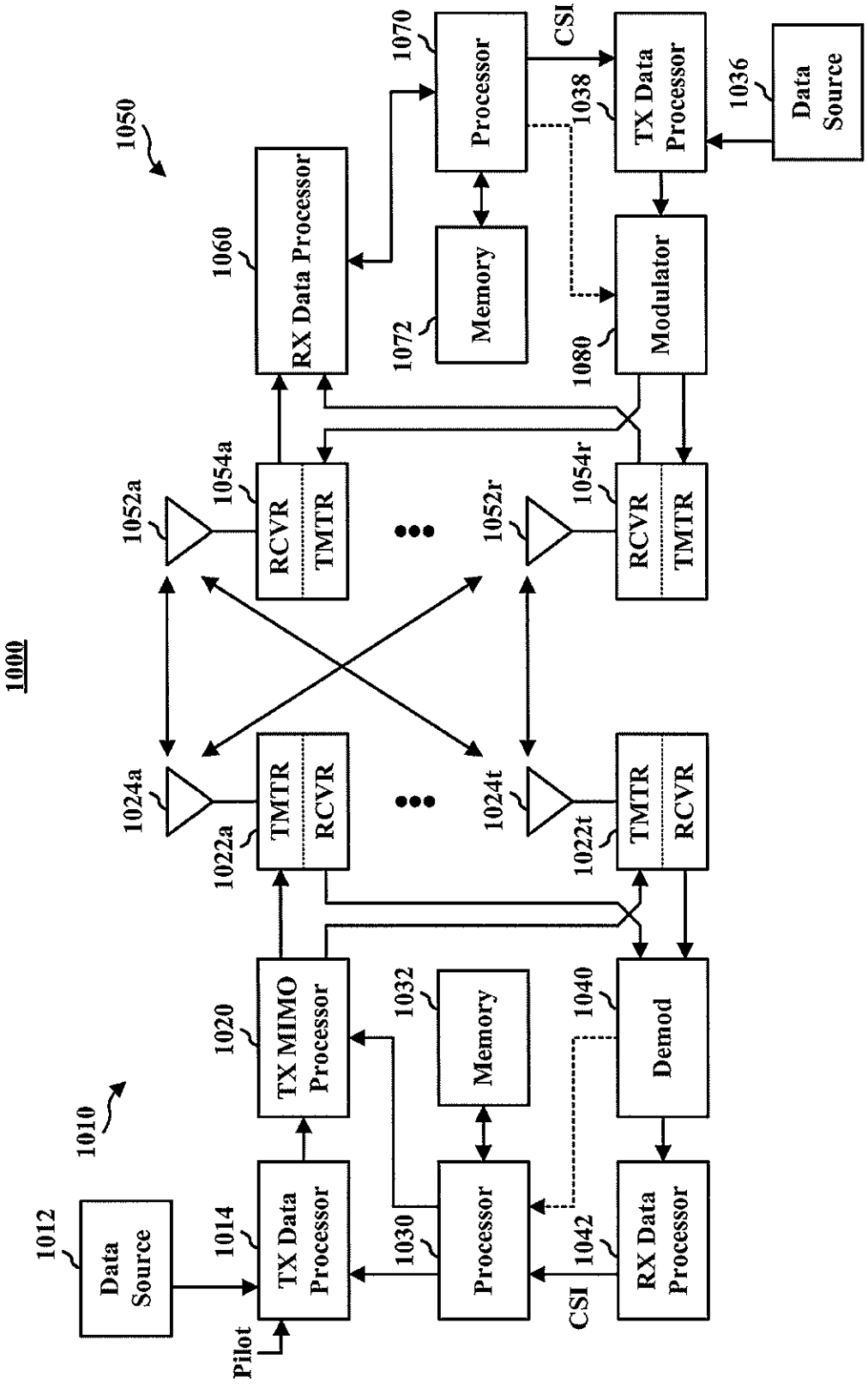

1000
1010
1012
Data Source
1014
TX Data Processor
Pilot
1020
TX MIMO Processor
1022a
TMTR
RCVR
1024a
1030
Processor
1032
Memory
1040
Demod
1042
RX Data Processor
CSI
1050
1052a
1054a
RCVR
TMTR
1052r
1054r
RCVR
TMTR
1060
RX Data Processor
1070
Processor
1072
Memory
1080
Modulator
1038
TX Data Processor
CSI
1036
Data Source
1022t
TMTR
RCVR
1024t

FIG. 10

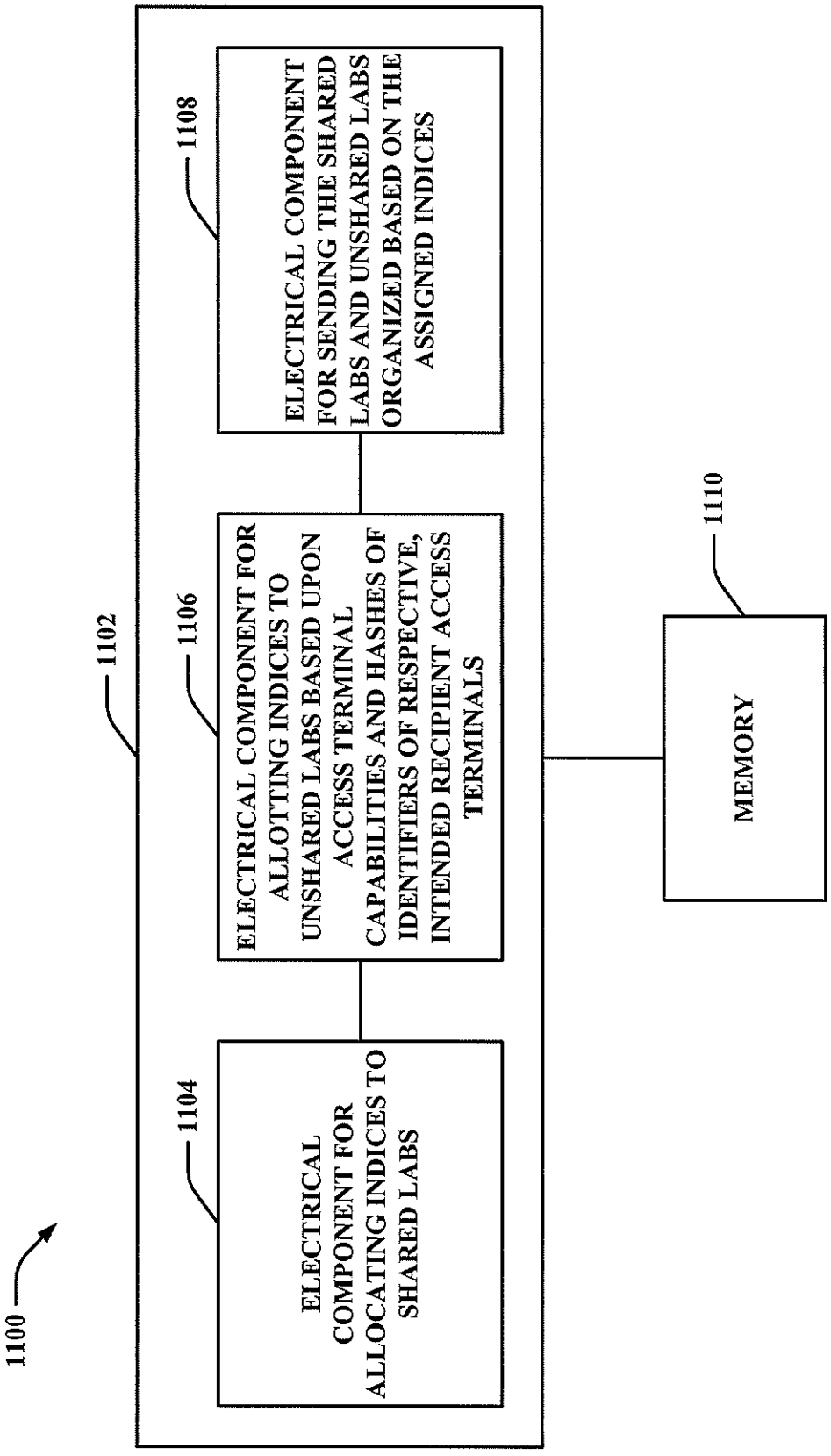

1100
1102
1104
ELECTRICAL COMPONENT FOR ALLOCATING INDICES TO SHARED LABS
1106
ELECTRICAL COMPONENT FOR ALLOTTING INDICES TO UNSHARED LABS BASED UPON ACCESS TERMINAL CAPABILITIES AND HASHES OF IDENTIFIERS OF RESPECTIVE, INTENDED RECIPIENT ACCESS TERMINALS
1108
ELECTRICAL COMPONENT FOR SENDING THE SHARED LABS AND UNSHARED LABS ORGANIZED BASED ON THE ASSIGNED INDICES
1110
MEMORY

FIG. 11

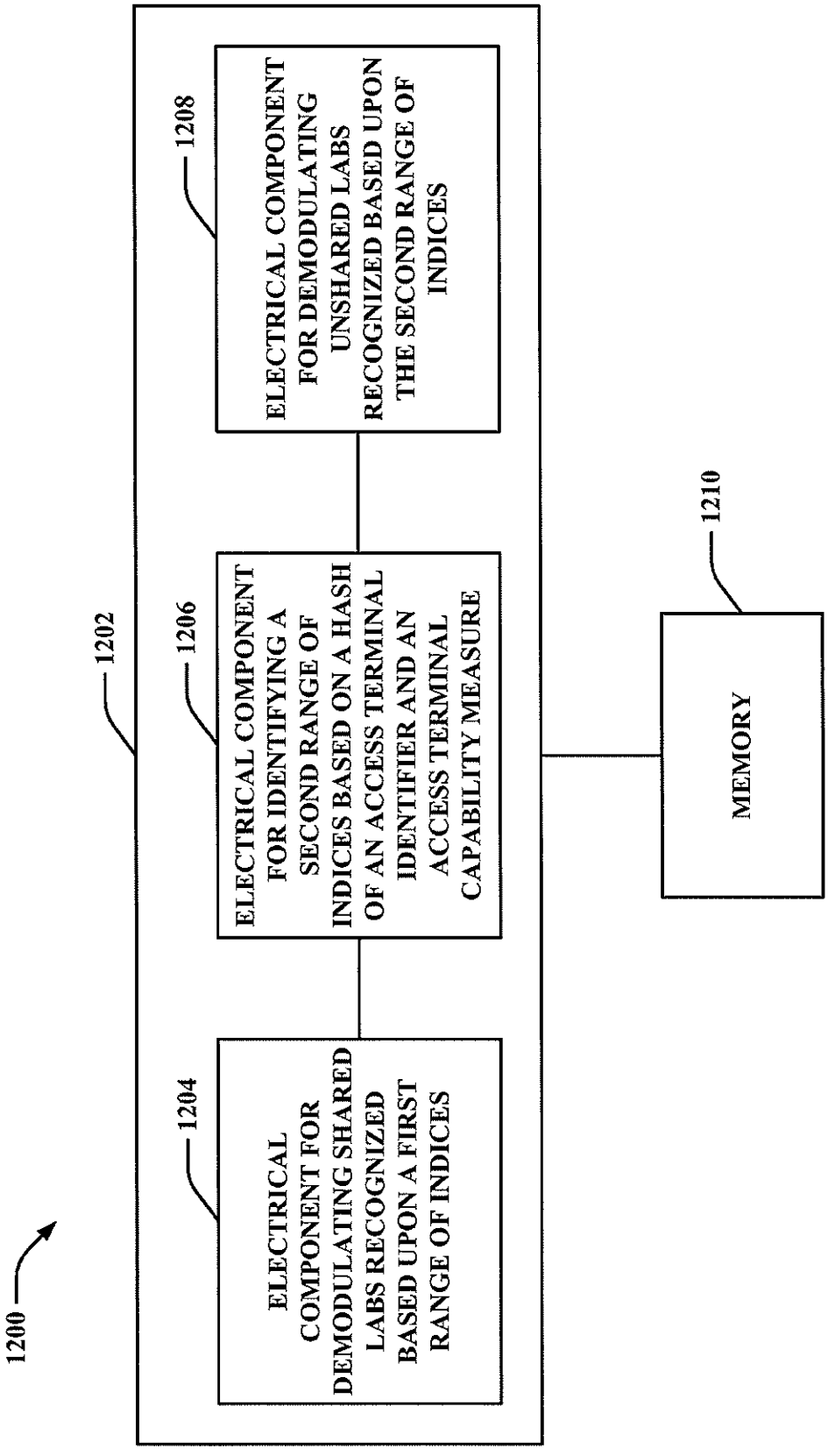

1200
1202
1204
ELECTRICAL COMPONENT FOR DEMODULATING SHARED LABS RECOGNIZED BASED UPON A FIRST RANGE OF INDICES
1206
ELECTRICAL COMPONENT FOR IDENTIFYING A SECOND RANGE OF INDICES BASED ON A HASH OF AN ACCESS TERMINAL IDENTIFIER AND AN ACCESS TERMINAL CAPABILITY MEASURE
1208
ELECTRICAL COMPONENT FOR DEMODULATING UNSHARED LABS RECOGNIZED BASED UPON THE SECOND RANGE OF INDICES
1210
MEMORY

FIG. 12