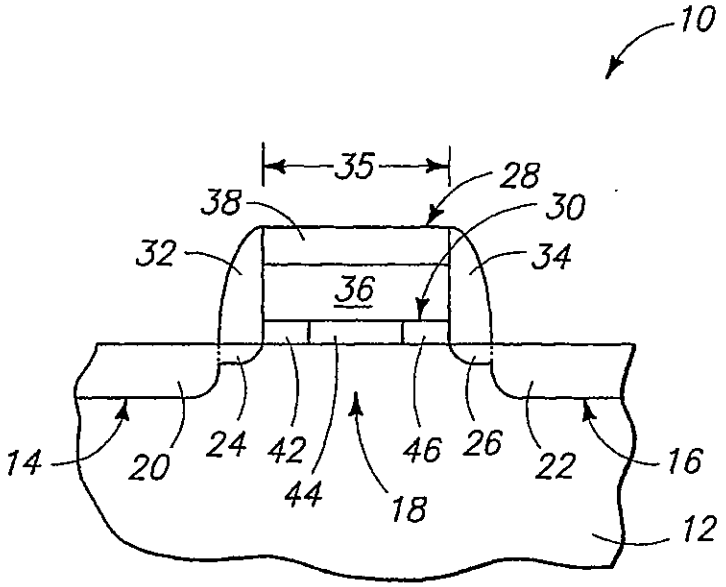

10
35
28
38
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32
34
36
14
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24
42
44
46
18
26
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16
12

Fig. 1

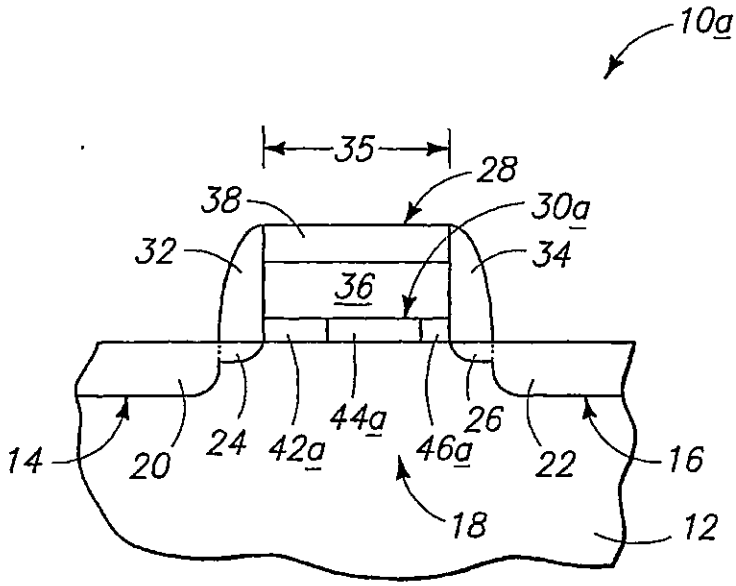

10a
35
28
38
30a
32
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36
14
20
24
42a
44a
46a
18
26
22
16
12

Fig. 2

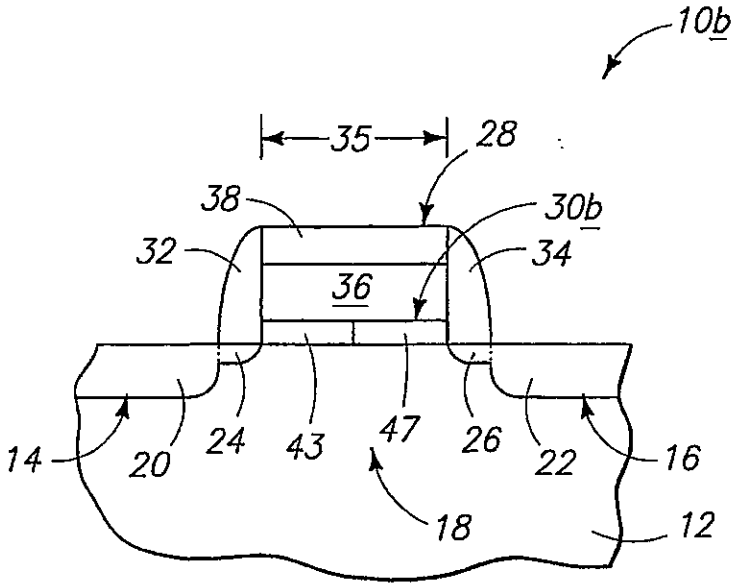

10b
35
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30b
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14
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24
43
47
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22
16
18
12

Fig 3

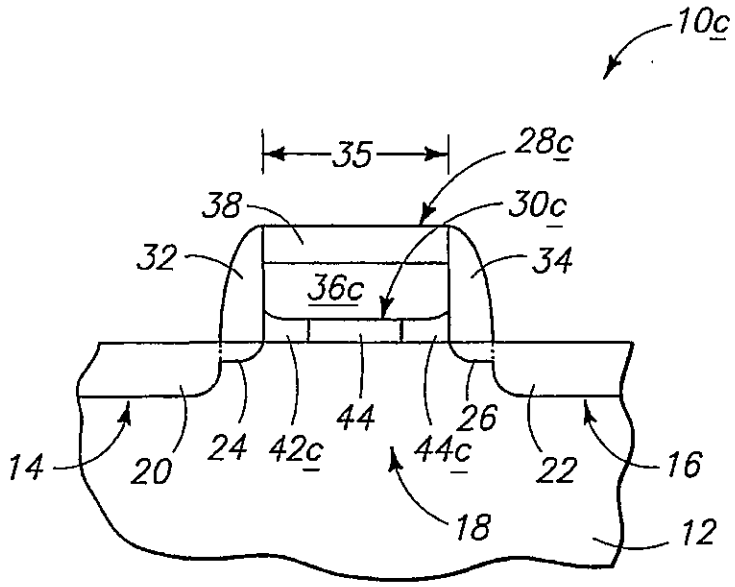

10c
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28c
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30c
32
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36c
14
20
24
42c
44
44c
26
22
16
18
12

Fig 4

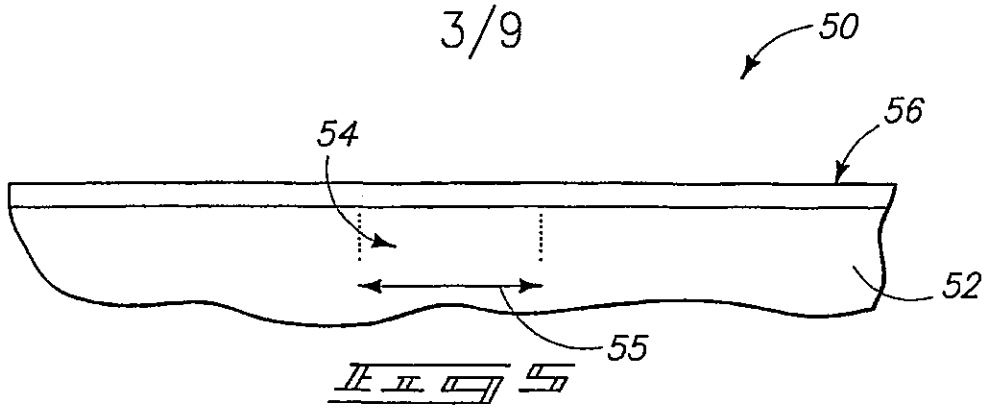


Fig 5

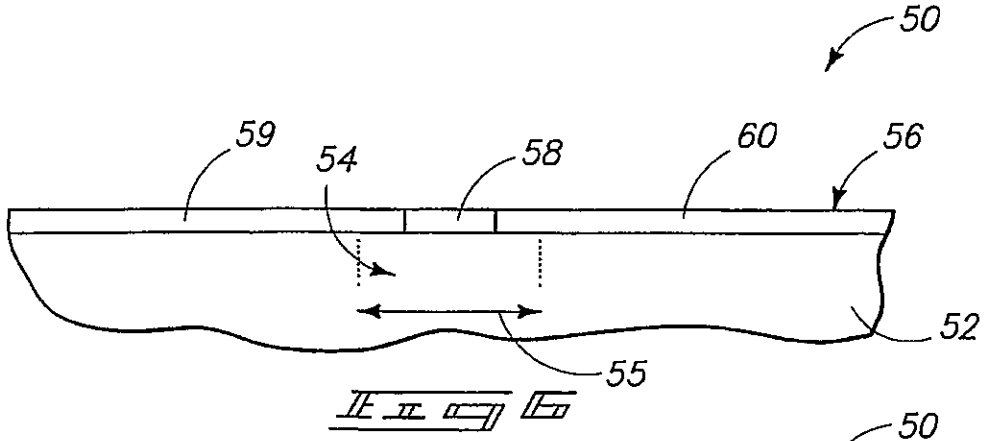


Fig 6

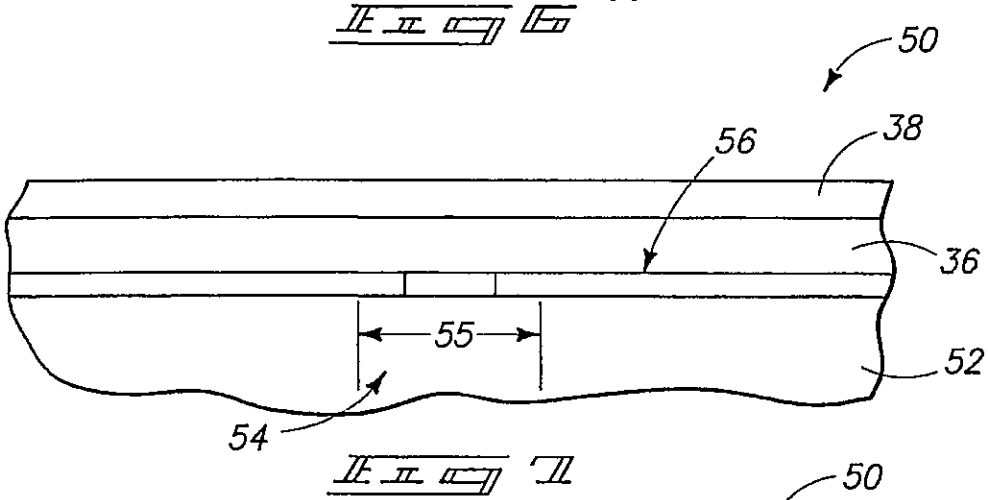


Fig 7

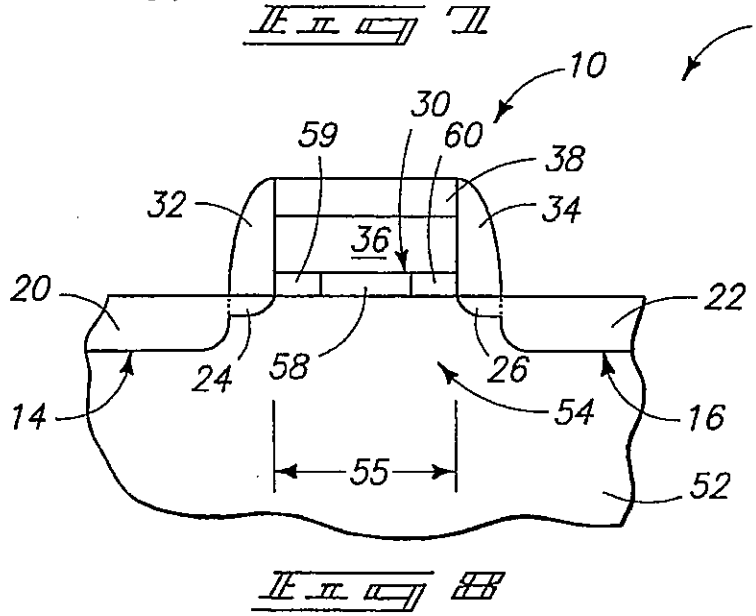


Fig 8

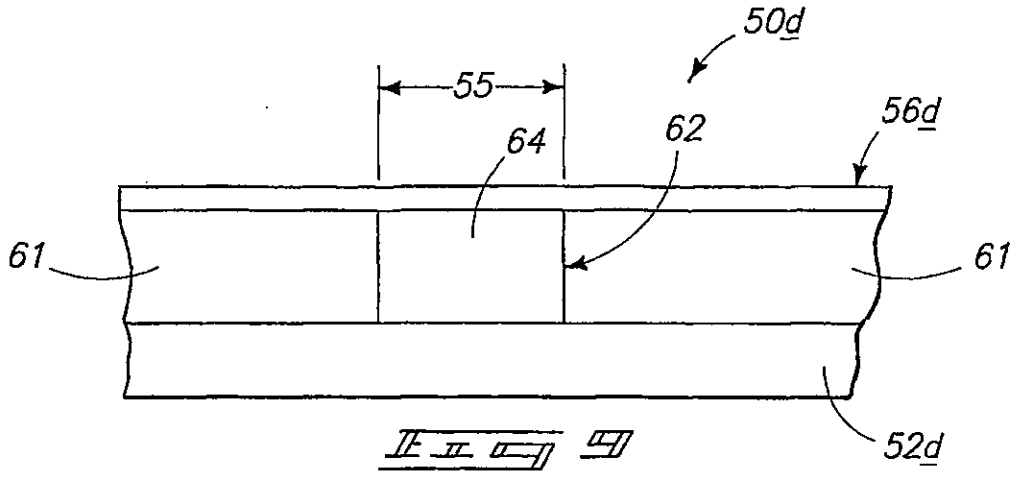


Fig. 4

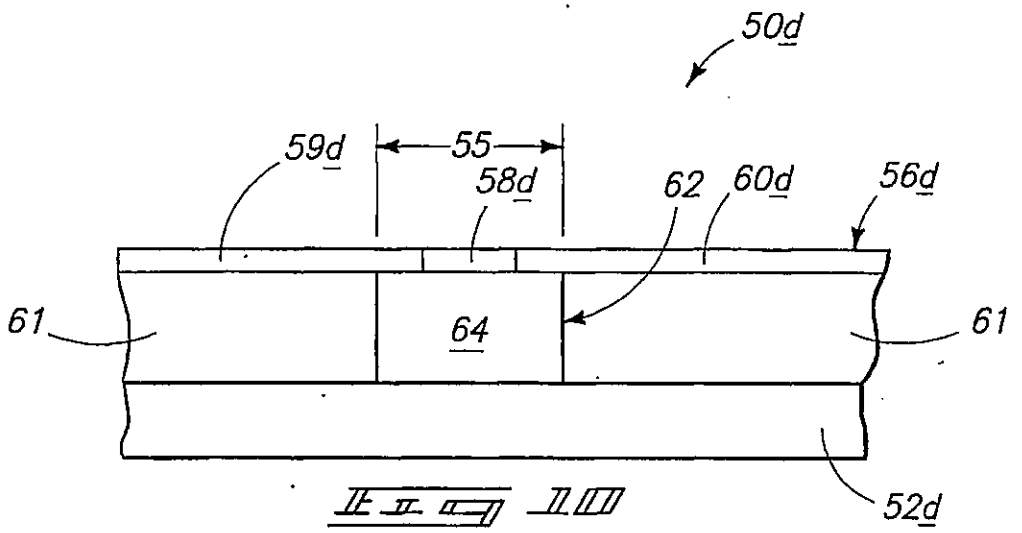


Fig. 3

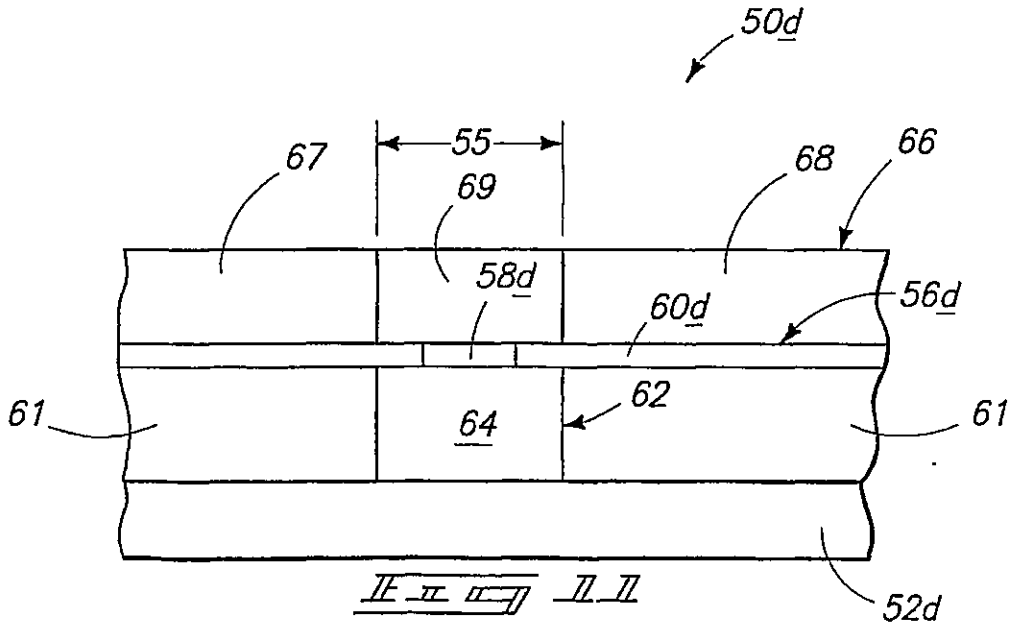


Fig. 2

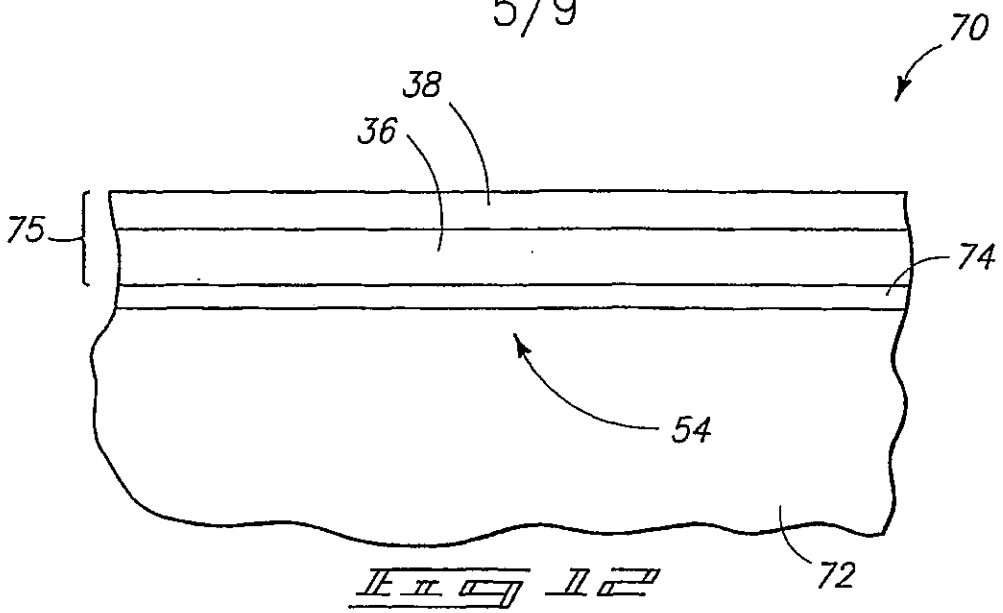


Fig. 2

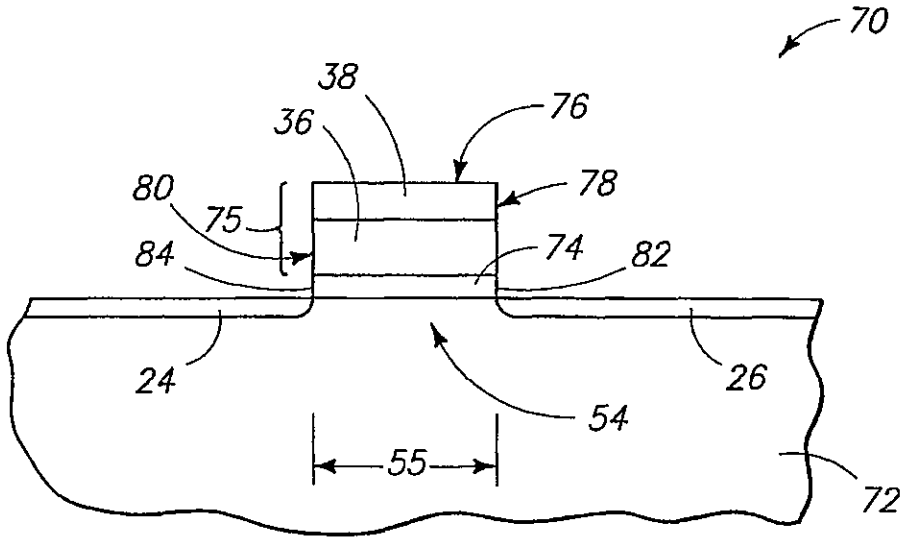


Fig. 3

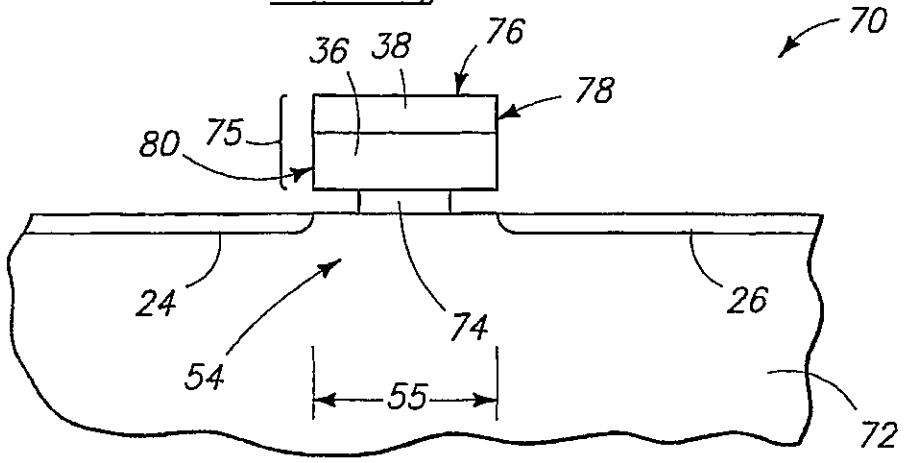


Fig. 4

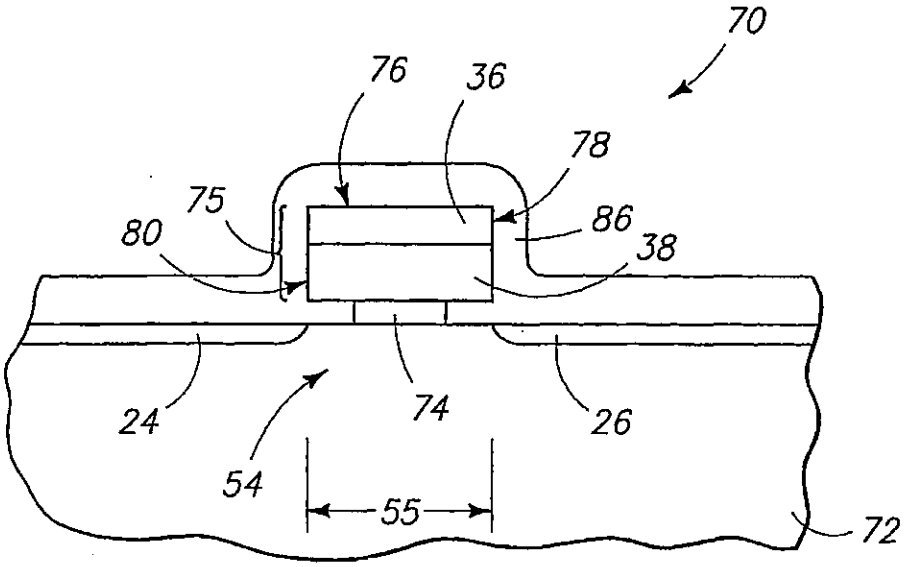

70
76
36
78
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86
38
24
74
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54
55
72

Fig 15

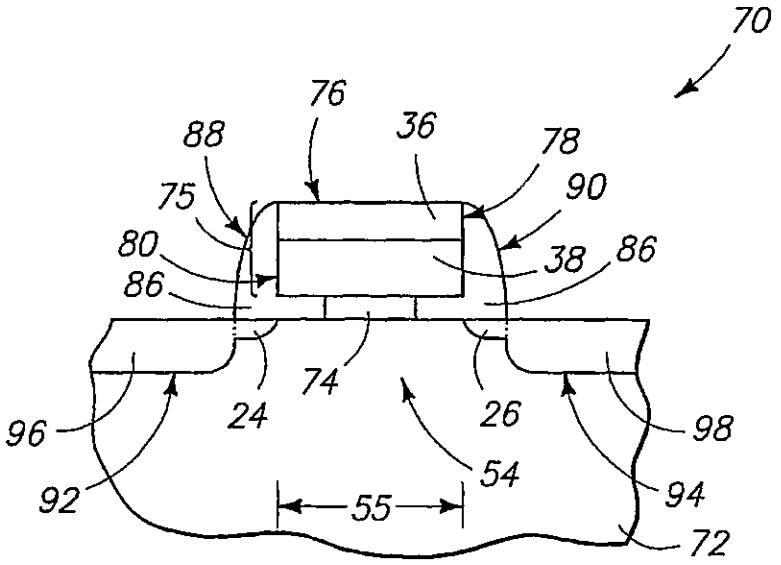

70
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38
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96
98
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92
94
55
72

Fig 16

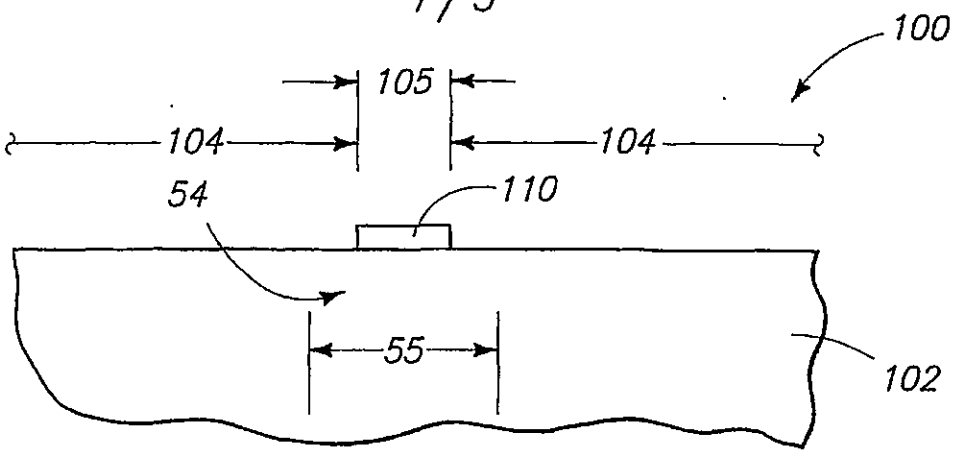


Fig. 17

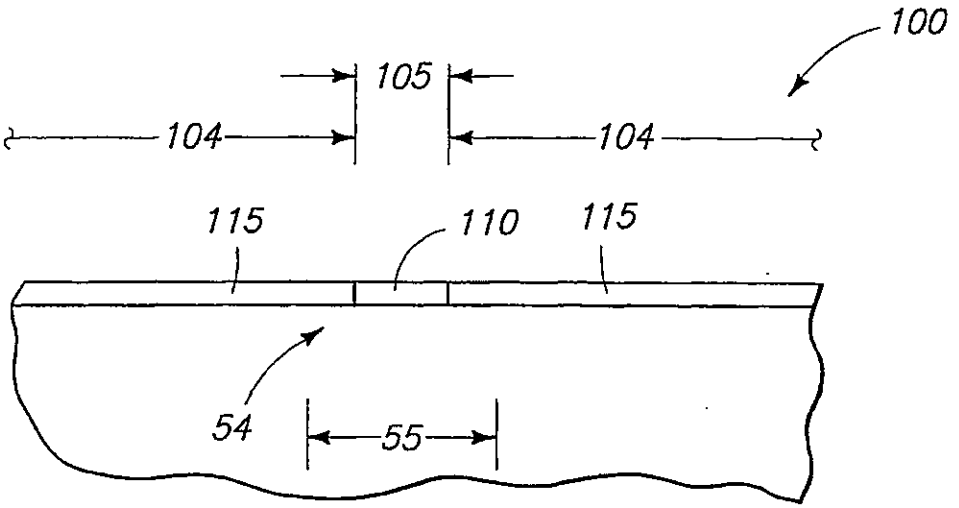


Fig. 18

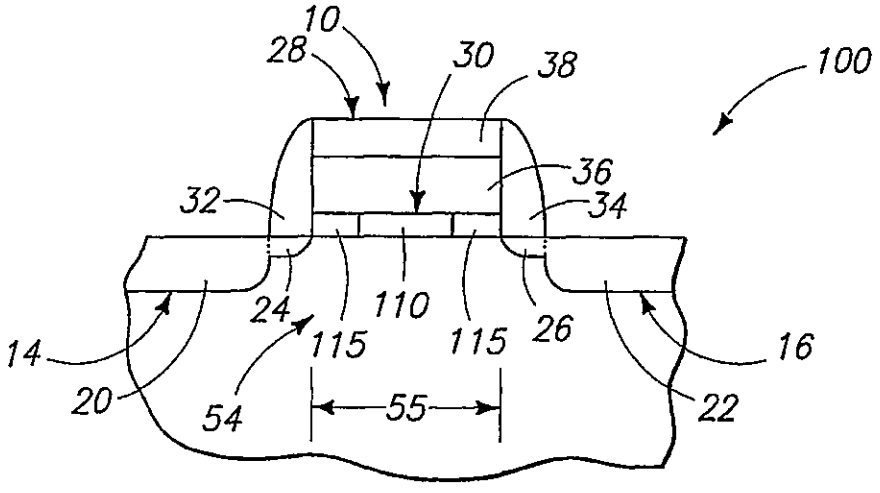


Fig. 19

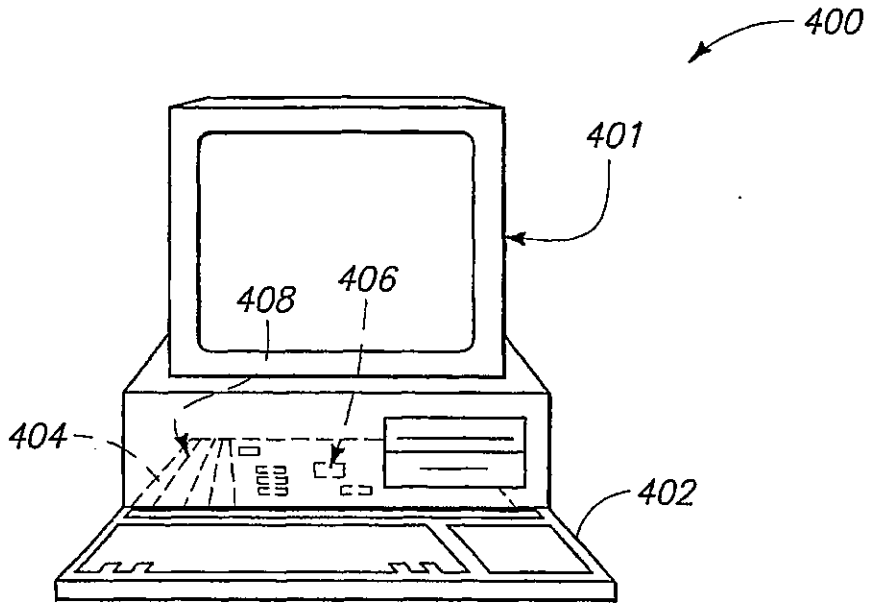


Fig 20

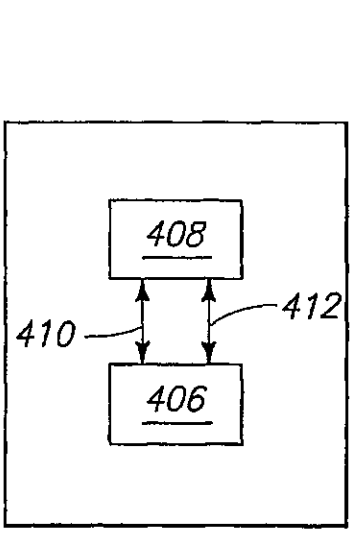


Fig 21

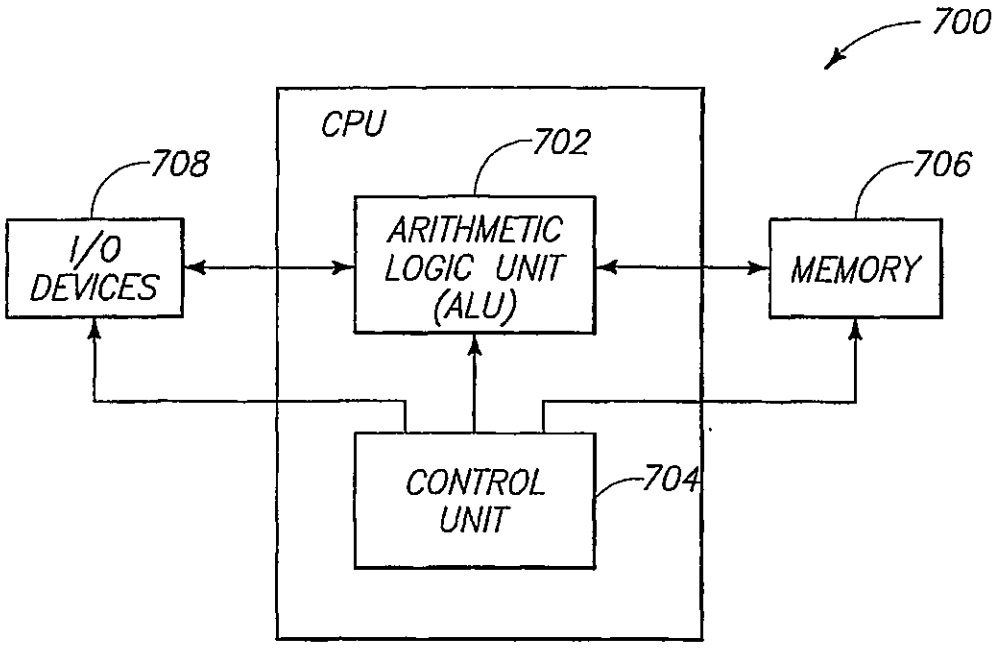

700
CPU
702
708
706
I/O DEVICES
ARITHMETIC LOGIC UNIT (ALU)
MEMORY
CONTROL UNIT
704

Fig 2

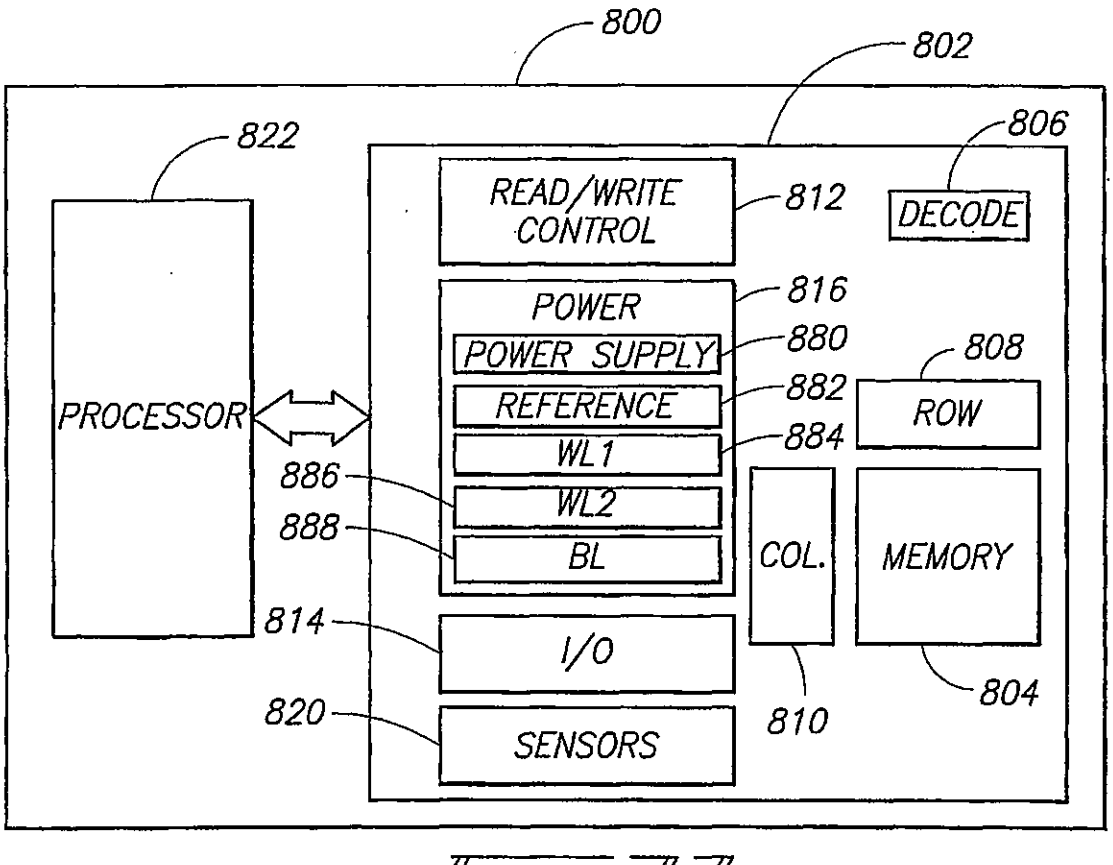

800
802
822
806
READ/WRITE CONTROL
812
DECODE
816
POWER
POWER SUPPLY
880
808
REFERENCE
882
ROW
PROCESSOR
884
WL1
886
WL2
888
BL
COL.
MEMORY
814
I/O
804
820
SENSORS
810

Fig 3