

**A CIRCUIT WHICH INCLUDES CELLS WITH HIGH FANOUT REQUIREMENTS
WHICH HAS A REDUCED RC DELAY**

ABSTRACT

A circuit (100) is provided that reduces the RC delay due to the high fanout requirements of a particular cell in the circuit (100). The reduction is accomplished by providing a copy of a cell which has the high fanout requirements and then utilizing that copy to drive a cell which is part of a faster path in the circuit (100). In so doing, the fanout required for the various functions of the circuit (100) is greatly reduced. The circuit (100) provides for significant advantages as the size of the circuit (100), such as an adder or the like increases.

Fig. 3