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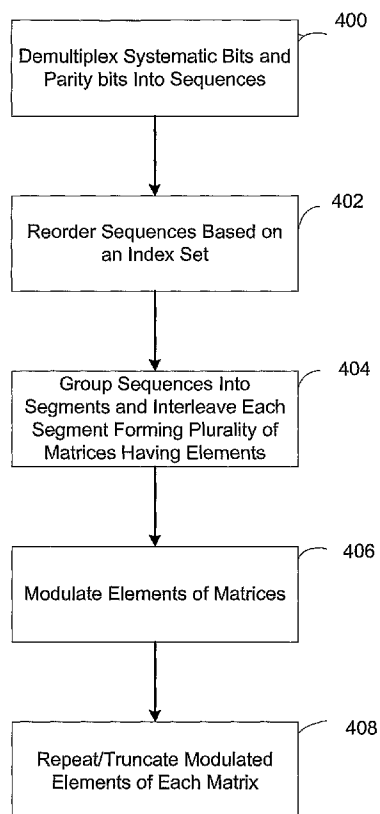
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Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))

[Continued on next page]

(54) Title: APPARATUS AND METHOD FOR CHANNEL INTERLEAVING IN COMMUNICATIONS SYSTEM



(57) Abstract: An apparatus and method for interleaving systematic bits and parity bits to generate an output sequence that can be transmitted in multi-slot packets from a base station to a remote station in a wireless communication system. The apparatus comprises a memory element and a control element coupled to the memory element, wherein the control element is configured to demultiplex the systematic bits and parity bits into sequences (400), wherein the systematic bits and parity bits are sequentially distributed among the sequences. The control element is further configured to reorder the sequences based on an index set (402), to group the sequences into segments and to interleave each of the segments forming matrices having elements. The control element is also configured to modulate the elements of the matrices (406), and to truncate the modulated elements of each matrix (408), so as to produce the output sequence which comprises truncated modulating elements from each matrix of the matrices.



— *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))*

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